

#### **General Description**

**Features** 

The WSP05N15 is the highest performance trench N-Ch MOSFET with extreme high cell density,which provide excellent RDSON and gate charge for most of the synchronous buck converter applications.

The WSF05N10 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

• Advanced high cell density Trench technology

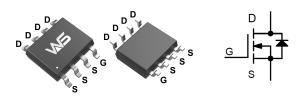
# **Product Summery**

BVDSS	RDSON	ID
150V	37mΩ	6A

#### Applications

- Power Management for Boost Converters.
- Synchronous Rectifiers for SMPS.
- LED Backlighting.

### **SOP-8 Pin Configuration**



#### Symbol Parameter Rating Units Drain-Source Voltage 150 $V_{DS}$ V Gate-Source Voltage v $\pm 25$ $V_{GS}$ Continuous Drain Current, V<sub>GS</sub> @ 10V<sup>1</sup> 6.0 А I<sub>D</sub>@T<sub>c</sub>=25℃ Continuous Drain Current, V<sub>GS</sub> @ 10V<sup>1</sup> 4.8 А I<sub>D</sub>@T<sub>c</sub>=70℃ Pulsed Drain Current<sup>2</sup> 24 А $I_{DM}$ Single Pulse Avalanche Energy<sup>3</sup> EAS 36 mJ Avalanche Current 12 А $I_{AS}$ W P<sub>D</sub>@T<sub>A</sub>=25℃ Total Power Dissipation<sup>4</sup> 3.5 Storage Temperature Range T<sub>STG</sub> -55 to 150 °C °C ΤJ **Operating Junction Temperature Range** -55 to 150

# Thermal Data

Symbol	Parameter	Тур.	Max.	Unit	
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>		70	°C/W	
R <sub>eJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>		24	°C/W	

# Absolute Maximum Ratings

Super Low Gate Charge

Green Device Available

Excellent Cdv/dt effect decline



**N-Ch MOSFET** 

## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	150			V
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\!\mathrm{C}$ , $I_D\text{=}1\text{mA}$		0.098		V/℃
	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =6A		37	45	-mΩ
R <sub>DS(ON)</sub>		V <sub>GS</sub> =6V , I <sub>D</sub> =2A		48	78	
V <sub>GS(th)</sub>	Gate Threshold Voltage		2.0	3.0	4.0	V
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_{D}=250$ uA		-5.52		mV/℃
	Drain-Source Leakage Current	$V_{DS}$ =80V , $V_{GS}$ =0V , $T_J$ =25 $^{\circ}$ C			10	
I <sub>DSS</sub>		V <sub>DS</sub> =80V , V <sub>GS</sub> =0V , T <sub>J</sub> =55℃			100	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm20V$ , $V_{DS}$ = $0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =3A		6.2		S
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		2.5	3.2	Ω
Qg	Total Gate Charge (10V)	V <sub>DS</sub> =50V , V <sub>GS</sub> =10V , I <sub>D</sub> =3A		23	33	nC
Q <sub>gs</sub>	Gate-Source Charge			6		
Q <sub>gd</sub>	Gate-Drain Charge			9.9		
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =30V , V <sub>GS</sub> =10V , R <sub>G</sub> =6Ω I <sub>D</sub> =1A ,RL=30Ω.		5.5	21.6	
Tr	Rise Time			27	48.6	- ns
T <sub>d(off)</sub>	Turn-Off Delay Time			56	112	
T <sub>f</sub>	Fall Time			24	48	
Ciss	Input Capacitance	V <sub>DS</sub> =30V , V <sub>GS</sub> =0V , f=1MHz		1160	1500	
C <sub>oss</sub>	Output Capacitance			90		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			45		1

### **Guaranteed Avalanche Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy <sup>5</sup>	V <sub>DD</sub> =25V , L=0.5mH , I <sub>AS</sub> =12A	30			mJ

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	$V_G = V_D = 0V$ , Force Current			4.0	А
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>				24	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =4A , T <sub>J</sub> =25℃			1.3	V
t <sub>rr</sub>	Reverse Recovery Time	IF=6A,dI/dt=100A/µs,Tյ=25℃		31		nS
Q <sub>rr</sub>	Reverse Recovery Charge			50		nC

Note :

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,t<10sec.

2.The data tested by pulsed , pulse width  $\,\leq\,$  300us , duty cycle  $\,\leq\,$  2%

3. The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}\text{=}25\text{V}, V_{\text{GS}}\text{=}10\text{V}, \text{L=}0.5\text{mH}, \text{I}_{\text{AS}}\text{=}12\text{A}$ 

4.The power dissipation is limited by 150  $^\circ\!\mathrm{C}$   $\,$  junction temperature

5.The Min. value is 100% EAS tested guarantee.

6. The data is theoretically the same as  $I_{\text{D}}$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.



**WSP05N15** 

#### **N-Ch MOSFET**

## **Typical Characteristics**

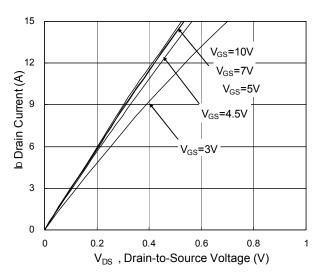


Fig.1 Typical Output Characteristics

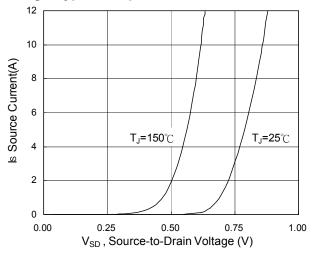


Fig.3 Forward Characteristics Of Reverse

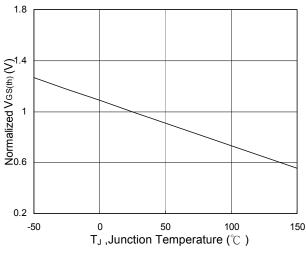


Fig.5 Normalized  $V_{GS(th)}$  vs. T<sub>J</sub>

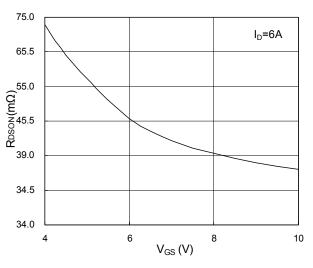


Fig.2 On-Resistance vs. Gate-Source

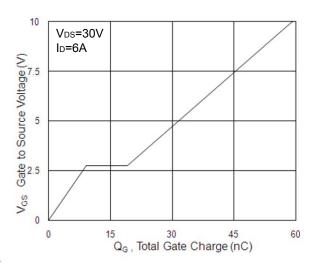


Fig.4 Gate-Charge Characteristics

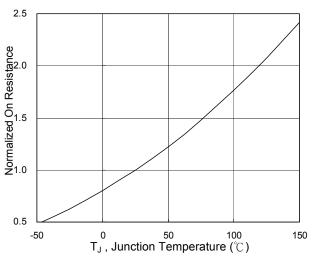


Fig.6 Normalized  $R_{\text{DSON}}$  vs.  $T_{\text{J}}$ 

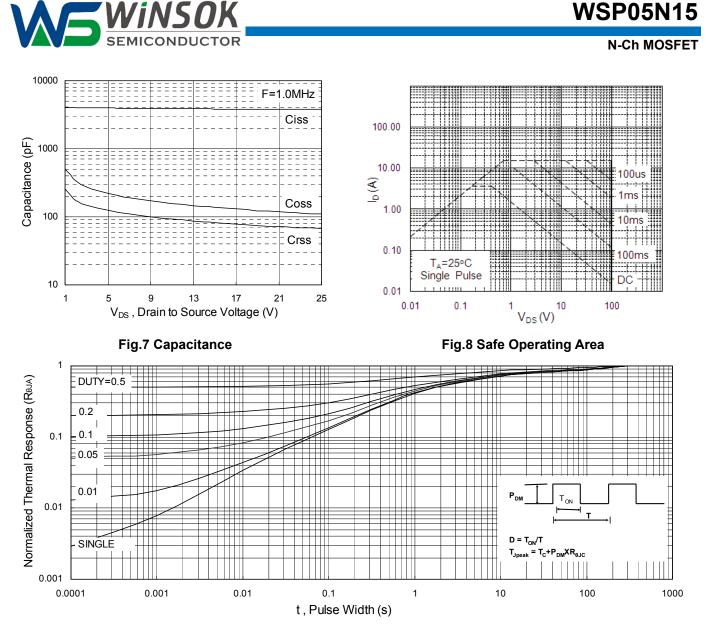
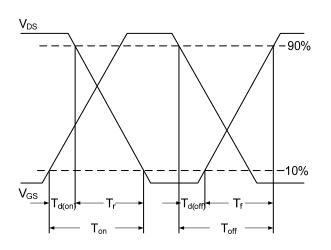


Fig.9 Normalized Maximum Transient Thermal Impedance





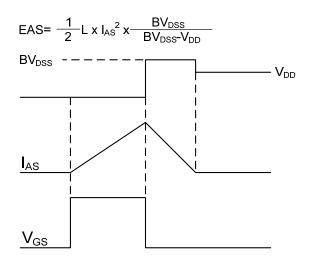


Fig.11 Unclamped Inductive Switching Waveform



# Attention

1, Any and all Winsok power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your Winsok power representative nearest you before using any Winsok power products described or contained herein in such applications.

2, Winsok power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all Winsok power products described or contained herein.

3, Specifications of any and all Winsok power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

4, Winsok power Semiconductor CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

5, In the event that any or all Winsok power products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

6, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of Winsok power Semiconductor CO., LTD.

7, Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Winsok power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

8, Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the Winsok power product that you Intend to use.

9, this catalog provides information as of Sep.2014. Specifications and information herein are subject to change without notice.