General Description

The WSD4080DN56 is the highest performance trench N-ch MOSFET with extreme high cell density , which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSD4080DN56 meet the RoHS and Green Product requirement 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

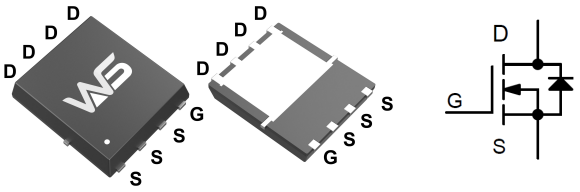
Product Summery

BVDSS	RDSON	ID
40V	4.5mΩ	85A

Applications

- Battery protection
- Load switch
- Uninterruptible power supply

DFN5X6-8 Pin Configuration



Absolute Maximum Ratings (T_C=25℃unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-Source Voltage	±20	V
I_D@T_C=25°C	Continuous Drain Current, V_{GS} @ 10V[1]	85	A
I_D@T_C=100°C	Continuous Drain Current, V_{GS} @ 10V[1]	58	A
I_{DM}	Pulsed Drain Current[2]	100	A
EAS	Single Pulse Avalanche Energy[3]	110.5	mJ
I_{AS}	Avalanche Current	47	A
P_D@T_C=25°C	Total Power Dissipation[4]	52.1	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1]	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case[1]	2.4	°C/W

Electrical Characteristics (T_J=25℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	40	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance2	V_{GS}=10V , I_D=10A	---	4.5	6.5	mΩ
		V_{GS}=4.5V , I_D=5A	---	6.4	8.5	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	1.0	---	2.5	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=32V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=32V , V_{GS}=0V , T_J=55℃	---	---	5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±20V , V_{DS}=0V	---	---	±100	nA
gfs	Forward Transconductance	V_{DS}=10V , I_D=5A	---	27	---	S
Q_g	Total Gate Charge (4.5V)	V_{DS}=20V , V_{GS}=4.5V , I_D=10A	---	20	---	nC
Q_{gs}	Gate-Source Charge		---	5.8	---	
Q_{gd}	Gate-Drain Charge		---	9.5	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=15V , V_{GS}=10V R_G=3.3Ω I_D=1A	---	15.2	---	ns
T_r	Rise Time		---	8.8	---	
$T_{d(off)}$	Turn-Off Delay Time		---	74	---	
T_f	Fall Time		---	7	---	
C_{iss}	Input Capacitance	V_{DS}=15V , V_{GS}=0V , f=1MHz	---	2354	---	pF
C_{oss}	Output Capacitance		---	215	---	
C_{rss}	Reverse Transfer Capacitance		---	175	---	
I_S	Continuous Source Current1,5	V_G=V_D=0V , Force Current	---	---	70	A
V_{SD}	Diode Forward Voltage2	V_{GS}=0V , I_S=1A , T_J=25℃	---	---	1	V

Note :

1.The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width ≦ 300us , duty cycle ≦ 2%

3.The EAS data shows Max. rating . The test condition is VDD=25V,VGS=10V,L=0.1mH,IAS=47A

4.The power dissipation is limited by 150℃ junction temperature

5.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Typical Characteristics

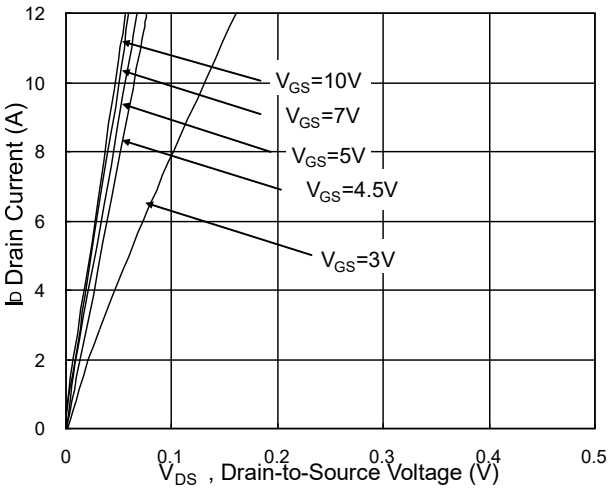


Fig.1 Typical Output Characteristics

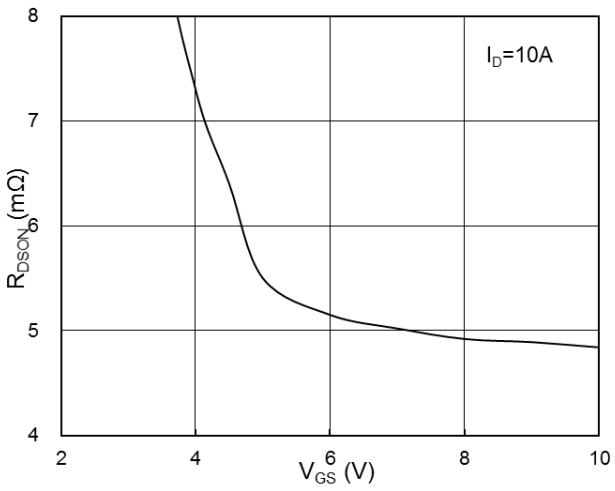


Fig.2 On-Resistance vs. G-S Voltage

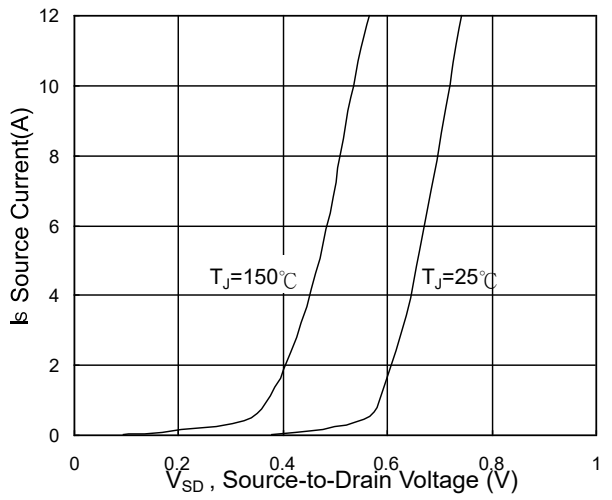


Fig.3 Forward Characteristics of Reverse

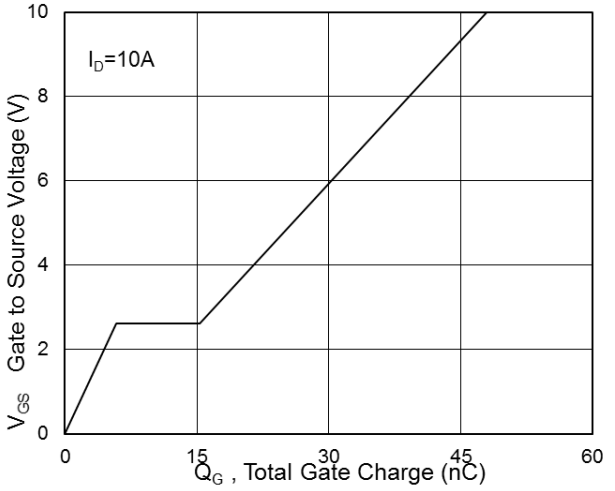


Fig.4 Gate-Charge Characteristics

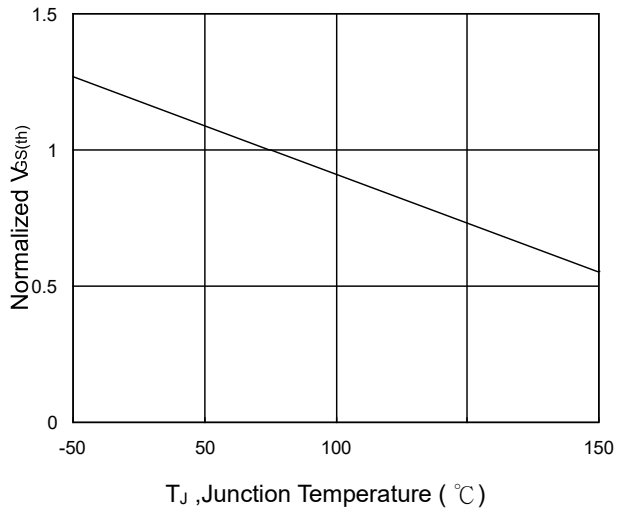


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

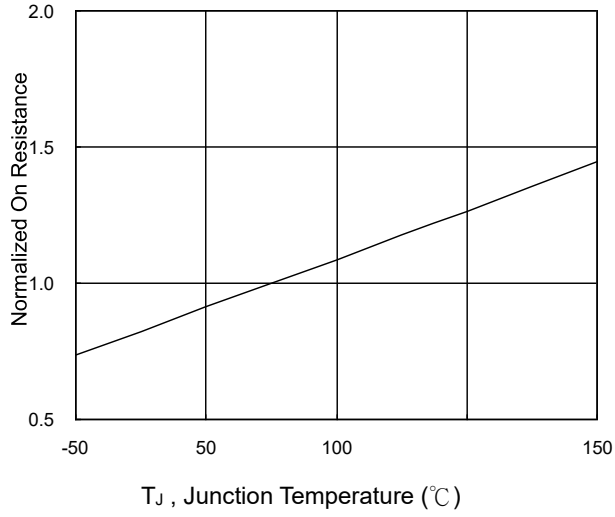


Fig.6 Normalized R_{DSON} vs. T_J

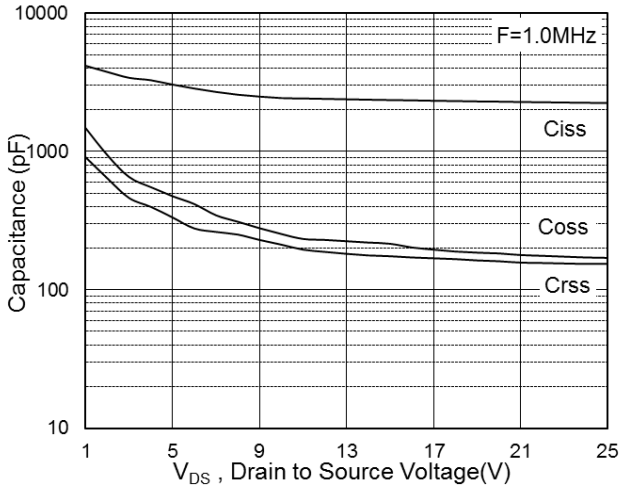


Fig.7 Capacitance

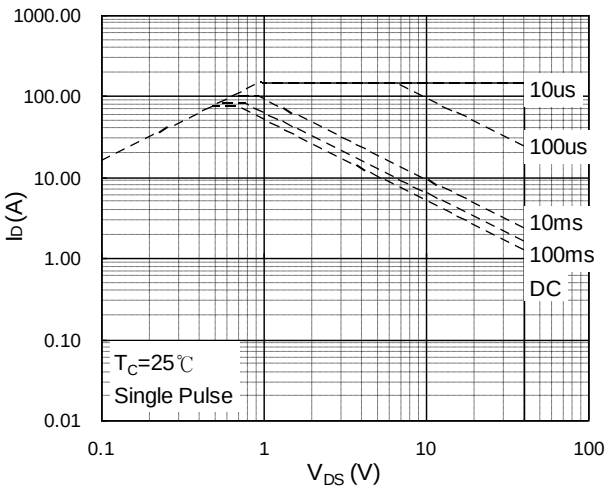


Fig.8 Safe Operating Area

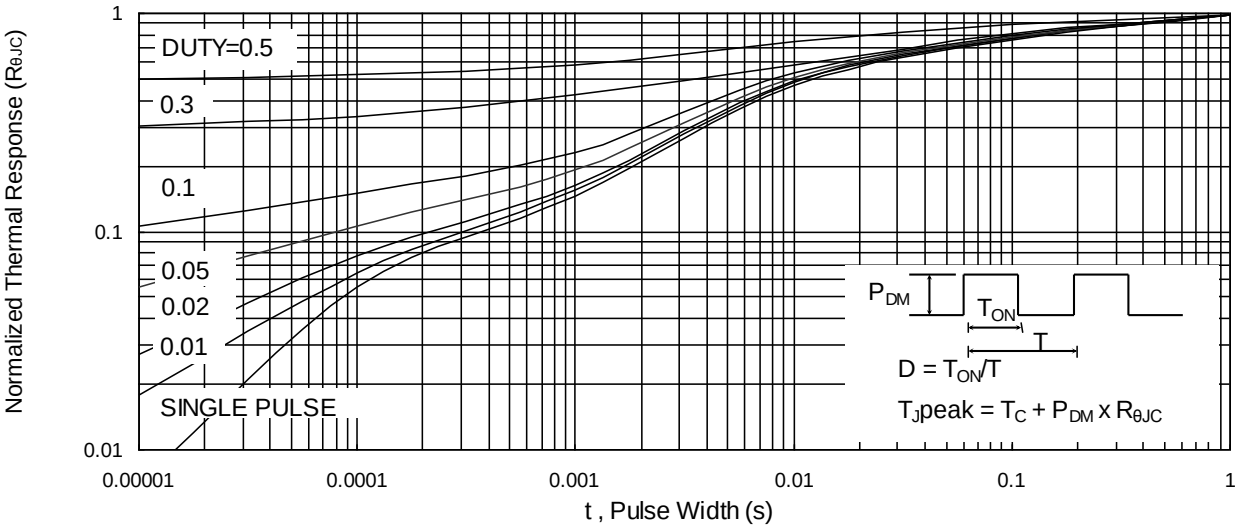


Fig.9 Normalized Maximum Transient Thermal Impedance

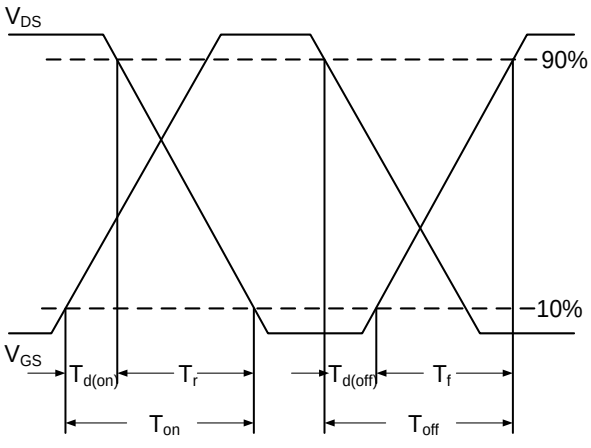


Fig.10 Switching Time Waveform

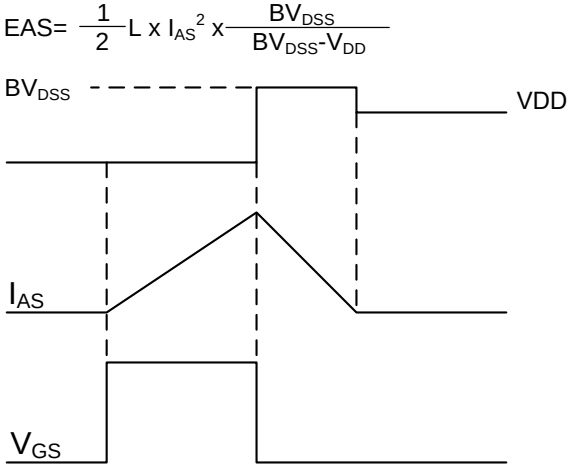


Fig.11 Unclamped Inductive Switching Wave

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