

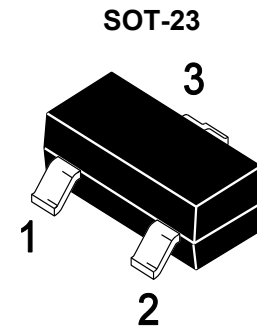


Features

- Low power consumption
- Low voltage drop
- Low temperature coefficient
- High input voltage (up to 24V)

Applications

- Battery-powered equipment
- Communication equipment
- Audio/Video equipment



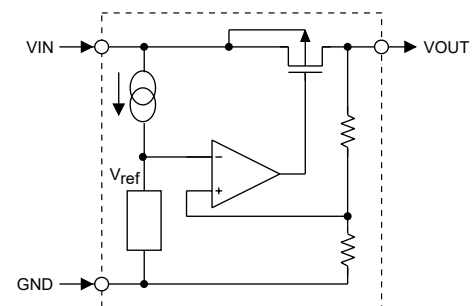
1.GND 2.VOUT 3.VIN

General Description

The PJ71KXXSA series is a set of three-terminal low power high voltage regulators implemented in CMOS technology. They allow input voltages as high as 24V. They are available with several fixed output voltages ranging from 3.0V to 5.0V. CMOS technology ensures low voltage drop and low quiescent current.

Although designed primarily as fixed voltage regulators, these devices can be used with external components to obtain variable voltages and currents.

Block Diagram



Selection Table

Part No.	Output Voltage	Tolerance
PJ71K30SA	3.0V	±5%
PJ71K33SA	3.3V	±5%
PJ71K36SA	3.6V	±5%
PJ71K44SA	4.4V	±5%
PJ71K50SA	5.0V	±5%



Absolute Maximum Ratings (Ta=25°C unless otherwise specified) ^{Note1}

Symbol	Parameter	Rating	Units
V _{IN}	Supply Voltage	-0.3V to 28V	V
P _D	Power Dissipation	200	mW
T _{OPR}	Operating Ambient	-40 ~ +85	°C
T _{STG}	Storage Temperature	-65 ~ +125	°C

Note1: These are stress ratings only. Stresses exceeding the range specified under Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

Electrical Characteristics

PJ71K30SA (Ta=25°C)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
		V _{IN}	Conditions				
V _{OUT}	Output Voltage	5V	I _{OUT} =10mA	2.85	3.0	3.15	V
I _{OUT}	Output Current	5V		20	30		mA
ΔV _{OUT}	Load Regulation	5V	1mA ≤ I _{OUT} ≤ 20mA		60	100	mV
V _D ^{Note2}	Voltage Drop		I _{OUT} =1mA		100		mV
I _{SS}	Current Consumption	5V	No load		4	6.0	uA
$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	Line Regulation		V _{OUT} +2V ≤ V _{IN} ≤ 24V I _{OUT} =1mA		0.2		%/V
V _{IN}	Input Voltage					24	V
$\frac{\Delta V_{OUT}}{\Delta T_a}$	Temperature Coefficient	5V	I _{OUT} =10mA 0°C < T _a < 70°C		±0.45		mV/°C

PJ71K33SA (Ta=25°C)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
		V _{IN}	Conditions				
V _{OUT}	Output Voltage	5.3V	I _{OUT} =10mA	3.135	3.3	3.465	V
I _{OUT}	Output Current	5.3V		20	30		mA
ΔV _{OUT}	Load Regulation	5.3V	1mA ≤ I _{OUT} ≤ 30mA		60	100	mV
V _D ^{Note2}	Voltage Drop		I _{OUT} =1mA		100		mV
I _{SS}	Current Consumption	5.3V	No load		4	6	uA
$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	Line Regulation		V _{OUT} +2V ≤ V _{IN} ≤ 24V I _{OUT} =1mA		0.2		%/V
V _{IN}	Input Voltage					24	V
$\frac{\Delta V_{OUT}}{\Delta T_a}$	Temperature Coefficient	5.3V	I _{OUT} =10mA 0°C < T _a < 70°C		±0.5		mV/°C



PJ71K36SA (Ta=25°C)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
		V _{IN}	Conditions				
V _{OUT}	Output Voltage	5.6V	I _{OUT} =10mA	3.42	3.6	3.78	V
I _{OUT}	Output Current	5.6V		20	30		mA
ΔV _{OUT}	Load Regulation	5.6V	1mA ≤ I _{OUT} ≤ 30mA		60	100	mV
V _D <small>Note2</small>	Voltage Drop		I _{OUT} =1mA		60		mV
I _{SS}	Current Consumption	5.6V	No load		3.0	7.0	uA
$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	Line Regulation		V _{OUT} +2V ≤ V _{IN} ≤ 24V I _{OUT} =1mA		0.2		%/V
V _{IN}	Input Voltage					24	V
$\frac{\Delta V_{OUT}}{\Delta T_a}$	Temperature Coefficient	5.6V	I _{OUT} =10mA 0°C < T _a < 70°C		±0.6		mV/°C

PJ71K44SA (Ta=25°C)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
		V _{IN}	Conditions				
V _{OUT}	Output Voltage	6.4V	I _{OUT} =10mA	4.18	4.4	4.62	V
I _{OUT}	Output Current	6.4V		20	30		mA
ΔV _{OUT}	Load Regulation	6.4V	1mA ≤ I _{OUT} ≤ 30mA		60	100	mV
V _D <small>Note2</small>	Voltage Drop		I _{OUT} =1mA		100		mV
I _{SS}	Current Consumption	6.4V	No load		4	7.5	uA
$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	Line Regulation		V _{OUT} +2V ≤ V _{IN} ≤ 24V I _{OUT} =1mA		0.2		%/V
V _{IN}	Input Voltage					24	V
$\frac{\Delta V_{OUT}}{\Delta T_a}$	Temperature Coefficient	6.4V	I _{OUT} =10mA 0°C < T _a < 70°C		±0.7		mV/°C

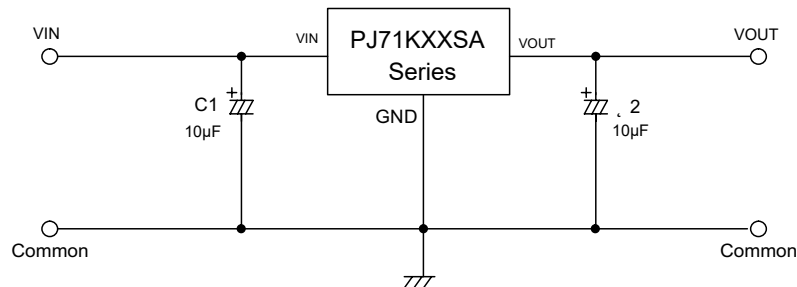


PJ71K50SA (Ta=25°C)

Symbol	Parameter	Test Conditions		Min.	Typ.	Max.	Units
		V _{IN}	Conditions				
V _{OUT}	Output Voltage	7V	I _{OUT} =10mA	4.75	5.0	5.25	V
I _{OUT}	Output Current	7V		20	30		mA
ΔV _{OUT}	Load Regulation	7V	1mA ≤ I _{OUT} ≤ 30mA		60	100	mV
V _D ^{Note2}	Voltage Drop		I _{OUT} =1mA		100		mV
I _{SS}	Current Consumption	7V	No load		5	9	uA
$\frac{\Delta V_{OUT}}{\Delta V_{IN} \times V_{OUT}}$	Line Regulation		V _{OUT} +2V ≤ V _{IN} ≤ 24V I _{OUT} =1mA		0.2		%/V
V _{IN}	Input Voltage					24	V
$\frac{\Delta V_{OUT}}{\Delta T_a}$	Temperature Coefficient	7V	I _{OUT} =10mA 0°C < T _a < 70°C		±0.75		mV/°C

NOTE: 1. The difference of input voltage and output voltage when input voltage falls down gradually till output voltage equals to 98% of rating V_{OUT}.

Basic Circuits

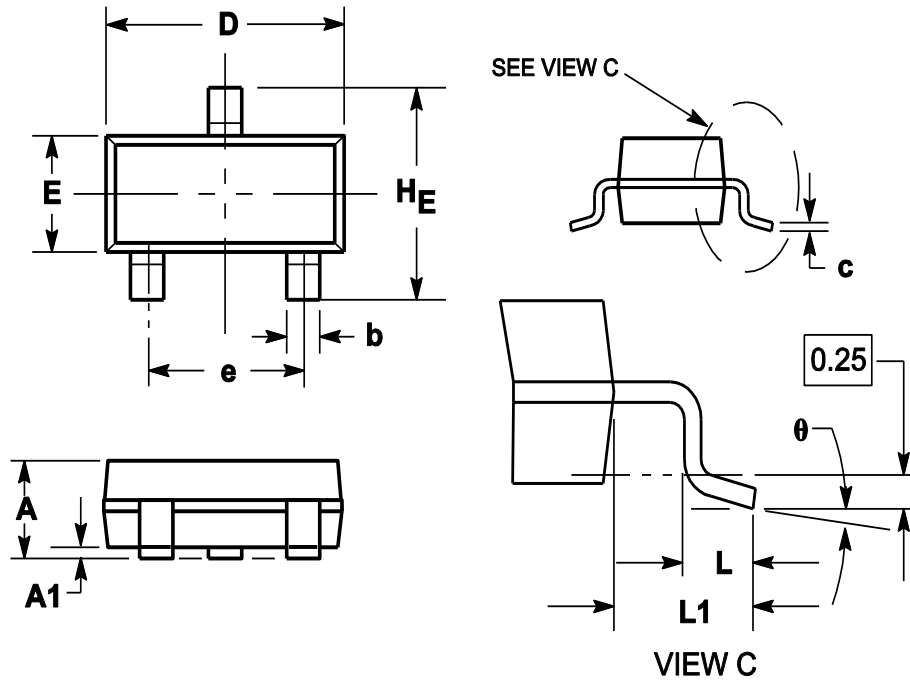




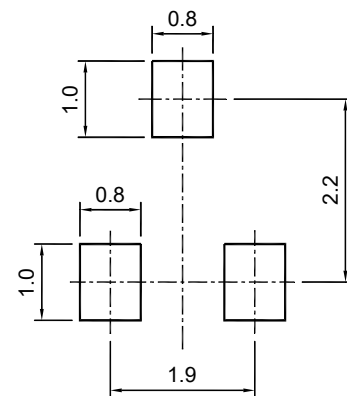
Package Outline

SOT-23

Dimensions in mm



Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	0.900	1.025	1.150
A1	0.000	0.050	0.100
b	0.300	0.400	0.500
c	0.080	0.115	0.150
D	2.800	2.900	3.000
E	1.200	1.300	1.400
HE	2.250	2.400	2.550
e	1.800	1.900	2.000
L1	0.550REF		
L	0.300		0.500
θ	0°		8°



SOT-23

Recommended soldering pad

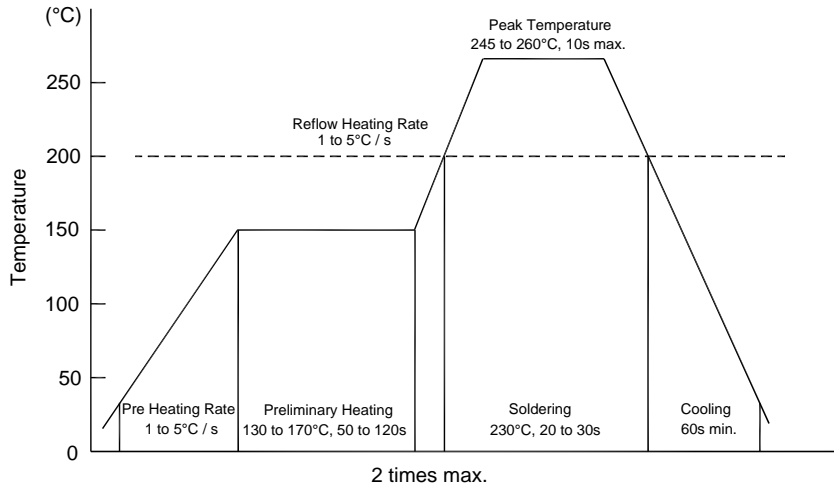
Ordering Information

Device	Package	Shipping
PJ71KXXSA Series	SOT-23	3000/Reel&Tape(7inch)



Conditions of Soldering and Storage

◆ Recommended condition of reflow soldering



Recommended peak temperature is over 245 °C. If peak temperature is below 245 °C, you may adjust the following parameters:

- Time length of peak temperature (longer)
- Time length of soldering (longer)
- Thickness of solder paste (thicker)

◆ Conditions of hand soldering

- Temperature: 370 °C
- Time: 3s max.
- Times: one time

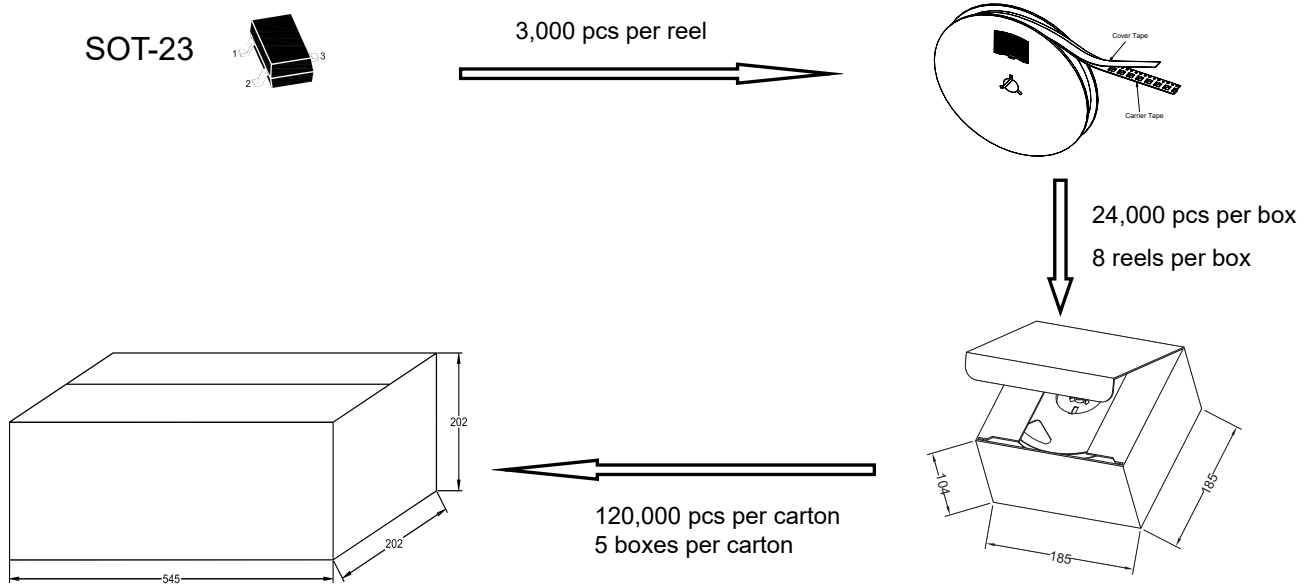
◆ Storage conditions

- **Temperature**
5 to 40 °C
- **Humidity**
30 to 80% RH
- **Recommended period**
One year after manufacturing

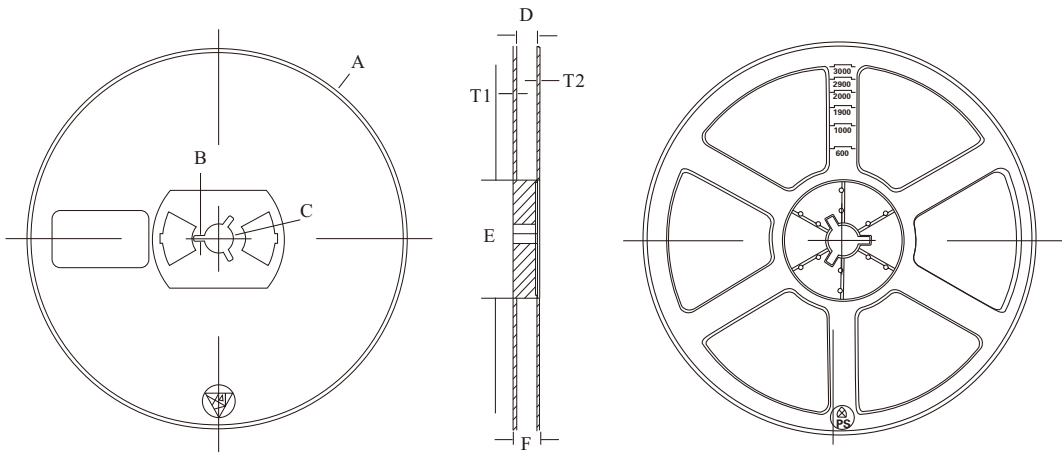


Package Specifications

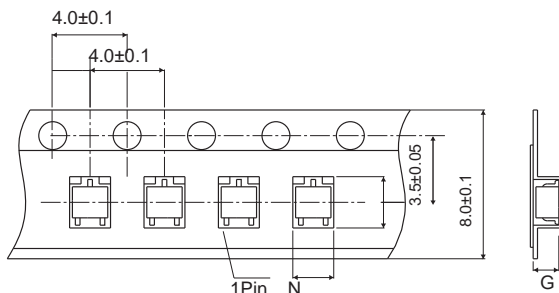
◆ The method of packaging



2. Tape and reel data(7inch Units:mm)



Reel (7")



Tape (8mm)

Symbol	Value (unit: mm)
A	∅ 177.8±1
B	2.7±0.2
C	∅ 13.5±0.2
E	∅ 54.5±0.2
F	12.3±0.3
D	9.6+2/-0.3
T1	1.0±0.2
T2	1.2±0.2
N	3.15±0.1
G	1.22±0.1