29 November 2018

**Product data sheet** 

# 1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a SOD523 plastic package designed to protect one transmission or data line from the damage caused by ESD and other transients.

## 2. Features and benefits

- Unidirectional ESD protection of one line
- Low clamping voltage: V<sub>CL</sub> = 40 V at I<sub>PPM</sub> = 5 A
- ESD protection > 30 kV
- IEC 61000-4-5 (surge); I<sub>PPM</sub> = 5 A at t<sub>p</sub> = 8/20 μs

# 3. Application information

- Computers and peripherals
- · Communication systems
- Audio and video equipment
- Data lines
- · CAN bus protection

## 4. Quick reference data

### Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RWM</sub>	reverse standoff voltage	T <sub>amb</sub> = 25 °C	-	-	15	V
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C	-	32	70	pF



# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	K	cathode[1]		1 +2
2	A	anode	1 2	sym035
			SOD523	

<sup>[1]</sup> The marking bar indicates pin 1.

# 6. Ordering information

## **Table 3. Ordering information**

Type number	Package					
	Name	Description	Version			
PESD15VS1UB		plastic, surface-mounted package; 2 leads; 1.2 mm x 0.8 mm x 0.6 mm body	SOD523			

# 7. Marking

### Table 4. Marking codes

Type number	Marking code
PESD15VS1UB	N4

# 8. Limiting values

#### Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
P <sub>PPM</sub>	rated peak pulse power	t <sub>p</sub> = 8/20 μs	[1]	-	160	W
I <sub>PPM</sub>	rated peak pulse current	t <sub>p</sub> = 8/20 μs	[1]	-	5	Α
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-55	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C
ESD maximu	m ratings			'	•	
V <sub>ESD</sub>	electrostatic discharge	IEC 61000-4-2 (contact discharge)	[2]	-	30	kV
	voltage	HBM MIL-STD883		-	10	kV

- [1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.
- [2] Device stressed with ten non-repetitive ESD pulses.

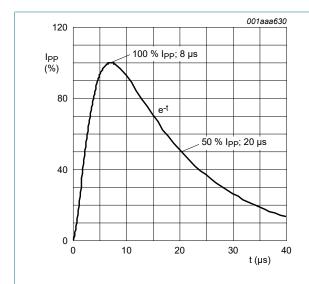


Fig. 1. 8/20 µs pulse waveform according to IEC 61000-4-5

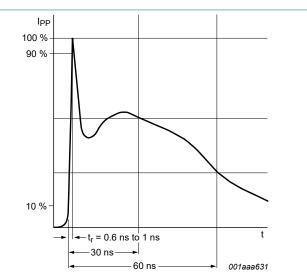


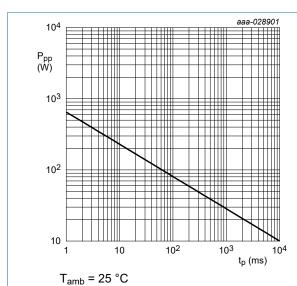
Fig. 2. ESD pulse waveform according to IEC 61000-4-2

## 9. Characteristics

#### **Table 6. Characteristics**

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{RWM}$	reverse standoff voltage	T <sub>amb</sub> = 25 °C		-	-	15	V
$V_{BR}$	breakdown voltage	I <sub>R</sub> = 5 mA; T <sub>amb</sub> = 25 °C		17.6	18	18.4	V
I <sub>RM</sub>	reverse leakage current	V <sub>RWM</sub> = 15 V; T <sub>amb</sub> = 25 °C		-	1	50	nA
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 0 V; T <sub>amb</sub> = 25 °C		-	32	70	pF
V <sub>CL</sub>	clamping voltage	I <sub>PPM</sub> = 1 A; T <sub>amb</sub> = 25 °C	[1]	-	-	23	V
		I <sub>PPM</sub> = 5 A; T <sub>amb</sub> = 25 °C	[1]	-	-	40	V
r <sub>dif</sub>	differential resistance	I <sub>R</sub> = 1 mA; T <sub>amb</sub> = 25 °C		-	-	225	Ω

[1] Non-repetitive current pulse 8/20 µs exponentially decay waveform.



 $t_p$  = 8/20 µs exponentially decay waveform. Fig. 3. Peak pulse power dissipation as a function of

pulse time; typical values

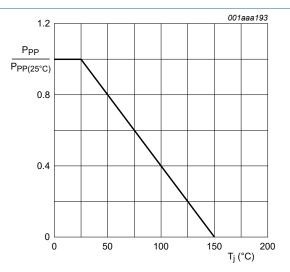


Fig. 4. Relative variation of peak pulse power as a function of junction temperature; typical values

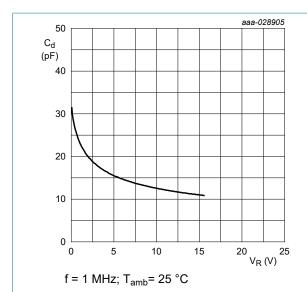


Fig. 5. Diode capacitance as a function of reverse voltage; typical values

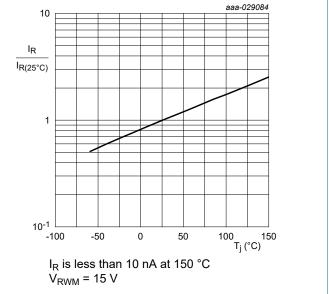
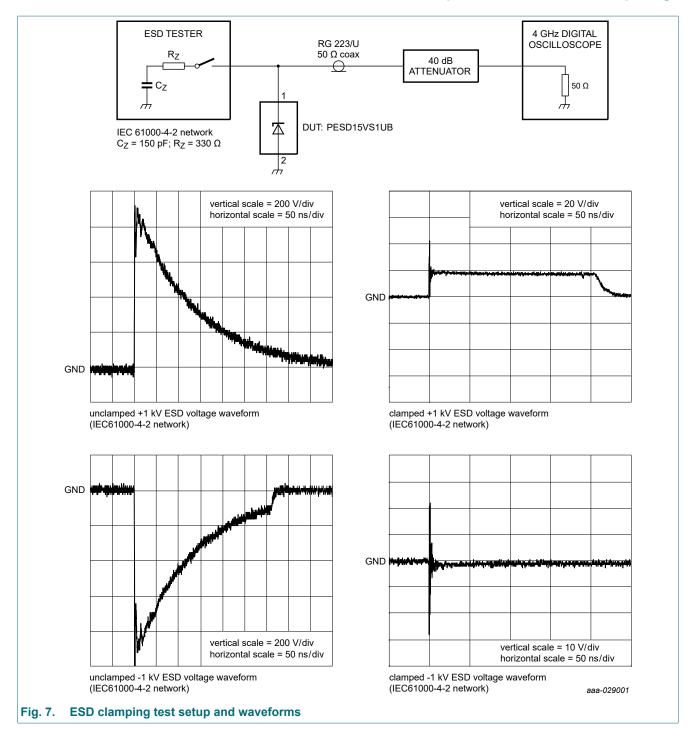


Fig. 6. Relative variation of reverse leakage current as a function of junction temperature; typical values

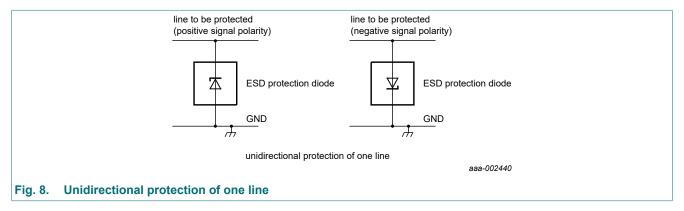
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## ESD protection diode in SOD523 package



# 10. Application information

The device is designed for unidirectional protection of one single data line from the damage caused by ESD and surge pulses. The device may be used on lines where the signal polarity is above or below ground. It provides a surge capability of up to 160 W per line for a 8/20 µs waveform.

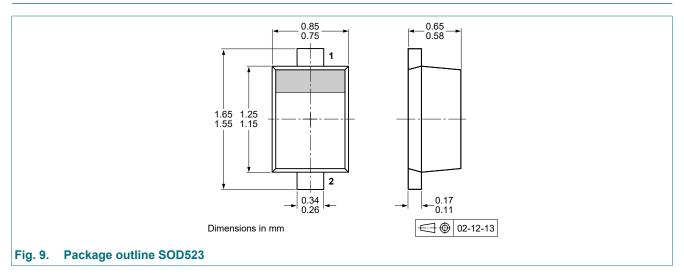


#### Circuit board layout and protection device placement

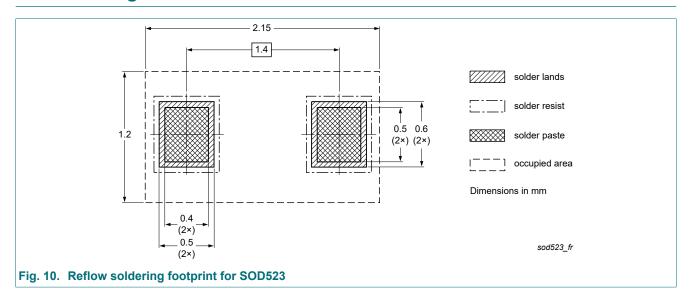
Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- $\textbf{8.} \ \ \text{Use ground planes whenever possible. For multilayer PCBs, use ground vias.}$

# 11. Package outline



# 12. Soldering



# 13. Revision history

### **Table 7. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PESD15VS1UB v.1	20181129	Product data sheet	-	PESDXS1UB_SERIES_2
Modifications:	Nexperia.  Legal texts hav Soldering section Application info	e been adapted to the ne on added. ormation: updated.		vith the identity guidelines of ere appropriate.
PESDXS1UB_SERIES_	2 20090824	Product data sheet	-	PESDXS1UB_SERIES_1
PESDXS1UB_SERIES_	1 20040614	Product data sheet	-	-

# 14. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
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