

MOSFET – N-Channel, QFET®

100 V, 48 A, 39 mΩ

FQH44N10

Description

This N-Channel enhancement mode power MOSFET is produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

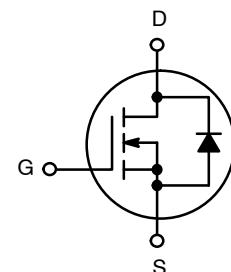
- 48 A, 100 V, $R_{DS(on)}$ = 39 mΩ (Max.) @ $V_{GS} = 10$ V, $I_D = 24$ A
- Low Gate Charge (Typ. 48 nC)
- Low C_{rss} (Typ. 85 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating

ON

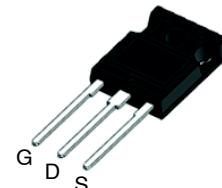
ON Semiconductor®

www.onsemi.com

V_{DSS}	$R_{DS(on)}$ MAX	I_D MAX
100 V	39 mΩ @ 10 V	48 A

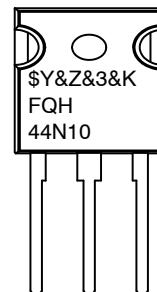


POWER MOSFET



TO-247-3LD
CASE 340CK

MARKING DIAGRAM



\$Y = ON Semiconductor Logo
&Z = Assembly Plant Code
&3 = Numeric Date Code
&K = Lot Code
FQH44N10 = Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

FQH44N10

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, Unless otherwise noted)

Symbol	Parameter		FQH44N10-133	Unit
V _{DSS}	Drain-Source Voltage		100	V
I _D	Drain Current	Continuous (T _C = 25°C)	48	A
		Continuous (T _C = 100°C)	34	
I _{DM}	Drain Current		192	A
V _{GSS}	Gate-Source Voltage		±25	V
E _{AS}	Single Pulsed Avalanche Energy (Note 1)		530	mJ
I _{AR}	Avalanche Current (Note 1)		48	A
E _{AR}	Repetitive Avalanche Energy (Note 1)		18	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		6.0	V/ns
P _D	Power Dissipation	(T _C = 25°C)	180	W
		Derate Above 25°C	1.2	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
T _L	Maximum Lead Temperature for Soldering Purpose 1/8" from Case for 5 seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. L = 0.345 mH, I_{AS} = 48 A, V_{DD} = 25 V, R_G = 25 Ω, starting T_J = 25°C.
3. I_{SD} ≤ 43.5 A, di/dt ≤ 300 A/μs, V_{DD} ≤ BV_{DSS}, starting T_J = 25°C.

THERMAL CHARACTERISTICS

Symbol	Parameter	FQH44N10-133	Unit
R _{θJC}	Thermal Resistance, Junction to Case, Max.	0.83	°C/W
R _{θCS}	Thermal Resistance, Case-to-Sink, Typ.	0.24	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max.	40	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQH44N10-133	FQH44N10	TO-247	Tube	N/A	N/A	30 Units

FQH44N10

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
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OFF CHARACTERISTICS

BV _{DSS}	Drain–Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	100			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.1		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100 V, V _{GS} = 0 V		1		μA
		V _{DS} = 80 V, T _C = 150°C			10	
I _{GSSF}	Gate–Body Leakage Current, Forward	V _{GS} = 25 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate–Body Leakage Current, Reverse	V _{GS} = -25 V, V _{DS} = 0 V			-100	nA

ON CHARACTERISTICS

V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0		4.0	V
R _{D(on)}	Static Drain–Source On–Resistance	V _{GS} = 10 V, I _D = 24 A		0.03	0.039	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 24 A		31		S

DYNAMIC CHARACTERISTICS

C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz		1400	1800	pF
C _{oss}	Output Capacitance			425	550	pF
C _{rss}	Reverse Transfer Capacitance			85	110	pF

SWITCHING CHARACTERISTICS

t _{d(on)}	Turn-On Delay Time	V _{DD} = 50 V, I _D = 43.5 A, R _g = 25 Ω (Note 4)		19	45	ns
t _r	Turn-On Rise Time			190	390	ns
t _{d(off)}	Turn-Off Delay Time			90	190	ns
t _f	Turn-Off Fall Time			100	210	ns
Q _g	Total Gate Charge	V _{DS} = 80 V, I _D = 43.5 A, V _{GS} = 10 V (Note 4)		48	62	nC
Q _{gs}	Gate–Source Charge			9.0		nC
Q _{gd}	Gate–Drain Charge			24		nC

DRAIN–SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS

I _S	Maximum Continuous Drain–Source Diode Forward Current			48	A
I _{SM}	Maximum Pulsed Drain–Source Diode Forward Current			192	A
V _{SD}	Drain–Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 48 A		1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 43.5 A, dI _F /dt = 100 A/μs		98	ns
Q _{rr}	Reverse Recovery Charge			360	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

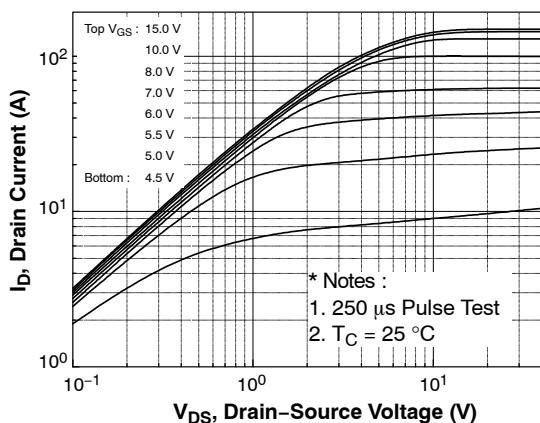


Figure 1. On-Region Characteristics

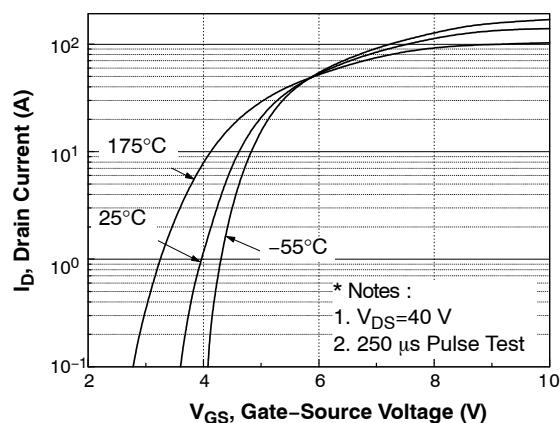


Figure 2. Transfer Characteristics

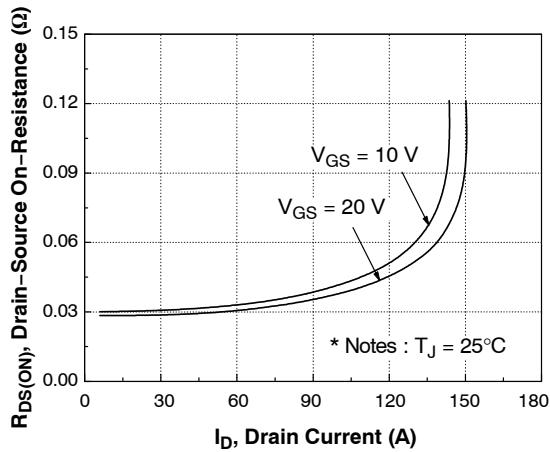


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

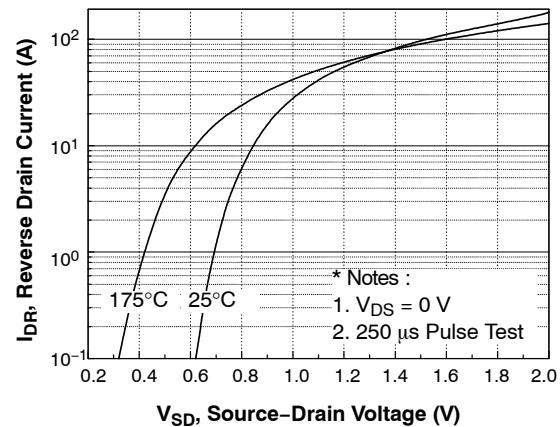


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

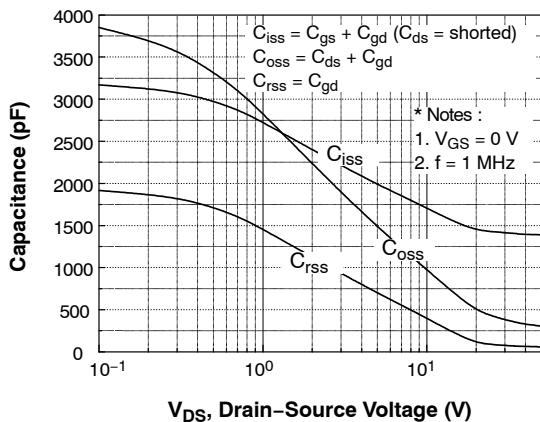


Figure 5. Capacitance Characteristics

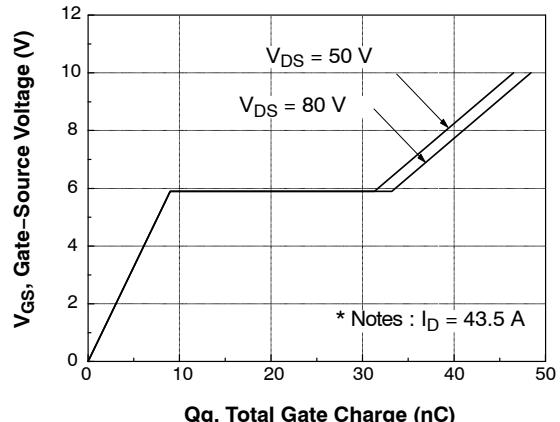


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

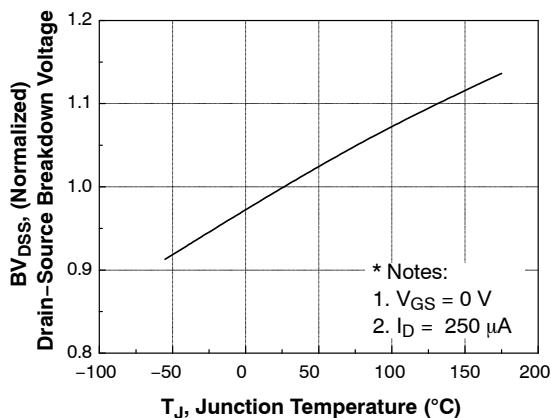


Figure 7. Breakdown Voltage Variation vs. Temperature

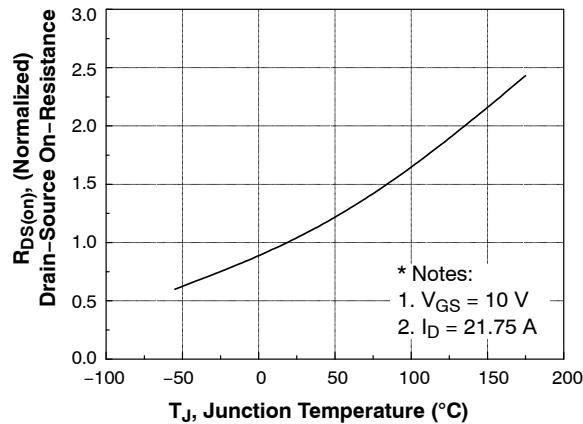


Figure 8. On-Resistance Variation vs. Temperature

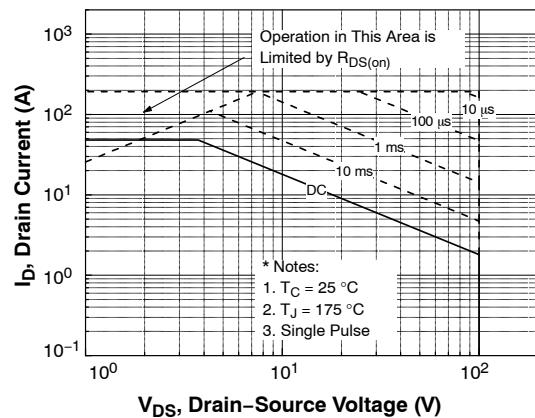


Figure 9. Maximum Safe Operating Area

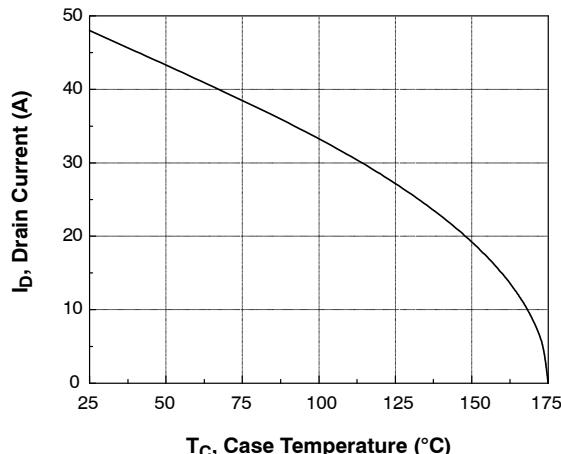


Figure 10. Maximum Drain Current vs. Case Temperature

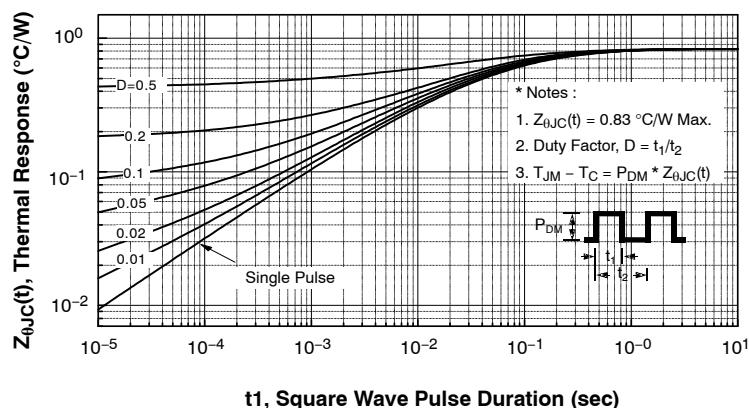


Figure 11. Transient Thermal Response Curve

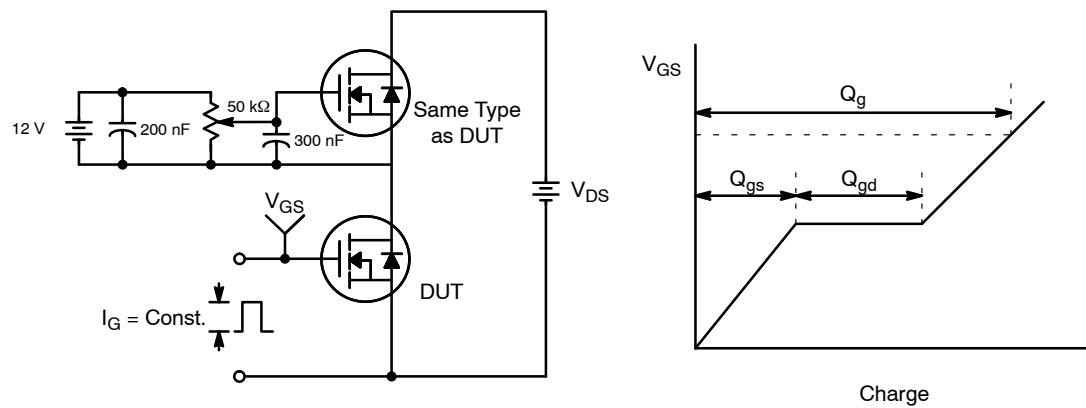


Figure 12. Gate Charge Test Circuit & Waveform

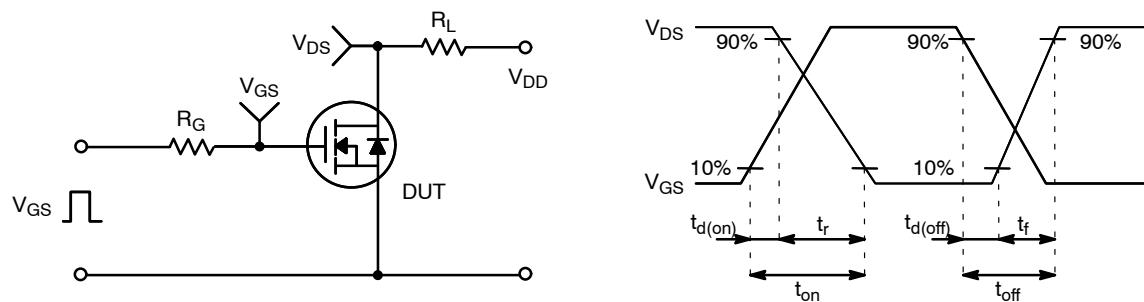


Figure 13. Resistive Switching Test Circuit & Waveforms

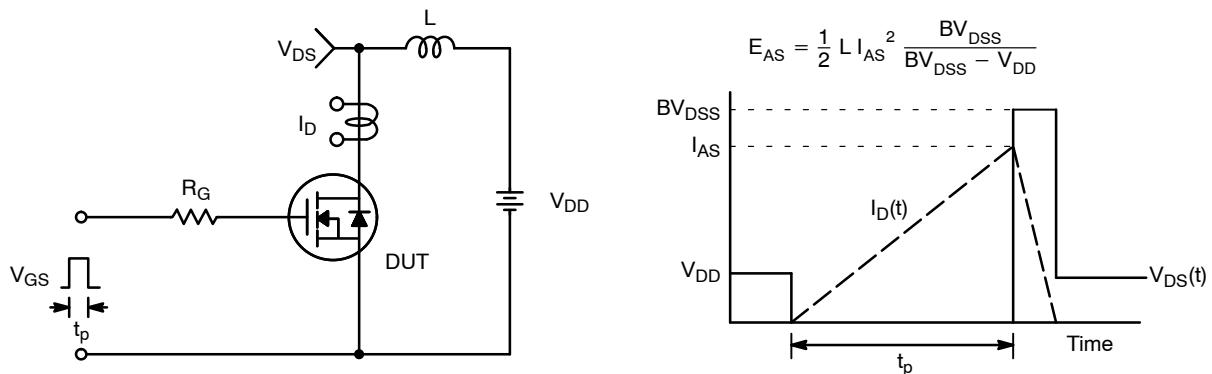


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

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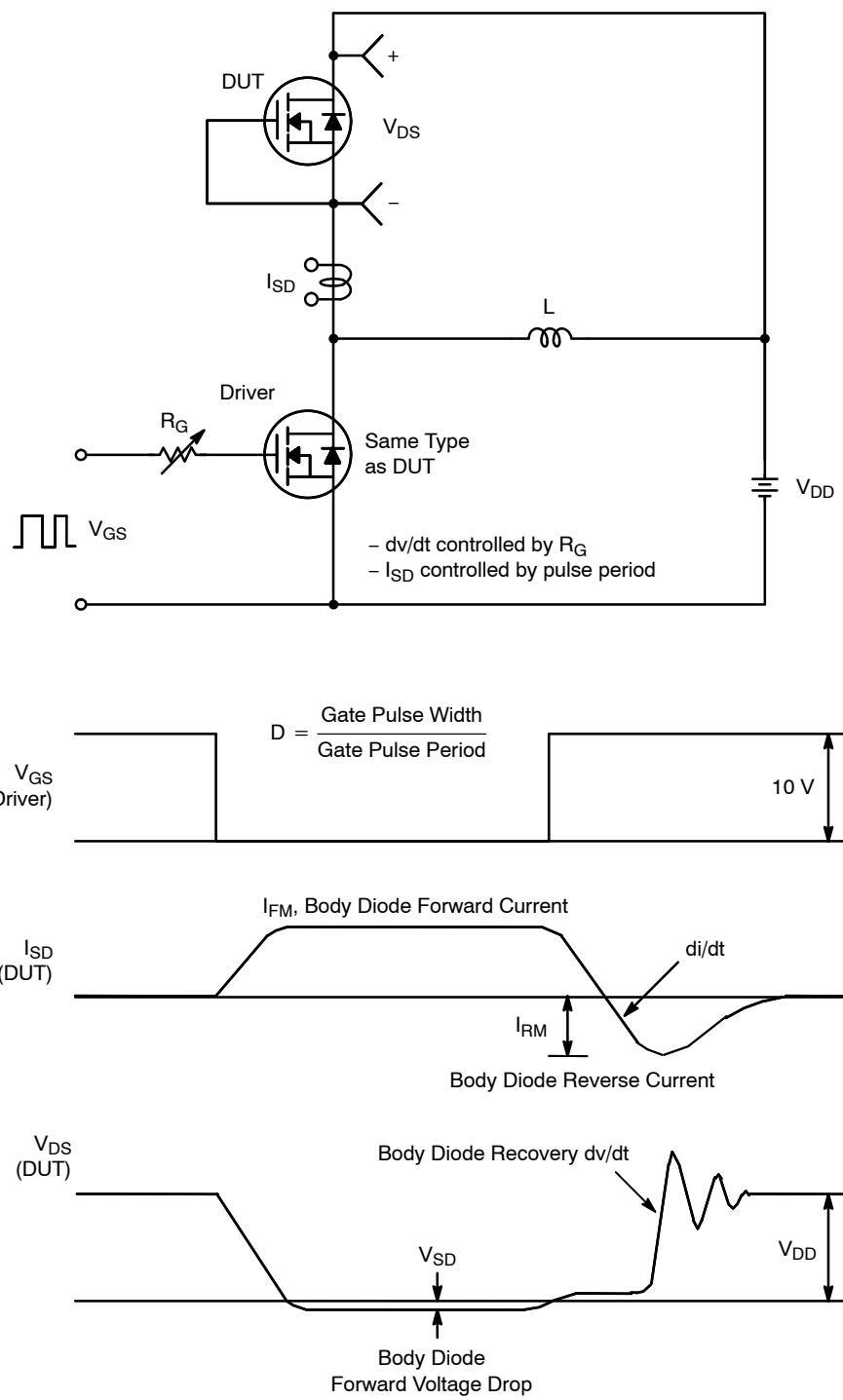
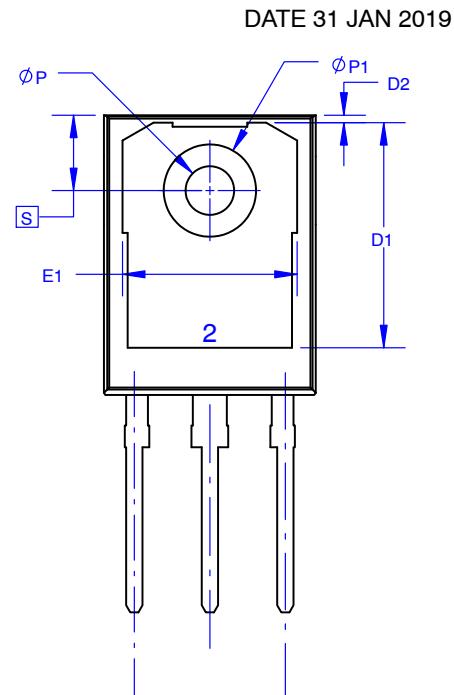
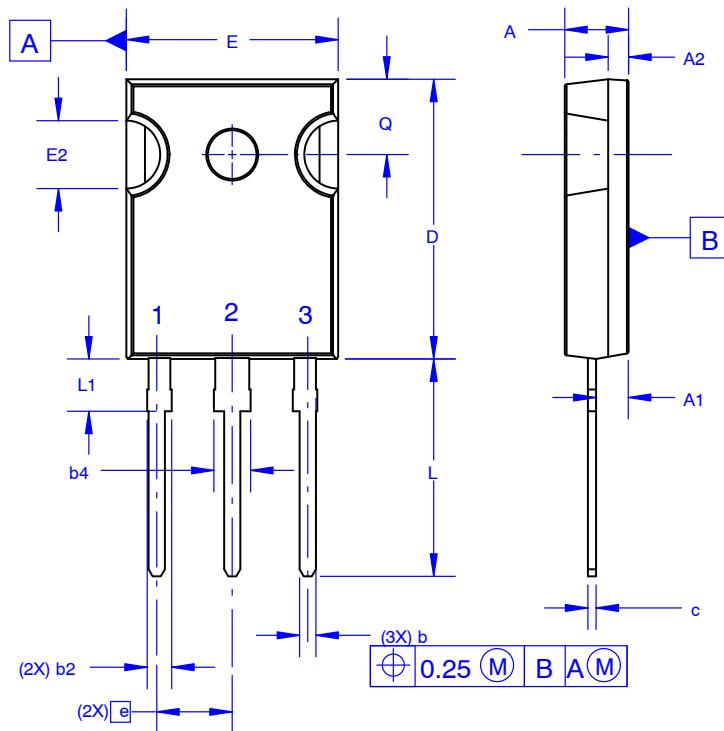


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

TO-247-3LD SHORT LEAD
CASE 340CK
ISSUE A



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

**GENERIC
MARKING DIAGRAM***



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D	20.32	20.57	20.82
D1	13.08	~	~
D2	0.51	0.93	1.35
E	15.37	15.62	15.87
E1	12.81	~	~
E2	4.96	5.08	5.20
e	~	5.56	~
L	15.75	16.00	16.25
L1	3.69	3.81	3.93
ØP	3.51	3.58	3.65
ØP1	6.60	6.80	7.00
Q	5.34	5.46	5.58
S	5.34	5.46	5.58

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