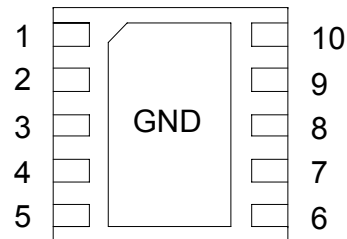


Features

- Ultra low leakage: nA level
- Operating voltage: 5V
- Low clamping voltage
- Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30\text{kV}$
 - Contact discharge: $\pm 30\text{kV}$
 - IEC61000-4-4 (EFT) 40A (5/50ns)
 - IEC61000-4-5 (Lightning) 80A (8/20 μs)
- RoHS Compliant

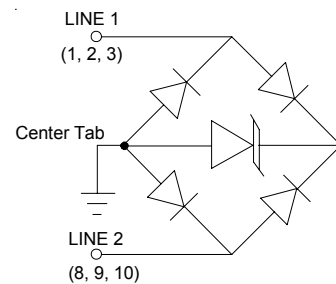
Dimensions DFN1616-6



Applications

- T3/E3
- 10/100/1000M
- Carrier Class Equipment
- ISDN interfaces
- Integrated Magnetic

Pin Configuration



Mechanical Characteristics

- Package: DFN2626-10
- Lead Finish: Lead Free
- UL Flammability Classification Rating 94V-0
- Quantity Per Reel: 3,000 pcs
- Reel Size: 7 inch

Absolute Maximum Ratings (Tamb=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (2/20 μs)	P _{pp}	2000	W
ESD per IEC 61000-4-2 (Air)	V _{ESD}	± 30	Kv
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{STJ}	-55 to +150	°C

Electrical Characteristics (TA=25°C unless otherwise specified)

Part Number	Device Marking	V _{RWM} (V)	V _{BR} (V)	I _T (mA)	V _c @100A 2/20μs note1	V _c		I _R μA (Max)	C (Pf) (Typ.)
						(Max) note2	(@A) 2/20μs		
ULC0602N	0602	6.0	6.8	1	20	25	100	5	10

Note1: V_c between I/O line

Note2: V_c between I/O line and ground

Characteristic Curves

Fig1. 8/20μs Pulse Waveform

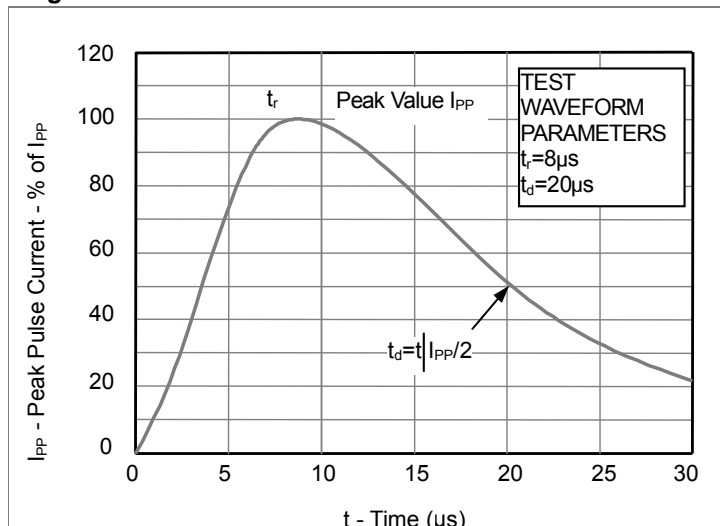


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

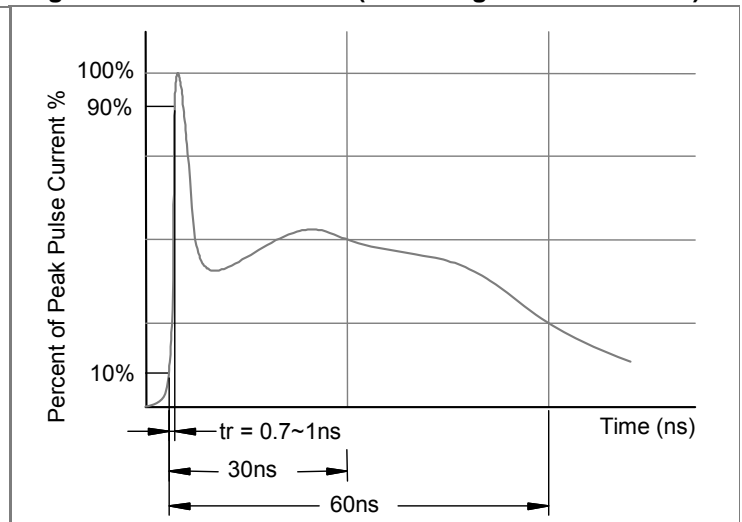
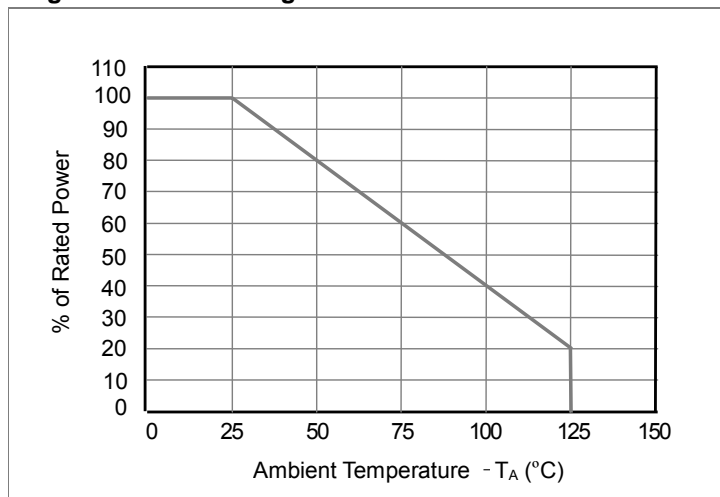
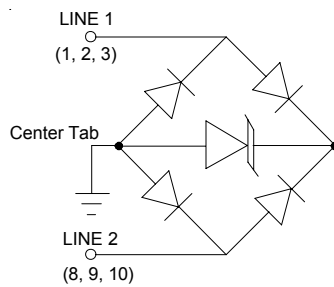


Fig3. Power Derating Curve

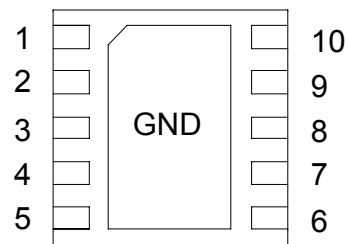


Applications Information

These devices are designed to protect two high-speed data lines (one differential pair) from transient overvoltages which result from lightning and ESD. They can be configured to protect in differential (Line-to-Line) and common (Line-to-Ground) mode. Data line inputs/outputs are connected at pins 1, 2 and 3, and 8, 9 and 10 as shown. For proper operation, pins 1 - 3 must be connected together and pins 8 - 10 must be connected together. Pins 4, 5, 6, and 7 left unconnected. For differential operation, the center tab is also left not connected. For common mode operation, the center tab is connected to ground. The ground connection should be made directly to a ground plane on the board for best results. The use of multiple vias is recommended for reduced ground loop inductance.



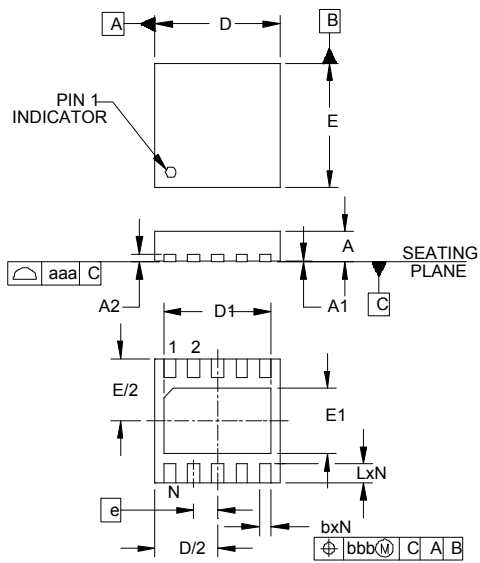
Circuit Diagram



Pin Configuration

Pin	Identification
1, 2, 3	Line 1 in/out
8, 9, 10	Line 2 in/out
4, 5, 6, 7	No Connect
Center Tab	Ground

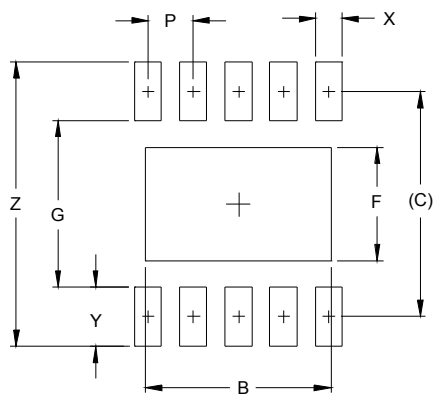
DFN2626-10 Package Outline Drawing



DIM	INCHES			MILLIMETERS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	.020	.022	.024	0.50	0.55	0.60
A1	.000	.001	.002	0.00	0.03	0.05
A2	(.007)			(0.17)		
b	.007	.010	.012	0.20	0.25	0.30
D	.098	.102	.106	2.50	2.60	2.70
D1	.079	.085	.089	2.00	2.15	2.25
E	.098	.102	.106	2.50	2.60	2.70
E1	.044	.050	.054	1.11	1.26	1.36
e	.020 BSC			0.50 BSC		
L	.011	.014	.016	0.30	0.35	0.40
N	10			10		
aaa	.003			0.08		
bbb	.004			0.10		

- NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
 2. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

Suggested Land Pattern



DIM	DIMENSIONS	
	INCHES	MILLIMETERS
B	.081	2.05
C	.100	2.50
F	.050	1.26
G	.073	1.85
P	.020	0.50
X	.012	0.30
Y	.025	0.65
Z	.124	3.15

- NOTES:
1. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.