

# N-Channel 200 V (D-S) MOSFET

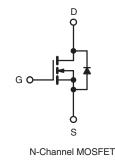
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	200				
R <sub>DS(on)</sub> (Ω)	V <sub>GS</sub> = 10 V	0.265			
Q <sub>g</sub> (Max.) (nC)	16				
Q <sub>gs</sub> (nC)	5				
Q <sub>gd</sub> (nC)	8				
Configuration	Single				

### **FEATURES**

- · Isolated Package
- High Voltage Isolation = 2.5 kV<sub>RMS</sub> (t = 60 s; f = 60 Hz)
- Sink to Lead Creepage Distance = 4.8 mm
- 175 °C Operating Temperature
- · Dynamic dV/dt Rating
- Low Thermal Resistance
- Lead (Pb)-free Available







<b>ABSOLUTE MAXIMUM RATINGS</b> T	$C = 25 \ ^{\circ}C$ , unless other	wise noted		
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	V <sub>DS</sub>	200	V	
Gate-Source Voltage	V <sub>GS</sub>	± 20		
Continuous Drain Current	$V_{GS} \text{ at } 10 \text{ V} \qquad \begin{array}{c} T_{C} = 25 \text{ °C} \\ T_{C} = 100 \text{ °C} \end{array}$	I-	10	
	$T_{\rm C} = 100 ^{\circ}{\rm C}$	I D	6.5	A
Pulsed Drain Current <sup>a</sup>	I <sub>DM</sub>	32		
Linear Derating Factor		0.24	W/°C	
Single Pulse Avalanche Energy <sup>b</sup>	E <sub>AS</sub>	36	mJ	
Repetitive Avalanche Current <sup>a</sup>	I <sub>AR</sub>	7.2	А	
Repetitive Avalanche Energy <sup>a</sup>	E <sub>AR</sub>	3.7	mJ	
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	PD	37	W
Peak Diode Recovery dV/dt <sup>c</sup>	dV/dt	5.5	V/ns	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C	
Soldering Recommendations (Peak Temperature)	for 10 s		300 <sup>d</sup>	
Mounting Torque	6-32 or M3 screw		10	lbf ⋅ in
	0-32 OF M3 SCIEW		1.1	N ⋅ m

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b.  $V_{DD} = 25 \text{ V}$ , starting  $T_J = 25 \text{ °C}$ , L = 1.0 mH,  $R_G = 25 \Omega$ ,  $I_{AS} = 7.2 \text{ A}$  (see fig. 12). c.  $I_{SD} \le 9.2 \text{ A}$ , dl/dt  $\le 110 \text{ A/}\mu\text{s}$ ,  $V_{DD} \le V_{DS}$ ,  $T_J \le 175 \text{ °C}$ .

d. 1.6 mm from case.



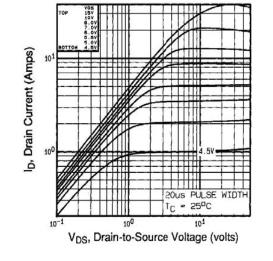
THERMAL RESISTANCE RA	TINGS							
PARAMETER	SYMBOL	TYP. MAX.			UNIT			
Maximum Junction-to-Ambient	R <sub>thJA</sub>	- 65			2014			
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>	- 4.1				°C/W		
<b>SPECIFICATIONS</b> $T_J = 25 \ ^{\circ}C$ ,	unless otherw	vise noted			0	T		
PARAMETER	SYMBOL	TEST	CONDITI	ONS	MIN.	TYP.	MAX.	UNIT
Static		1				1	•	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	0 V, I <sub>D</sub> = 2	50 μΑ	200	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	to 25 °C,	I <sub>D</sub> = 1 mA	-	0.13	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V$	$I_{\rm GS}, I_{\rm D} = 2$	250 μΑ	2.0	-	4.0	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20 V$			-	-	± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 2$	$V_{DS} = 200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$		-	-	25	
		$V_{DS}$ =160 V, $V_{GS}$ = 0 V, $T_{J}$ = 150 °C			-	-	250	μΑ
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub>	= 4.3 A <sup>b</sup>	-	0.265	-	Ω
Forward Transconductance	<b>g</b> <sub>fs</sub>	$V_{DS} = 50 \text{ V}, \text{ I}_{D} = 4.3 \text{ A}^{b}$			2.3	-	-	S
Dynamic								
Input Capacitance	C <sub>iss</sub>	$V_{GS} = 0 V, V_{DS} = 25 V, f = 1.0 MHz, see fig. 5 f = 1.0 MHz$		-	560	-	pF	
Output Capacitance	C <sub>oss</sub>			-	260	-		
Reverse Transfer Capacitance	C <sub>rss</sub>			-	110	-		
Drain to Sink Capacitance	С			-	12	-		
Total Gate Charge	Qg				-	-	16	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	l <sub>D</sub> = 9.2 /	A, V <sub>DS</sub> = 80 V, g. 6 and 13 <sup>b</sup>	-	-	4.4	nC
Gate-Drain Charge	Q <sub>gd</sub>		300 IQ	g. o and to	-	-	7.7	
Turn-On Delay Time	t <sub>d(on)</sub>				-	8.8	-	
Rise Time	t <sub>r</sub>			9.2 A,	-	30	-	
Turn-Off Delay Time	t <sub>d(off)</sub>			-	19	-	ns	
Fall Time	t <sub>f</sub>			-	20	-		
Internal Drain Inductance	L <sub>D</sub>	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nH	
Internal Source Inductance	L <sub>S</sub>			-	7.5	-		
Drain-Source Body Diode Characteristic	cs							
Continuous Source-Drain Diode Current	I <sub>S</sub>	MOSFET symbol showing the integral reverse p - n junction diode		-	10	-	A	
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>			-	32	-		
Body Diode Voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C,	<sub>S</sub> = 7.2 A,	$V_{GS} = 0 V^{b}$	-	-	2.5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	$T_{J} = 25 \text{ °C}, I_{F} = 9.2 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}^{b}$		dt - 100 A/uch	-	130	260	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			-	0.65	1.3	μC	
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by $L_S$ and $L_D$ )					_D)	

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width  $\leq$  300  $\mu s;$  duty cycle  $\leq$  2 %.





## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



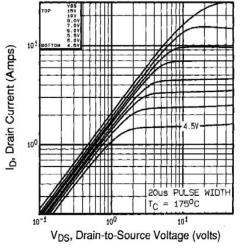


Fig. 2 - Typical Output Characteristics,  $T_C$  = 175  $^\circ C$ 

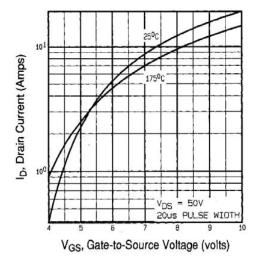


Fig. 3 - Typical Transfer Characteristics

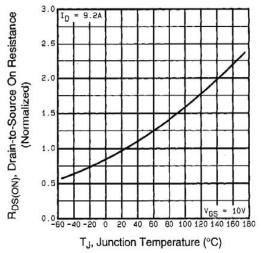


Fig. 4 - Normalized On-Resistance vs. Temperature



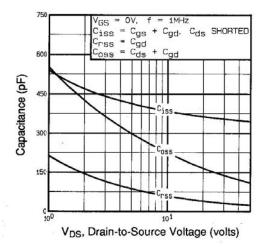


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

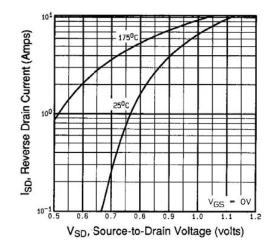


Fig. 7 - Typical Source-Drain Diode Forward Voltage

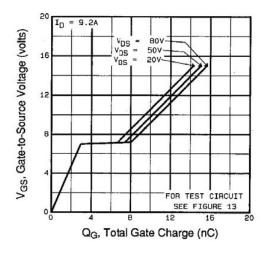


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

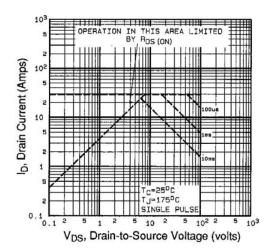


Fig. 5 - Fig. 8 - Maximum Safe Operating Area



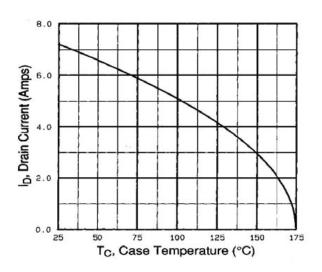


Fig. 9 - Maximum Drain Current vs. Case Temperature

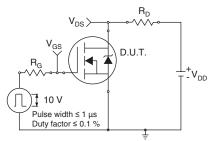


Fig. 10a - Switching Time Test Circuit

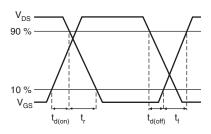
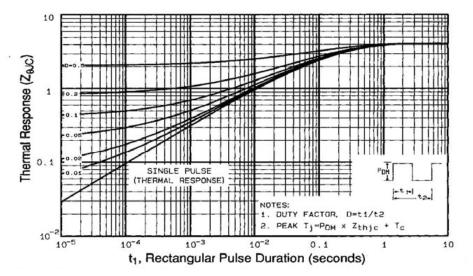


Fig. 10b - Switching Time Waveforms





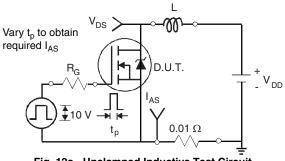


Fig. 12a - Unclamped Inductive Test Circuit

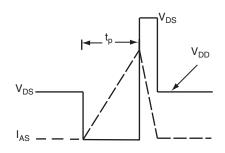
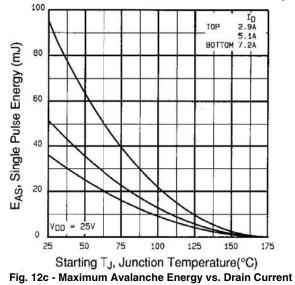


Fig. 12b - Unclamped Inductive Waveforms







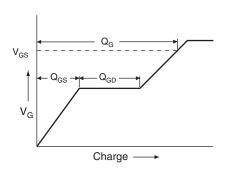
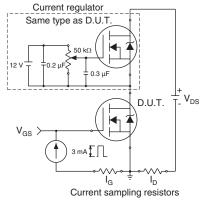
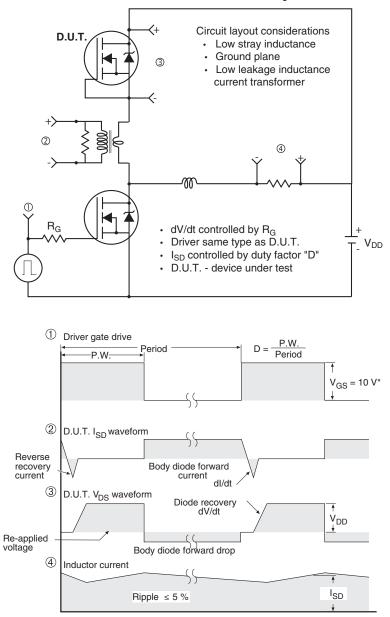


Fig. 13a - Basic Gate Charge Waveform









## Peak Diode Recovery dV/dt Test Circuit

\*  $V_{GS}$  = 5 V for logic level devices

Fig. 14 - For N-Channel



# Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

# **Material Category Policy**

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.