# MSKSEMI















**ESD** 

**TVS** 

**TSS** 

MOV

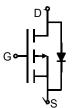
**GDT** 

**PLED** 

Broduct data speet



# P-Channel Enhancement Mode Power MOSFET



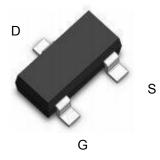
#### Schematic diagram

#### **FEATURE**

TrenchFET Power MOSFET

#### **APPLICATIONS**

- Load Switch for Portable Devices
- DC/DC Converter



### Maximum ratings (T₂=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit	
Drain-Source Voltage	$V_{DS}$	-20	V	
Gate-Source Voltage	V <sub>GS</sub>	±8	7 '	
Continuous Drain Current	I <sub>D</sub>	-3		
Pulsed Drain Current	I <sub>DM</sub>	-10	Α	
Continuous Source-Drain Diode Current	ls	-0.72		
Maximum Power Dissipation	P <sub>D</sub>	0.35	W	
Thermal Resistance from Junction to Ambient(t ≤5s)	$R_{\theta JA}$	357	°C/W	
Junction Temperature	TJ	150	_ ზ	
Storage Temperature	T <sub>stg</sub>	-55 ~+150		





# Electrical characteristics (Ta=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
Static			<u>'</u>			
Drain-source breakdown voltage	V(BR)DSS	SS V <sub>GS</sub> = 0V, I <sub>D</sub> =-250μA				\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
Gate-source threshold voltage	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-0.4		-1	V
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±8V			±100	nA
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V			-1	μA
Drain-source on-state resistance <sup>a</sup>	Б	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-2.8A		0.090	0.112	
	RDS(on)	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2.0A		0.110	0.142	Ω
Forward transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-2.8A		6.5		S
Dynamic <sup>b</sup>		,		'		
Input capacitance	C <sub>iss</sub>			405		pF
Output capacitance	Coss	V <sub>DS</sub> =-10V,V <sub>GS</sub> =0V,f =1MHz		75		
Reverse transfer capacitance	C <sub>rss</sub>			55		
Total gate charge	0	V <sub>DS</sub> =-10V,V <sub>GS</sub> =-4.5V,I <sub>D</sub> =-3A		5.5	10	
	$Q_g$			3.3	6	nC
Gate-source charge	Q <sub>gs</sub>	V <sub>DS</sub> =-10V,V <sub>GS</sub> =-2.5V,I <sub>D</sub> =-3A		0.7		
Gate-drain charge	$Q_{gd}$			1.3		
Gate resistance	Rg	f=1MHz		6.0		Ω
Turn-on delay time	td(on)	101		11	20	
Rise time	tr	$V_{DD}$ =-10V, $R_{L}$ =10 $\Omega$ , $I_{D}$ =-1A, $V_{GEN}$ =-4.5V, $R_{Q}$ =1 $\Omega$		35	60	ns
Turn-off delay time	td(off)			30	50	
Fall time	<b>t</b> f	VGEN4.5V,Ng-112		10	20	
Drain-source body diode characterist	tics					
Continuous source-drain diode current	Is	T <sub>C</sub> =25°C			-1.3	А
Pulse diode forward current <sup>a</sup>	I <sub>SM</sub>				-10	1
Body diode voltage	V <sub>SD</sub>	Is=-0.7A		-0.8	-1.2	V

#### Notes:

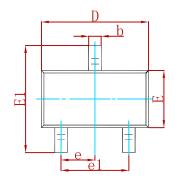
a.Pulse Test : Pulse Width < 300 $\mu$ s, Duty Cycle ≤2%.

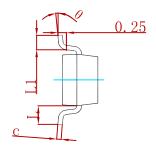
b.Guaranteed by design, not subject to production testing.

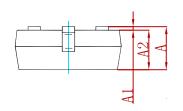


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# **PACKAGE MECHANICAL DATA**

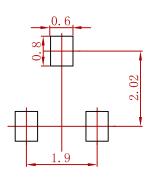






Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min	Max	Min	Max	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
Е	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP		0.037 TYP		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF		0.022 REF		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

# **Suggested Pad Layout**



- 1.Controlling dimension:in millimeters.2.General tolerance:± 0.05mm.3.The pad layout is for reference purposes only.

# **REEL SPECIFICATION**

P/N	PKG	QTY
AO3401CI-MS	SOT-23	3000



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