

MS88SF2

Bluetooth Low Energy module

Specification V1.0

Small-sized nRF52840 Module MS88SF2 Specification



MS88SF2 is advanced, compact (23.2mm×17.4mm×2mm), highly flexible, ultra-low power wireless BLE 5.0 Module based on nRF52840 SoCs. Its powerful 32-bit ARM Cortex™ M4F CPU, a 1 MB flash memory, a 256KB RAM and integrating a 2.4 GH transceiver offers the perfect solution for Bluetooth connectivity.

Features

1. ARM ® Cortex ® -M4 32-bit processor with FPU, 64 MHz
2. BLE 5 data rate: 2Mbps, 1Mbps, 500 kbps, 125kbps. IEEE 802.15.4 Thread and Zigbee data rate: 250kbps, Proprietary 2.4 GHz: 2 Mbps, 1 Mbps
3. Flash/RAM: 1MB/256KB
4. GPIO: 20
5. 2×UART/4×SPI masters/3×SPI slaves
6. SoC TX power: -40dB to +8dB
7. Operating temperature: -40°C to +85°C
8. Antenna: PCB, u.FL/IPEX connector
9. Module size: 23.2mm×17.4mm×2mm
10. Range: 125kbps: up to 250 meters in open space.

Application

1. Medical devices
2. Heart rate monitor
3. Blood pressure monitor
4. Blood glucose meter
5. Thermometer
6. Sport facilities
7. Weighing machine
8. Sports and fitness sensors
9. Accessories
10. 3D glasses and gaming controller
11. Mobile accessories
12. Remote controllers / Toys
13. Electronic devices
14. Cycle computer

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1 Product Introduction

MS88SF2 series are advanced, compact (23.2mm×17.4mm×2mm), highly flexible, ultra-low power wireless BLE 5.0 Module based on nRF52840 SoCs. Its powerful 32-bit ARM Cortex™ M4F CPU, a 1 MB flash memory, a 256KB RAM and integrating a 2.4 GH transceiver can offers the perfect solution for Bluetooth connectivity.

Fully qualified BLE stacks for nRF52840 are implemented in the S140 SoftDevices which can be freely downloaded. In the meantime, ANT, ZIGBEE and THREAD applications can be supported by nRF52840 with extensive software.

MS88SF2 can meet the most demanding applications, it allow faster time to market with high performance, low power management and reduced development cost. Also, its range is estimated up to 250 meters in open space for 1Mbps data rate; up to 120 meters in open space for 2 Mbps data rate.

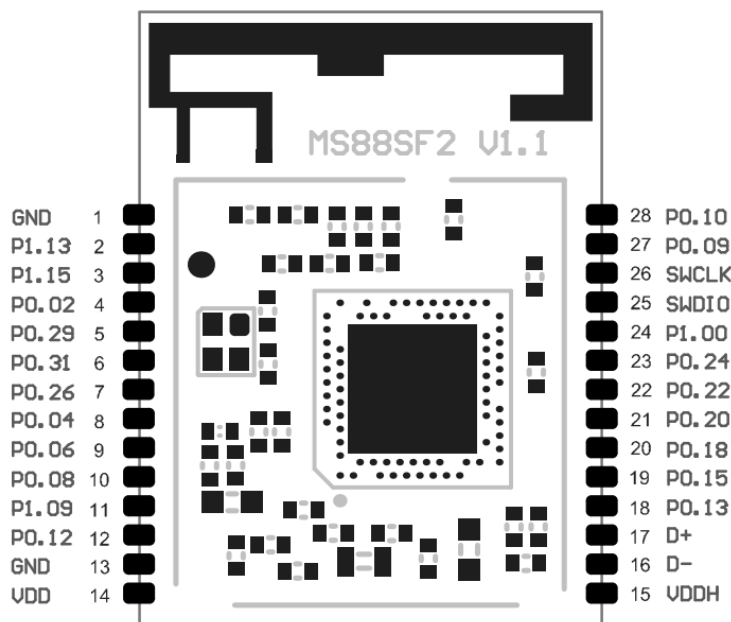
1.1 Ordering information

Ordering number	Description
MS88SF2-1Y40AIR	nRF52840-QIAA BT 5.0 Module, PCB Antenna, reel pack
MS88SF2-3Y40AIR	nRF52840-QIAA BT 5.0 Module, U.FL connector, reel pack

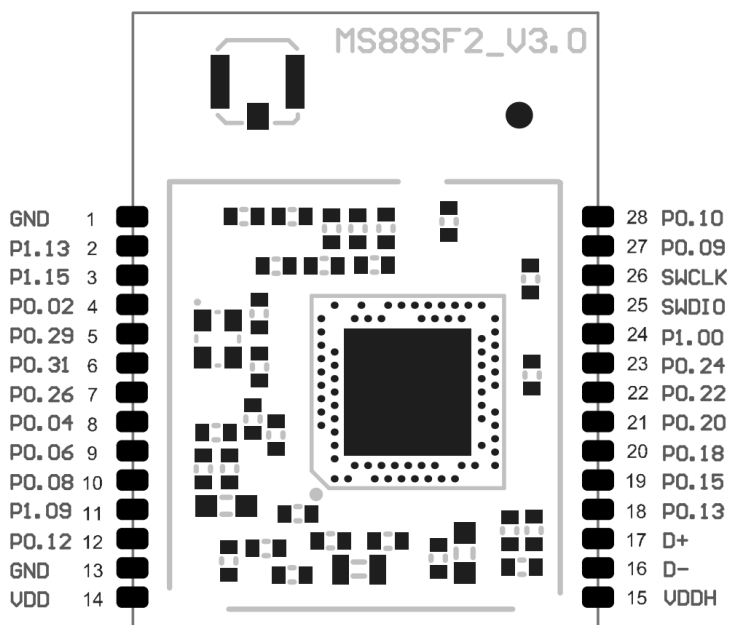
2 Pin Description

2.1 Pin assignment

2.1.1 MS88SF2 PCB Antenna



2.1.2 MS88SF2 u.FL/IPEX connector

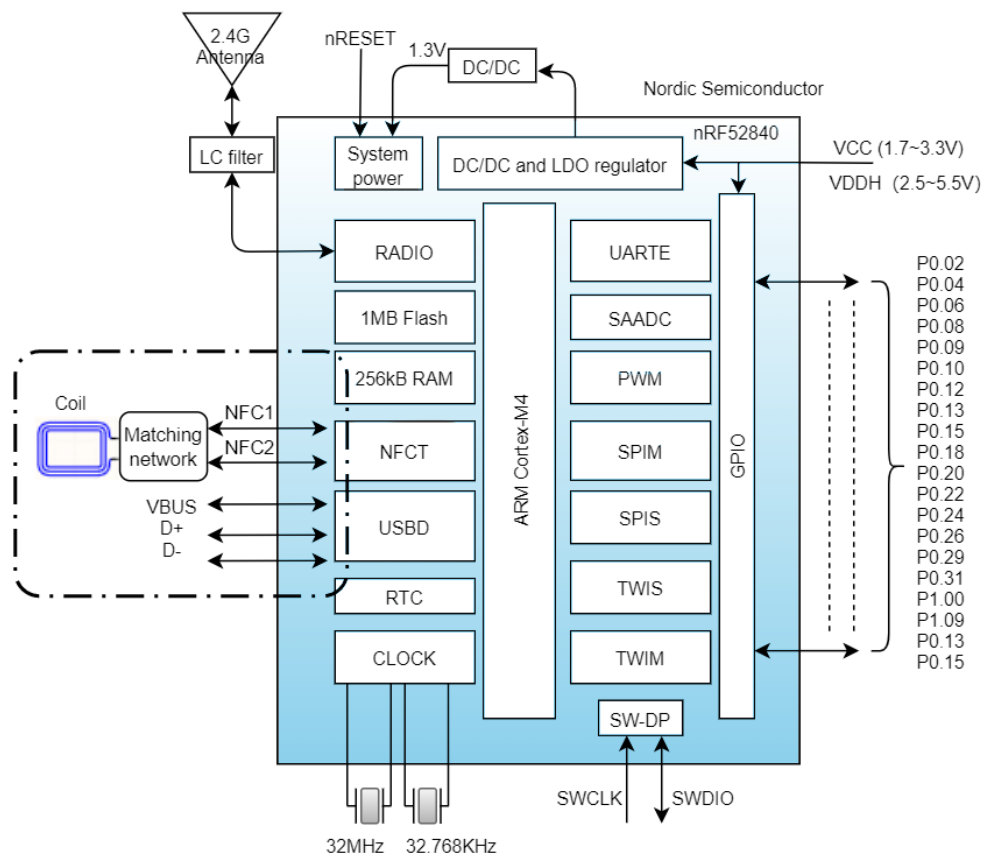


2.2 Pin definition

Symbol	Type	Description
VDD	Power	Power supply
VDDH	Power	Power supply, 2.5V-5.5V
GND	Power	Ground
SWDIO	Digital I/O	Hardware debug and flash/programming I/O/programming I/O.
SWCLK	Digital input (debug)	Hardware debug and flash programming I/O
P 0.29 AIN5	Digital I/O	General purpose I/O
	Analog input	Analog input
P 0.02 AIN0	Digital I/O	General purpose I/O
	Analog input	Analog input
P 1.15	Digital I/O	General purpose I/O
P1.13	Digital I/O	General purpose I/O
P 0.31 AIN7	Digital I/O	General purpose I/O
	Analog input	Analog input
P 0.04 AIN2	Digital I/O	General purpose I/O
	Analog input	Analog input
P 1.09 TRACEDATA33	Digital I/O	General purpose I/O
	Trace data	Trace buffer TRACEDATA[3]
P 0.12 TRACEDATA1	Digital I/O	General purpose I/O
	Trace data	Trace buffer TRACEDATA[1]
P 0.10 NFC2	Digital I/O	General purpose I/O
	NFC input	NFC antenna connection
P 0.09 NFC1	Digital I/O	General purpose I/O
	NFC input	NFC antenna connection

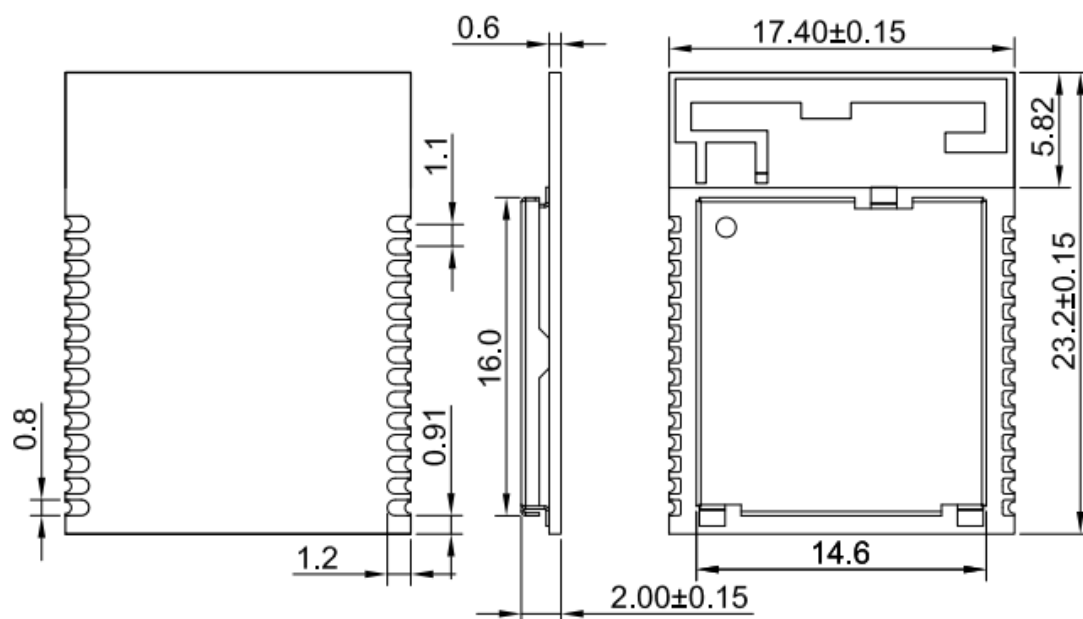
P 1.00 TRACEDATA0	Digital I/O	General purpose I/O
	Trace data	Trace buffer TRACEDATA[0]
P 0.18 nRESET	Digital I/O	General purpose I/O Configurable as system RESET
P 0.26	Digital I/O	General purpose I/O
P 0.06	Digital I/O	General purpose I/O
P 0.08	Digital I/O	General purpose I/O
P 0.24	Digital I/O	General purpose I/O
P 0.22	Digital I/O	General purpose I/O
P 0.20	Digital I/O	General purpose I/O
P 0.15	Digital I/O	General purpose I/O
P 0.13	Digital I/O	General purpose I/O
D+	Digital I/O	USB D+
D-	Digital I/O	USB D-

2.3 Block diagram

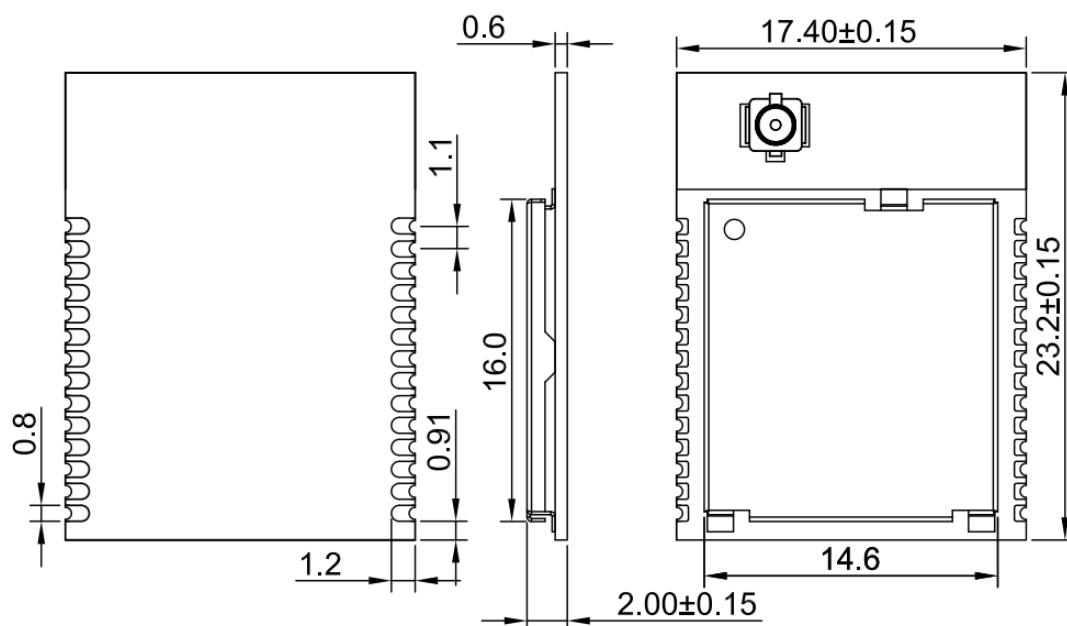


2.4 Mechanical Drawing

2.4.1 MS88SF2 PCB Antenna



2.4.2 MS88SF2 u.FL/IPEX connector



Important: Unit: mm Tolerance: +/- 0.1, default
Recommend solder pad size: 1.8*0.8mm, solder pad is suggested to extend 0.7mm distance.

3 Electrical Specification

The electrical specifications of the module are directly related to the Nordic semiconductor Specifications for the nRF52840 chipset. The below information is only the extract from nRF52840 specification. For more detailed information, please refer to the up-to-date specification of the chipset available on the Nordic semiconductor website.

3.1 Absolute maximum ratings

	Note	Min.	Max	Unit
Supply voltages				
VDD		-0.3	+3.9	V
VDDH		-0.3	+5.8	V
VBUS		-0.3	+5.8	V
VSS			0	V
I/O Pin voltage				
$V_{I/O}, VDD \leq 3.6V$		-0.3	$VDD + 0.3V$	V
$V_{I/O}, VDD > 3.6V$		-0.3	3.9V	V
NFC antenna pin current				
$I_{NFC1/2}$			80	mA
Radio				
RF input level			10	dBm
Environmental (AQFN package)				
Storage temperature		-40	+125	°C
MSL	Moisture Sensitivity Level		2	
ESD HBM	Human Body Model		4	KV
ESD CDM _{QF}	Charged Device Model (AQFN73,7×7mm package)		750	V

Important: Maximum ratings are the extreme limits to which the chip can be exposed for a limited amount of time without permanently damaging it. Exposure to absolute maximum ratings for prolonged periods of time may affect the reliability of the device.

3.2 Recommended operating conditions

The operating conditions are the physical parameters that the chip can operate within.

Symbol	Parameter	Min.	Nom.	Max.	Units
VDD	VDD supply voltage, independent of DCDC enable	1.7	3.0	3.6	V
VDD _{POR}	VDD supply voltage needed during power-on reset	1.75			V
VDDH	VDDH supply voltage, independent of DCDC enable	2.5	3.7	5.5	V
VBUS	VBUS USB supply voltage	4.35	5	5.5	V
t _{R,VDD}	Supply rise time (0 V to 1.7 V)			60	ms
t _{R,VDDH}	Supply rise time (0 V to 3.7 V)			100	ms
TA	Operating temperature	-40	25	85	°C

Important: The on-chip power-on reset circuitry may not function properly for rise times longer than the specified maximum.

3.3 Electronic characteristic

3.3.1 General radio characteristics

Symbol	Description	Min.	Nom.	Max.	Units
f _{OP}	Operating frequencies	2360		2500	MHz
f _{PLL,CH,SP}	PLL channel spacing		1		MHz
f _{DELTA,1M}	Frequency deviation@1 Mbps		±170		kHz
f _{DELTA,BLE,1M}	Frequency deviation @ BLE 1Mbps		±250		kHz
f _{DELTA,2M}	Frequency deviation @ 2Mbps		±320		kHz
f _{skBPS}	On-the-air data rate	125		2000	kbps
f _{chip,IEEE 802.15.4}	Chip rate in IEEE 802.15.4 mode		2000		kchipAs

3.3.2 Radio current consumption (Transmitter)

Symbol	Description	Min.	Typ.	Max.	Units
$I_{TX,PLUS8dBm,DCDC}$	TX only run current(DCDC,3V) $P_{RF}=+8$ dBm		14.1		mA
$I_{TX,PLUS8dBm}$	TX only run current $P_{RF}=+8$ dBm		30.4		mA
$I_{TX,PLUS4dBm,DCDC}$	TX only run current(DCDC,3V) $P_{RF}=+4$ dBm		9.3		mA
$I_{TX,PLUS4dBm}$	TX only run current $P_{RF}=+4$ dBm		18.9		mA
$I_{TX,0dBm,DCDC,5V,REGO}$	TX only run current (DCDC,5V,REGO out=3.3V) $P_{RF}=0$ dBm		6.0		mA
$I_{TX,0dBm,DCDC,5V,REGOLO}$	TX only run current(DCDC,5V,REGO out=1.8V) $P_{RF}=0$ dBm		5.2		mA
$I_{TX,0dBm,DCDC}$	TX only run current(DCDC,3V) $P_{RF}=0$ dBm		4.9		mA
$I_{TX,0dBm}$	TX only run current $P_{RF}=0$ dBm		10.2		mA
$I_{TX,MINUS4dBm,DCDC}$	TX only run current DCDC,3V $P_{RF}=-4$ dBm		3.4		mA
$I_{TX,MINUS4dBm}$	TX only run current $P_{RF}=-4$ dBm		7.3		mA
$I_{TX,MINUS8dBm,DCDC}$	TX only run current DCDC,3V $P_{RF}=-8$ dBm		3.0		mA
$I_{TX,MINUS8dBm}$	TX only run current $P_{RF}=-8$ dBm		6.4		mA
$I_{TX,MINUS12dBm,DCDC}$	TX only run current DCDC,3V $P_{RF}=-12$ dBm		2.7		mA
$I_{TX,MINUS12dBm}$	TX only run current $P_{RF}=-12$ dBm		5.7		mA
$I_{TX,MINUS16dBm,DCDC}$	TX only run current DCDC,3V $P_{RF}=-16$ dBm		2.5		mA
$I_{TX,MINUS16dBm}$	TX only run current $P_{RF}=-16$ dBm		5.3		mA
$I_{TX,MINUS20dBm,DCDC}$	TX only run current DCDC,3V $P_{RF}=-20$ dBm		2.3		mA
$I_{TX,MINUS20dBm}$	TX only run current $P_{RF}=-20$ dBm		5.0		mA
$I_{TX,MINUS40dBm,DCDC}$	TX only run current DCDC,3V $P_{RF}=-40$ dBm		2.0		mA
$I_{TX,MINUS40dBm}$	TX only run current $P_{RF}=-40$ dBm		4.0		mA
$I_{START,TX,DCDC}$	TX start-up current DCDC,3V $P_{RF}=4$ dBm		5.2		mA
$I_{START,TX}$	TX start-up current $P_{RF}=4$ dBm		11.0		mA
$I_{0dBm(DCDC, 3V)}$	TX current(DCDC,3V)1Mbps BLE measured from VBAT with PRF =0 dBm		6.4		mA

$I_{2dBm(DCDC, 3V)}$	TX current(DCDC,3V)1Mbps BLE measured from VBAT with $P_{RF}=2$ dBm	8.9	mA
$I_{4dBm(DCDC, 3V)}$	TX current(DCDC,3V)1Mbps BLE measured from VBAT with $P_{RF}=4$ dBm	10.6	mA
$I_{9dBm(DCDC, 3V)}$	TX current(DCDC,3V)1Mbps BLE measured from VBAT with $P_{RF}=9$ dBm	15.5	mA
$I_{0dBm(3V)}$	TX current(3V)1Mbps BLE measured from VBAT with $P_{RF}=0$ dBm	11.2	mA
$I_{9dBm(3V)}$	TX current(3V)1Mbps BLE measured from VBAT with $P_{RF}=9$ dBm	30.7	mA

3.3.3 Radio current consumption (Receiver)

Symbol	Description	Min	Typ.	Max.	Units
$I_{RX,1M,DCDC}$	RX only run current(DCDC,3V)1Mbps/1Mbps BLE		4.8		mA
$I_{RX,1M}$	RX only run current(LDO,3V)1Mbps/1Mbps BLE		10.3		mA
$I_{RX,2M,DCDC}$	RX only run current(DCDC,3V)2Mbps/2Mbps BLE		5.4		mA
$I_{RX,2M}$	RX only run current(LDO,3V)2Mbps/2Mbps BLE		11.6		mA
$I_{START,RX,1M,DCDC}$	RX start-up current(DCDC,3V)1Mbps/1Mbps BLE		3.7		mA
$I_{START,RX,1M}$	RX start-up current 1Mbps/1Mbps BLE		6.7		mA

3.3.4 Transmitter specification

Symbol	Description	Min	Typ.	Max	Units
P_{RF}	Maximum output power		8		dBm
P_{RFC}	RF power control range		28		dB
P_{RFCR}	RF power accuracy			±4	dB
$P_{RF1,1}$	1st adjacent channel transmit power 1 MHz(1 Mbps)		-23		dBc
$P_{RF2,1}$	2nd adjacent channel transmit power 2 MHz(1 Mbps)		-50		dBc
$P_{RF1,2}$	1st adjacent channel transmit power 2 MHz(2 Mbps)		-24		dBc
$P_{RF2,2}$	2nd adjacent channel transmit power 4 MHz(2 Mbps)		-50		dBc
E_{vm}	Error Vector Magnitude IEEE 802.15.4				%rms
$P_{harm2nd,IEEE\ 802.15.4}$	2 nd Harmonics in IEEE 802.15.4 mode				dBm
$P_{harm3rd,IEEE\ 802.15.4}$	3 rd Harmonics in IEEE 802.15.4				dBm

3.3.5 Receiver operation

Symbol	Description	Min	Typ.	Max.	Units
$P_{RX,MAX}$	Maximum received signal strength at <0.1% PER		0		dBm
$P_{SENS,IT,1M}$	Sensitivity, 1 Mbps nRF mode ¹		-93		dBm
$P_{SENS,IT,SP,1M,BLE}$	Sensitivity, 1 Mbps BLE ideal transmitter, <=37 bytes BER=1E-3 ²		-95		dBm
$P_{SENS,IT,LP,1M,BLE}$	Sensitivity, 1 Mbps BLE ideal transmitter, >=128 bytes BER=1E-4 ³		-95		dBm
$P_{SENS,IT,2M}$	Sensitivity, 2Mbps nRF mode ⁴		-89		dBm
$P_{SENS,IT,SP,2M,BLE}$	Sensitivity, 2 Mbps BLE ideal transmitter, Packet length <=37 bytes		-92		dBm
$P_{SENS,IT,BLE\ LE125K}$	Sensitivity, 125kbps BLE mode		-103		dBm
$P_{SENS,IT,BLE\ LE500K}$	Sensitivity, 500kbps BLE mode		-99		dBm
$P_{SENSE,IEEE\ 802.15.4}$	Sensitivity in IEEE 802.15.4 mode		-100		dBm

¹ Typical sensitivity applies when ADDR0 is used for receiver address correlation. When ADDR[1...7] are used for receiver address correlation, the typical sensitivity for this mode is degraded by 3dB.

² As defined in the Bluetooth Core Specification v4.0 Volume 6: Core System Package (Low Energy Controller Volume)

³ Equivalent BER limit < 10E-04

⁴ Desired signal level at PIN = -67 dBm. One interferer is used, having equal modulation as the desired signal. The input power of the interferer where the sensitivity equals BER = 0.1% is presented.

3.3.6 RX selectivity

Symbol	Description	Min	Typ.	Max.	Units
$C/I_{1M,co-channel}$	1Mbps mode, Co-Channel interference		9		dB
$C/I_{1M,-1MHz}$	1 Mbps mode, Adjacent (-1 MHz) interference		-2		dB
$C/I_{1M,+1MHz}$	1 Mbps mode, Adjacent (+1 MHz) interference		-10		dB
$C/I_{1M,-2MHz}$	1 Mbps mode, Adjacent (-2 MHz) interference		-19		dB
$C/I_{1M,+2MHz}$	1 Mbps mode, Adjacent (+2 MHz) interference		-42		dB
$C/I_{1M,-3MHz}$	1 Mbps mode, Adjacent (-3 MHz) interference		-38		dB
$C/I_{1M,+3MHz}$	1 Mbps mode, Adjacent (+3 MHz) interference		-48		dB
$C/I_{1M,\pm 6MHz}$	1 Mbps mode, Adjacent (≥ 6 MHz) interference		-50		dB
$C/I_{1MBLE,co-channel}$	1 Mbps BLE mode, Co-Channel interference		6		dB
$C/I_{1MBLE,-1MHz}$	1 Mbps BLE mode, Adjacent (-1 MHz) interference		-2		dB
$C/I_{1MBLE,+1MHz}$	1 Mbps BLE mode, Adjacent (+1 MHz) interference		-9		dB
$C/I_{1MBLE,-2MHz}$	1 Mbps BLE mode, Adjacent (-2 MHz) interference		-22		dB
$C/I_{1MBLE,+2MHz}$	1 Mbps BLE mode, Adjacent (+2 MHz) interference		-46		dB
$C/I_{1MBLE,>3MHz}$	1 Mbps BLE mode, Adjacent (≥ 3 MHz) interference		-50		dB
$C/I_{1MBLE,image}$	Image frequency Interference		-22		dB
$C/I_{1MBLE,image,1MHz}$	Adjacent (1 MHz) interference to in-band image frequency		-35		dB
$C/I_{2M,co-channel}$	2Mbps mode, Co-Channel interference		10		dB
$C/I_{2M,-2MHz}$	2 Mbps mode, Adjacent (-2 MHz) interference		6		dB
$C/I_{2M,+2MHz}$	2 Mbps mode, Adjacent (+2 MHz) interference		-19		dB
$C/I_{2M,-4MHz}$	2 Mbps mode, Adjacent (-4 MHz) interference		-20		dB
$C/I_{2M,+4MHz}$	2 Mbps mode, Adjacent (+4 MHz) interference		-44		dB
$C/I_{2M,-6MHz}$	2 Mbps mode, Adjacent (-6 MHz) interference		-42		dB
$C/I_{2M,+6MHz}$	2 Mbps mode, Adjacent (+6 MHz) interference		-42		dB
$C/I_{2M,\geq 12MHz}$	2 Mbps mode, Adjacent (≥ 12 MHz) interference		-52		dB

$C/I_{125k\text{ BLE LR,co-channel}}$	125 kbps BLE LR mode, Co-Channel interference	dB
$C/I_{125k\text{ BLE LR,-1MHz}}$	125 kbps BLE LR mode, Adjacent (-1 MHz) interference	dB
$C/I_{125k\text{ BLE LR,+1MHz}}$	125 kbps BLE LR mode, Adjacent (+1 MHz) interference	dB
$C/I_{125k\text{ BLE LR,-2MHz}}$	125 kbps BLE LR mode, Adjacent (-2 MHz) interference	dB
$C/I_{125k\text{ BLE LR,+2MHz}}$	125 kbps BLE LR mode, Adjacent (+2 MHz) interference	dB
$C/I_{125k\text{ BLE LR,>3MHz}}$	125 kbps BLE LR mode, Adjacent (≥ 3 MHz) interference	dB
$C/I_{125k\text{ BLE LR,image}}$	Image frequency Interference	dB
$C/I_{500k\text{ BLE LR,co-channel}}$	500 kbps BLE LR mode, Co-Channel interference	dB
$C/I_{500k\text{ BLE LR,-1MHz}}$	500 kbps BLE LR mode, Adjacent (-1 MHz) interference	dB
$C/I_{500k\text{ BLE LR,+1MHz}}$	500 kbps BLE LR mode, Adjacent (+1 MHz) interference	dB
$C/I_{500k\text{ BLE LR,-2MHz}}$	500 kbps BLE LR mode, Adjacent (-2 MHz) interference	dB
$C/I_{500k\text{ BLE LR,+2MHz}}$	500 kbps BLE LR mode, Adjacent (+2 MHz) interference	dB
$C/I_{500k\text{ BLE LR,>3MHz}}$	500 kbps BLE LR mode, Adjacent (≥ 3 MHz) interference	dB
$C/I_{500k\text{ BLE LR,image}}$	Image frequency Interference	dB

3.3.7 RX intermodulation

Symbol	Description	Min	Typ.	Max.	Units
$P_{\text{IMD,1M}}$	IMD performance, 1 Mbps, 3rd, 4th, and 5th offset channel		-29		dBm
$P_{\text{IMD,1M,BLE}}$	IMD performance, BLE 1 Mbps, 3rd, 4th, and 5th offset channel		-30		dBm
$P_{\text{IMD,2M}}$	IMD performance, 2 Mbps, 3rd, 4th, and 5th offset channel		-30		dBm

3.3.8 Radio timing

Symbol	Description	Min	Typ.	Max.	Units
t_{TXEN}	Time between TXEN task and READY event after channel FREQUENCY configured		140		us
$t_{TXEN,FAST}$	Time between TXEN task and READY event after channel FREQUENCY configured (Fast Mode)		40		us
$t_{TXDISABLE}$	Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 1Mbps		6		us
$t_{TXDISABLE,2M}$	Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Mbps				us
t_{RXEN}	Time between the RXEN task and READY event after channel FREQUENCY configured in default mode		140		us
$t_{RXEN,FAST}$	Time between the RXEN task and READY event after channel FREQUENCY configured in fast mode		40		us
$t_{RXDISABLE}$	Time between DISABLE task and DISABLED event when the radio was in RX		0		us
$t_{TXCHAIN}$	TX chain delay		0.6		us
$t_{RXCHAIN}$	RX chain delay		9.4		us
$t_{RXCHAIN,2M}$	RX chain delay in 2Mbps mode		5		us
$t_{RXCHAIN,LR\ 125k}$	RX chain delay in BLE LR125kbps mode				us
$t_{RXCHAIN,LR\ 500k}$	RX chain delay in BLE LR500kbps mode				us
$t_{RX-to-TX\ turnaround}$	Maximum TX-to-RX or RX-to-TX turnaround time in IEEE 802.15.4 mode				us

3.3.9 Received signal strength indicator (RSSI) specifications

Symbol	Description	Min	Typ.	Max.	Units
RSSI _{ACC}	RSSI Accuracy Valid range -90 to -20 dBm		-2		dB
RSSI _{RESOLUTION}	RSSI resolution		1		dB
RSSI _{PERIOD}	Sample period		8		us
RSSI _{min,IEEE 802.15.4}	Minimum RSSI sensitivity in 802.15.4 mode				dBm

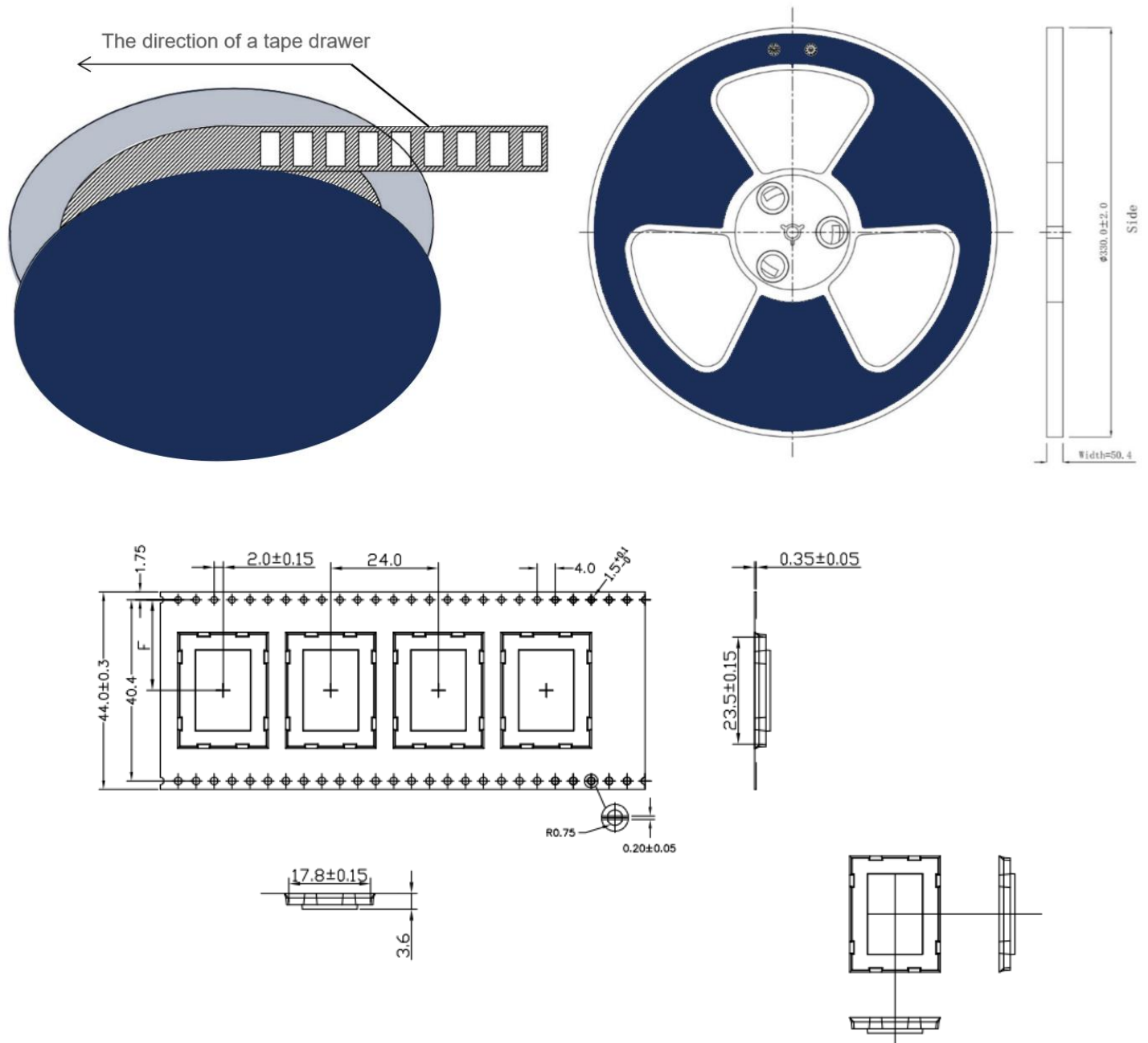
The schematic diagram illustrates the internal components and connections of the 52840 module. At the center is the 52840 microcontroller, with its pins connected to various external components. Power is supplied through VDD and GND pins, with decoupling capacitors (C1, C2, C3, C4, C5, C6, C7, C8, C9, C10, C11, C12, C13, C14, C15, C16, C17, C18, C19, C20, C21, C22, C23) used for stability. A voltage regulator (U1) is connected to the VDDH pin. The module also includes a DCDCEN pin, a VBUS pin, and a SWCLK pin. An optional NFC antenna is shown, consisting of a loop and two capacitors (Ctune1, Ctune2) connected to the P0.10 and P0.09 pins. The module is housed in a package with pins labeled P0.00 to P0.31, P1.00 to P1.31, and P2.00 to P2.31. A table at the bottom right provides configuration options for the module.

Config	Supply VDDH	VDD	DCDCEN1	R1	R2
1	USB(VDDH=VBUS)	N/A	Yes	NC	CONN
2	Battery/Ext.regulator	N/A	Yes	NC	NC
3	N/A	Battery/Ext.regulator	Yes	CON	NC

default config : 3
 USB can be used in config 1 or 2 for R2 is connected, and highly recommended to solder a 4.7 resistor on the external circuit of VDDH pin in series

5 Package Information

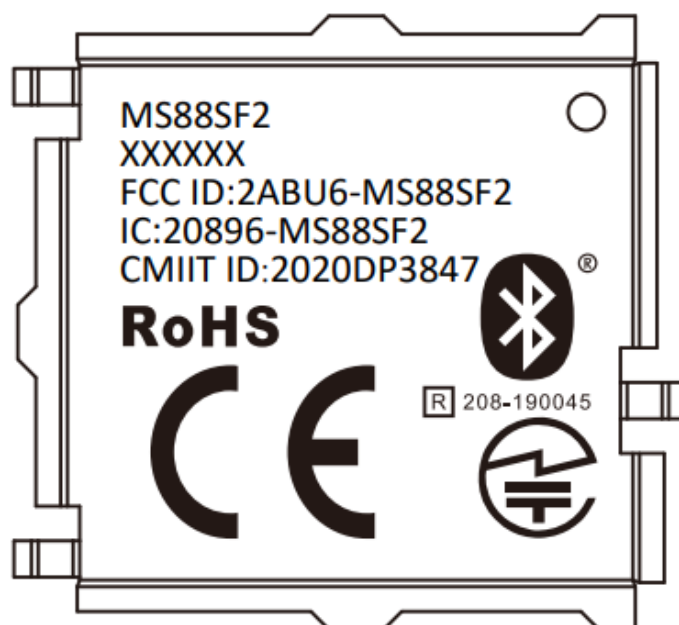
5.1 Package dimension



Details of Package Dimension:

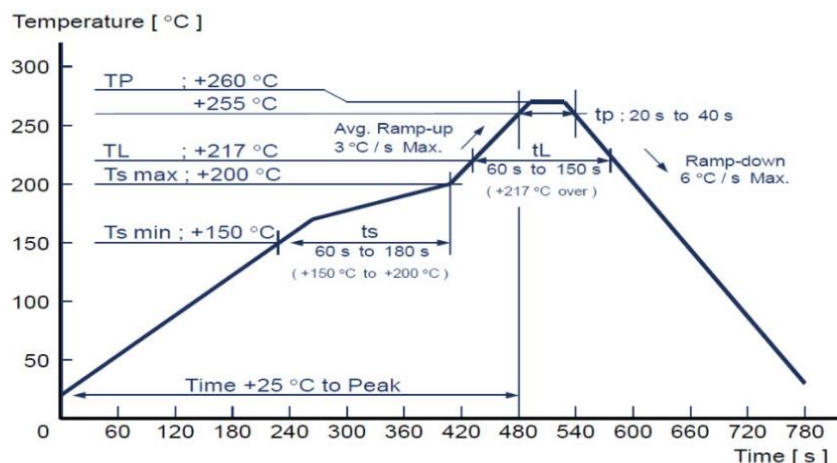
Details	Reel-MS88SF2
Quantity(module)	650PCS
Tape Weight	460g
Single module Weight	1.1g
Gross Weight	1200g
Dimension	W: 44mm T: 0.35mm

5.2 Mark on metal shield



6 Reflow and Soldering

Reflow condition (Follow of JEDEC STD-020D.01)



Profile Feature	Sn-Pb Assembly	Pb-Free Assembly
Solder Paste	Sn63/Pb37	Sn96.5/Ag3/Cu0.5
Preheat Temperature min (Tsmín)	100°C	150°C
Preheat Temperature max (Tsmáx)	150°C	200°C
Preheat Time (Tsmín to Tsmáx)(ts)	60-120 sec	60-120 sec
Average ramp-up rate (Tsmáx to Tp)	3°C/second max	3°C/second max
Liquidous Temperature (TL)	183°C	217°C
Time (tL) Maintained Above (TL)	60-90 sec	30-90 sec
Peak Temperature (Tp)	220-235°C	230-250°C
Average ramp-down rate (Tp to Tsmáx)	6°C/second max	6°C/second max
Time 25°C to peak temperature	6 minutes max	8 minutes max

Important:

- When SMT involves double-sided patch, it is recommended that the module surface be reflowed only once.
- For module SMT, it is recommended to make a partial stepped stencil with a thickness of 0.2mm, and the stencil hole should be extended by 0.8mm size.
- After opening the package, it should be stored in vacuum environment. Module should not be exposed to the air for a long time to prevent moisture and pad oxidation. If there is an interval of 7 to 30 days during SMT process, it is recommended to bake it with reel at 65-70 degrees for 24 hours before using for SMT again.

7 Certification

FCC ID: 2ABU6-MS88SF2

BQB QDID: 131802

TELEC: 208-190045

IC:20896-MS88SF2

CE,RCM,WPC,RoHS & Reach certified

7.1 CE Certification

MS88SF2 module is being tested and is expected to be compliant against the EU-Radio Equipment standards. OEM integrator should consult with qualified test house to verify all regulatory requirements have been met for their complete device.

7.2 FCC Certification

MS88SF2 module is being tested and is expected to be compliant against the Federal Communications Commission standards.

As for the OEM integration:

Only OEM integrator have right to intend this device under the following conditions:

Any other transmitter or antenna must not be co-located with the antenna and transmitter. The module shall be only used with the integral antenna(s) that has been originally tested and certified with this module.

As long as the two conditions below are met, further transmitter testing will not be required.

(1) This device may not cause harmful interference.

(2) This device must accept any interference received, including interference that may cause undesired operation.

However, the OEM integrator shall test their end-product for any additional compliance requirements with this module installed (for example, digital device emission, PC peripheral requirements, etc.).

If these conditions cannot be met (for example certain laptop configuration or co-location with another transmitter), then the FCC authorization for this module in combination with the host equipment is no longer considered valid and the final product shall not use the FCC ID of the module. In these circumstances, the OEM integrator shall be re-evaluating the end product (including the transmitter) and obtaining a separate FCC authorization.

The OEM shall be verifying end product compliance with FCC Part 15, sub-part B limits for unintentional radiators through an accredited test facility.

7.3 Full Bluetooth Declaration ID

Minewsemi technologies meet the Bluetooth specification maintained by Bluetooth SIG, and all the product produced by Minewsemi is properly tested and comply with the Bluetooth license agreements.

Minewsemi is one of the associate member of Bluetooth SIG. The requirement of listing products by companies is become the members of the SIG and also pay the listed fees for each product required.

Please Kindly check the below Link to know more info:

<https://www.bluetooth.com/develop-with-bluetooth/qualification-listing>.

The Minewsemi Bluetooth Low Energy modules based on Nordic Semiconductor SoCs are listed as a “End product”.

7.4 Canada (IC)

Minew’s modules have been certified for use in Canada under Industry Canada (IC) Radio Standards Specification (RSS) RSS-210 and RSS-Gen. Modular approval permits the installation of a module in a host device without the need to rectify the device.

As for the labeling & user Information Requirements, MS88SF2 is assigned the IC ID number: 20896-MS88SF2.

Labeling Requirements for the Host Device (from Section 3.2.1, RSS-Gen, Issue 3, December 2010): The host device shall be properly labeled to identify the module within the host device.

User Manual Notice for License-Exempt Radio Apparatus (from Section 7.1.3 RSS-Gen, Issue 3, December 2010): User manuals for license-exempt radio apparatus shall contain the following or equivalent notice in a conspicuous location in the user manual or alternatively on the device or both: This device complies with Industry Canada license exempt RSS standard(s). Operation is subject to the following two conditions:

this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Transmitter Antenna (from Section 7.1.2 RSS-Gen, Issue 3, December 2010): User manuals for transmitters shall display the following notice in a conspicuous location:

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

7.5 India(WPC)

The MS88SF2 has been tested to comply with the wireless planning & coordination (WPC) Wing of the Ministry of Communications standard. WPC created in 1952, is the National Radio Regulatory Authority responsible for Frequency Spectrum Management, including licensing and caters for the needs of all wireless users (Government and Private) in the country.

It exercises the statutory functions of the Central Government and issues licenses to establish, maintain and operate wireless stations. WPC is divided into major sections like Licensing and Regulation (LR), New Technology Group (NTG) and Standing Advisory Committee on Radio

Frequency Allocation (SACFA). SACFA makes the recommendations on major frequency allocation issues, formulation of the frequency allocation plan, making recommendations on the various issues related to International Telecom Union (ITU), to sort out problems referred to the committee by various wireless users, Siting clearance of all wireless installations in the country etc.

7.6 RCM

The MS88SF2 has been tested to comply with the AS/NZS 4268:2017, Radio equipment and systems – Short range devices – Limits and methods of measurement. It may be used as evidence in obtaining permission to use the Regulatory Compliance Mark (RCM).

Information on registration as a Responsible Party, license and labeling requirements may be found at the following websites:

Australia: <http://www.acma.gov.au/theACMA/radiocommunications-short-range-devices-standard-2004>.

New Zealand: <http://www.rsm.govt.nz/compliance>.

Only Australian-based and New Zealand-based companies who are registered may be granted permission to use the RCM. An Australian-based or New Zealand-based agent or importer may also register as a Responsible Party to use the RCM on behalf of a company not in Australia or New Zealand.

7.7 Japan (TELEC)

The MS88SF2 is approved for use in the Japanese market.

7.8 Environmental

7.8.1 RoHs

MS88SF2 modules are in compliance with Directive 2011/65/EU, 2015/863/EU of the European Parliament and the Council on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

7.8.2 Reach

MS88SF2 modules listed below do not contain the 191 SVHC (Substance of Very High Concern), as defined by Directive EC/1907/2006 Article according to REACH Annex XVII.

8 Notes & Cautions

We cannot assure that the specification has no errors and omission even though this specification is under collate and check strictly.

This specification is under the protection of laws and regulations of copyright, please do not copy and duplicate at any form, or do not transmit part or full of this specification in any wire and wireless network in any form, or do not edit or translate to any other format, word, code, etc.

8.1 Design notes

- It is critical to following the recommendations of this document to ensure the module meets the specifications.
- The module should be placed at the edge of the circuit board as far as possible to keep away from other circuits.
- Antenna should be kept away from other circuits. It can prevent low radiation efficiency and the normal use of other circuits from being affected.
- The landing of components should be appropriate and that is better for reducing the parasitic inductance.
- Please refuse to supply voltage that is not within the range of specification.
- Please make sure the module or its surface may not suffer from the physical shock or extreme stress.

8.2 Layout notes

To make sure wireless performance is at its best condition, please layout the MS88SF2 module on the carrier board as below instructions and picture.

a) Placement of the antenna

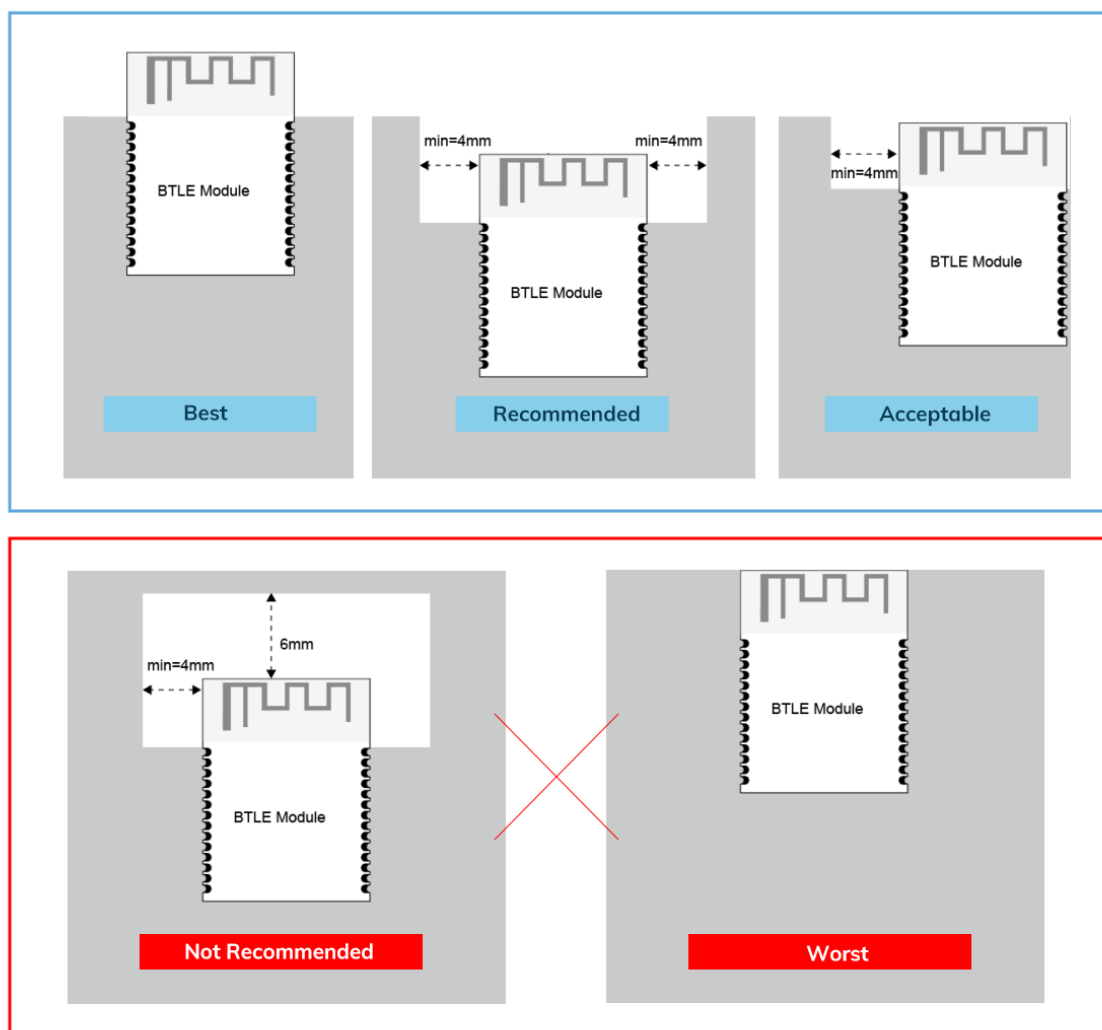
The antenna area of module shall lay clearance completely and should not be blocked by the metal. Otherwise it will have effect on antenna performance (As the picture indicated below).

b) Placement of top-layer

The placement of top-layer in carrier board shall be lay copper completely to reduce the signal line in carrier board or other interference.

c) Clearance

The upper and below area of antenna (including the case) shall have 4mm or more than 4mm clearance to reduce the influences for antenna.



*The Grey area above is Carrier board.

8.3 Installation and soldering

Please do not lay copper under the module antenna. It can prevent the influence of signal radiation and the transmission distance from being affected.

8.4 Handling and storage

- Due to the fact that CMOS components are included in the module, it is better to eliminate static electricity at any methods when transporting or working with the module. Moreover, it is strongly recommended adding anti-ESD components to circuit design to hinder damage from real-life ESD events. Anti-ESD methods can be also used in mechanical design.



- b) Please store the modules within -40°C to $+125^{\circ}\text{C}$ before and after installation and make sure the modules is away from the direct sunlight exposure for a long duration. Modules should be far away from humid and salty air conditions, and any corrosive gasses or substances.
- c) Please not to wash the module. No-Clean Paste is used in production. The metal shield may be oxidized by the washing process and may lead to chemistry reaction with No-Clean Paste. If modules goes through the washing process, functions of the module may not guaranteed.
- d) After opening the package, it should be stored in vacuum environment. Module should not be exposed to the air for a long time to prevent moisture and pad oxidation. If there is an interval of 7 to 30 days during SMT process,

8.5 Life support applications

- a) The module is not design for life support device or system and not allowed to be used in destructive devices or system in any direct, or indirect ways. Minewsemi is not responsible for compensation of any losses when applying modules under such application as described above.
- b) MinewSemi shall not responsible for the customer's products or application.

9 Disclaimer

The factory has passed the ISO9001 quality management system, ISO14001 environmental management system and OAHS18001 occupational health and safety assessment . Each product has been rigorously tested (transmission power test, sensitivity test, power consumption test, stability test, aging test, etc.).

* NOTICES:

- The Bluetooth trade mark is owned by the Bluetooth SIG Inc. USA.
- All other trademarks listed herein are owned by their respective owners.
- All specifications are subject to change without notice.
- Please do not use this specification for produce, sell or illegal purpose without MinewSemi's authorization.
- MinewSemi have right to interpret all the items above.

Revision History

Version	Date	Notes	Contributor (s)	Person of Approve
1.0	2022-03-01	First edition	Eddie	Coral

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MINEWSEMI

Tel: 0086-755-2103 8160

Email: minewsemi@minew.com

URL: <https://www.minew.com/>

Address: 3rd Floor, Building I, Gangzhilong Science Park, Qinglong Road Longhua District, Shenzhen 518109, China

