



N-Channel 30-V (D-S) MOSFET

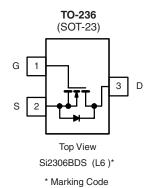
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
30	0.047 at $V_{GS} = 10 \text{ V}$	4.0	3.0		
	0.065 at V _{GS} = 4.5 V	3.5	3.0		

FEATURES

- Halogen-free Option Available
- TrenchFET® Power MOSFET
- 100 % R_g Tested







Ordering Information: Si2306BDS-T1-E3 (Lead (Pb)-free)

Si2306BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30		V	
Gate-Source Voltage		V _{GS}	± 20			
Continuous Brain Comment /T 150 °C\2 b	T _A = 25 °C	I _D	4.0	3.16		
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		3.5	2.7		
Pulsed Drain Current		I _{DM}	20		Α	
Continuous Source Current (Diode Conduction) ^{a, b}	I _S	1.04	0.62			
	T _A = 25 °C	P _D	1.25	0.75	w	
Maximum Power Dissipation ^{a, b}	T _A = 70 °C		0.8	0.48		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 t	o 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manifestor Localitan to Applicant	t ≤ 5 s	- R _{thJA}	80	100	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		130	166		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75		

Notes:

- a. Surface Mounted on FR4 board, $t \le 5 \text{ s.}$
- b. Pulse width limited by maximum junction temperature.
- c. Surface Mounted on FR4 board.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm

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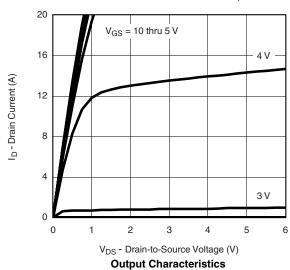


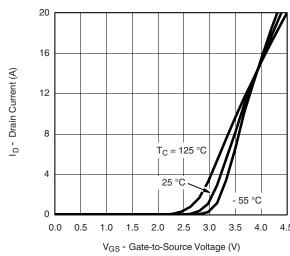
			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.0		3.0		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zoro Coto Voltago Droin Current	l	V _{DS} = 30 V, V _{GS} = 0 V			0.5	μΑ	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 4.5 \text{ V}, V_{GS} = 10 \text{ V}$	6			Α	
Durin Course On Braintana		$V_{GS} = 10 \text{ V}, I_D = 3.5 \text{ A}$		0.038	0.047	Ω	
Drain-Source On-Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 2.8 \text{ A}$	V _{GS} = 4.5 V, I _D = 2.8 A 0.052 0.0		0.065	7 22	
Forward Transconductance ^a	g _{fs}	$V_{DS} = 4.5 \text{ V}, I_{D} = 2.5 \text{ A}$		7.0		S	
Diode Forward Voltage	V_{SD}	I _S = 1.25 A, V _{GS} = 0 V		0.8	1.2	V	
Dynamic							
Gate Charge	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_D = 2.5 \text{ A}$		3.0	4.5		
Total Gate Charge	Q _{gt}			6	9	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 2.5 \text{ A}$		1.6		110	
Gate-Drain Charge	Q_{gd}			0.6			
Gate Resistance	R_{g}	f = 1.0 MHz	2.5	5	7.5	Ω	
Input Capacitance	C _{iss}			305			
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		65		pF	
Reverse Transfer Capacitance	C _{rss}			29			
Switching							
Turn-On Delay Time	t _{d(on)}			7	11		
Rise Time	t _r	V_{DD} = 15 V, R_L = 15 Ω		12	18	ns	
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ 1 A, V_{GEN} = 10 V, R_g = 6 Ω		14	25		
Fall Time	ì,			6	10		
Reverse Recovery Time	t _{rr}	I _F = 1.25 A, di/dt = 100 A/μs		14	21		
Body Diode Reverse Recovery Charge	Q _{rr}	i _F = 1.25 A, αί/αι = 100 A/μS		6	10	nC	

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Transfer Characteristics

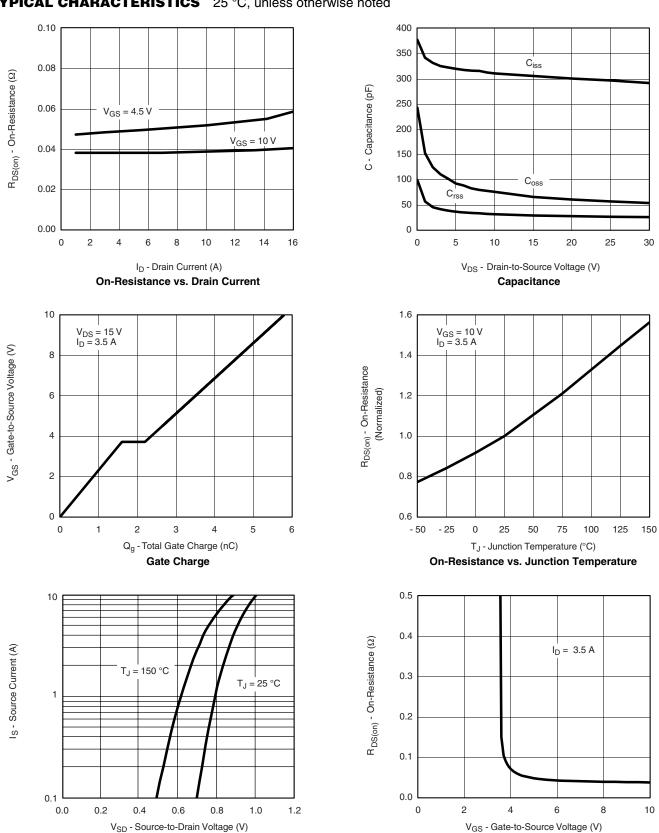
a. Pulse test: Pulse width \leq 300 μ s, duty cycle \leq 2 %.







TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



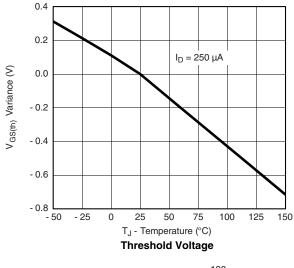
Source-Drain Diode Forward Voltage

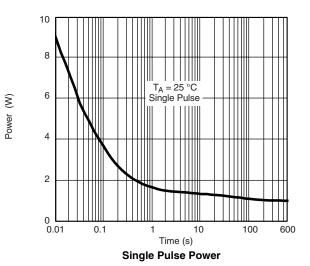
On-Resistance vs. Gate-to-Source Voltage

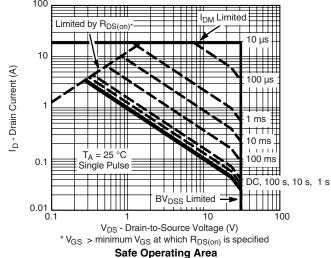
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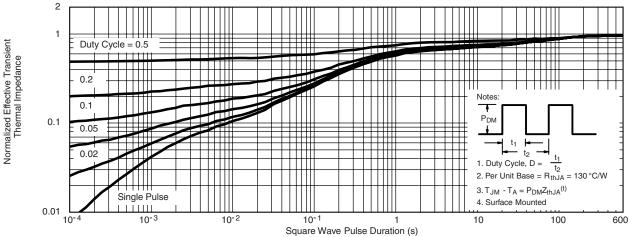
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted









Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?73234.

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SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES			
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.037	0.0374 Ref		
e ₁	1.90 BSC		0.074	0748 Ref		
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025	5 Ref		
S	0.50 Ref		0.020) Ref		
q	3°	8°	3°	8°		
FCN: S-03946-Rev K 09-	lul-01	•				

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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