

N-Channel Enhancement Mode Power MOSFET

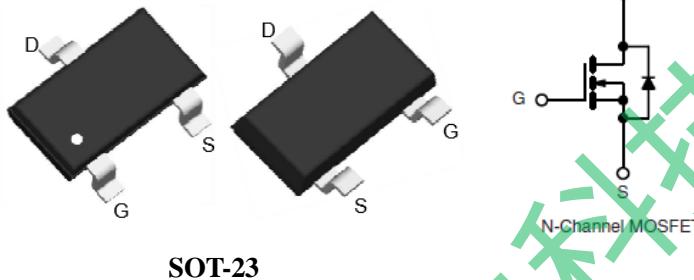
- Features**

$V_{DS} = 60V$,
 $I_D = 3A$
 $R_{DS(ON)} @ V_{GS} = 10V$, TYP 82mΩ
 $R_{DS(ON)} @ V_{GS} = 4.5V$, TYP 102mΩ

- General Description**

- Load Switch
- DC/DC Converters

- Pin Configurations**



- Absolute Maximum Ratings** @ $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DSS}	60	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current (Continuous) *AC	I_D	3	A
$T_A=70^\circ C$		2.4	
Drain Current (Pulse) *B	I_{DM}	12	A
Power Dissipation	P_D	1.25	W
Operating Temperature/ Storage Temperature	T_J/T_{STG}	-55~150	°C

- Thermal Resistance Ratings**

Parameter	Symbol	Maximum	Unit
Maximum Junction-to-Ambient	$t \leq 5s$	R_{thJA}	100 °C/W



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● Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$	60	--	--	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 48\text{V}, V_{GS} = 0\text{V}$	--	--	1	μA
Gate Threshold Voltage	$V_{GS(\text{TH})}$	$V_{GS} = V_{DS}, I_{DS} = 250\mu\text{A}$	1	1.7	2.5	V
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	--	--	± 100	nA
Drain-Source On-state Resistance	$R_{DS(\text{on})}$	$V_{GS} = 10\text{V}, I_D = 3\text{A}$	--	82	105	$\text{m}\Omega$
	$R_{DS(\text{on})}$	$V_{GS} = 4.5\text{V}, I_D = 5\text{A}$	--	102	125	$\text{m}\Omega$
Diode Forward Voltage	V_{SD}	$I_{SD} = 1\text{A}, V_{GS} = 0\text{V}$	--	0.75	1.2	V
Diode Forward Current	I_S	$T_A = 25^\circ\text{C}$	--	--	1.7	A
Switching						
Total Gate Charge	Q_g	$V_{GS} = 10\text{V}, V_{DS} = 30\text{V}, I_D = 3\text{A}$ $V_{DD} = 30\text{V}, R_G = 6\Omega$ $I_D = 1\text{A}, V_{GS} = 10\text{V}$	--	13	--	nC
Gate-Source Charge	Q_{gs}		--	1	--	nC
Gate-Drain Charge	Q_{gd}		--	4	--	nC
Turn-on Delay Time	$t_{d(on)}$		--	11	--	ns
Turn-on Rise Time	t_r		--	5	--	ns
Turn-off Delay Time	$t_{d(off)}$		--	26	--	ns
Turn-Off Fall Time	t_f		--	4	--	ns
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = 25\text{V}, f = 1.0\text{MHz}$	--	560	--	pF
Output Capacitance	C_{oss}		--	70	--	pF
Reverse Transfer Capacitance	C_{rss}		--	40	--	pF

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The value in any given application depends on the user's specific board design.

B: Repetitive rating, pulse width limited by junction temperature.

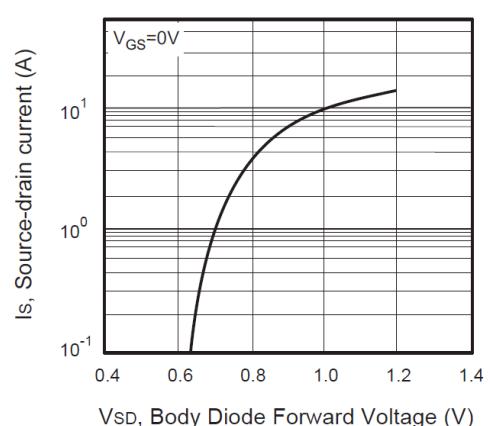
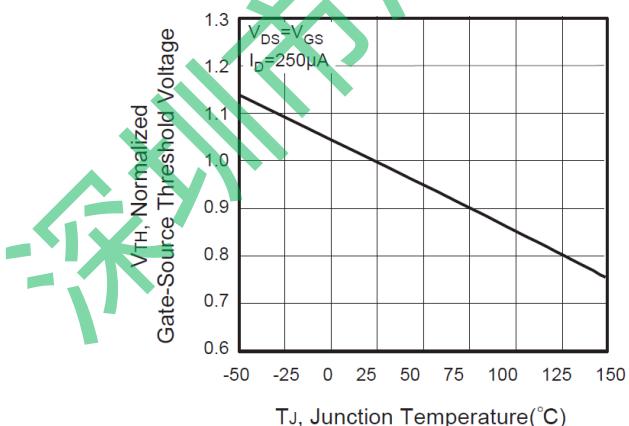
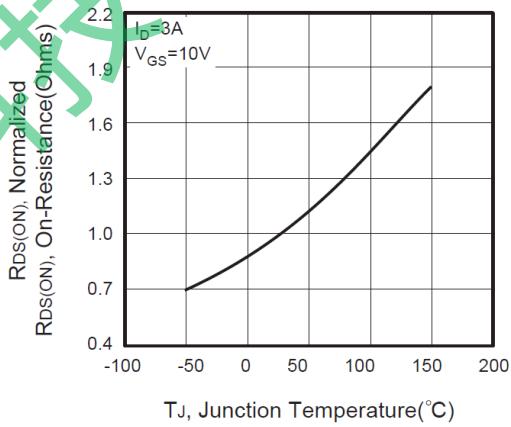
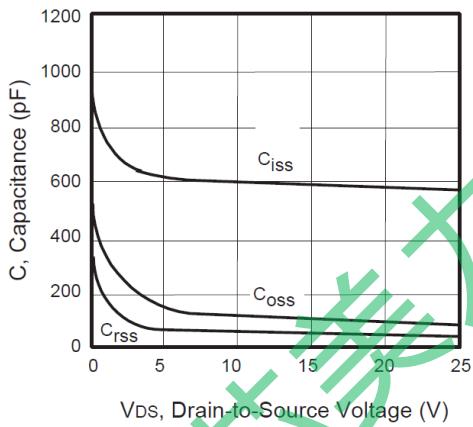
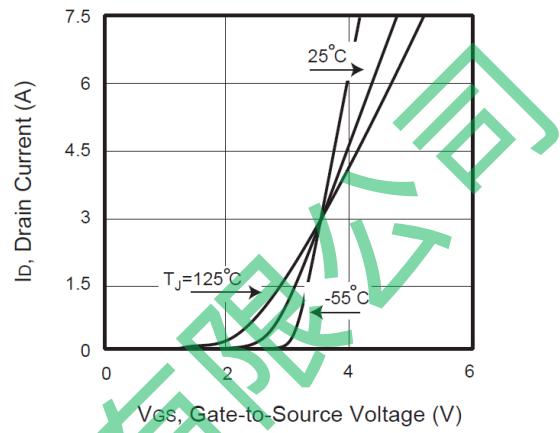
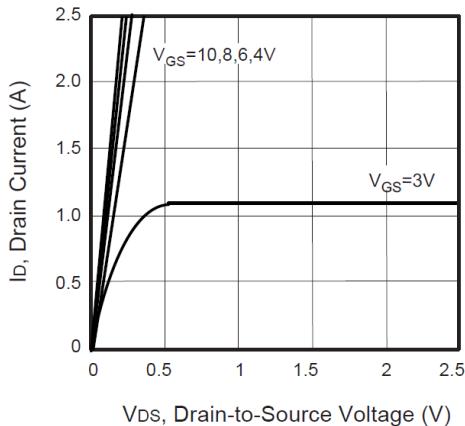
C: The current rating is based on the t_{S10} junction to ambient thermal resistance rating.



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- Typical Performance Characteristics (($T_J = 25^\circ\text{C}$, unless otherwise noted))





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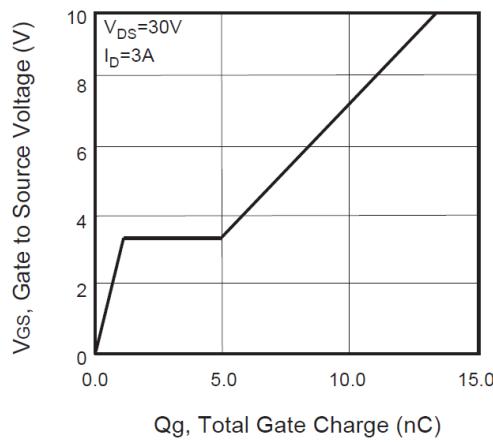


Figure 7. Gate Charge

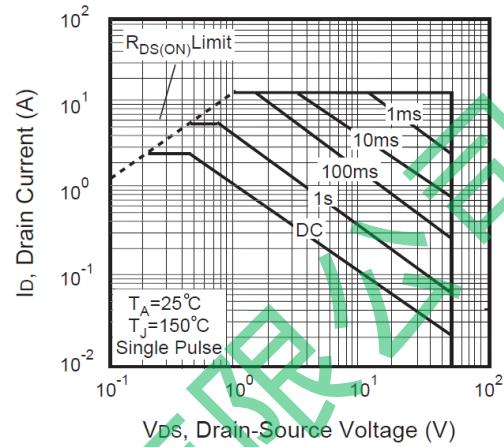


Figure 8. Maximum Safe Operating Area

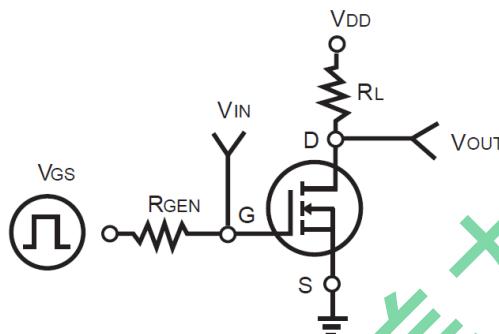


Figure 9. Switching Test Circuit

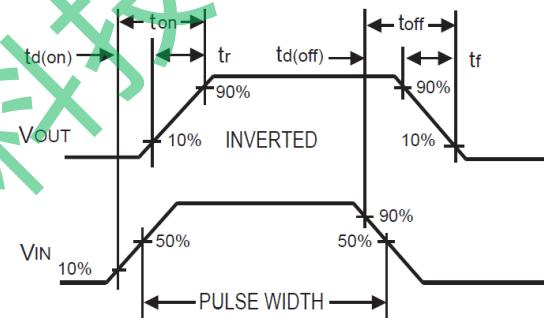


Figure 10. Switching Waveforms

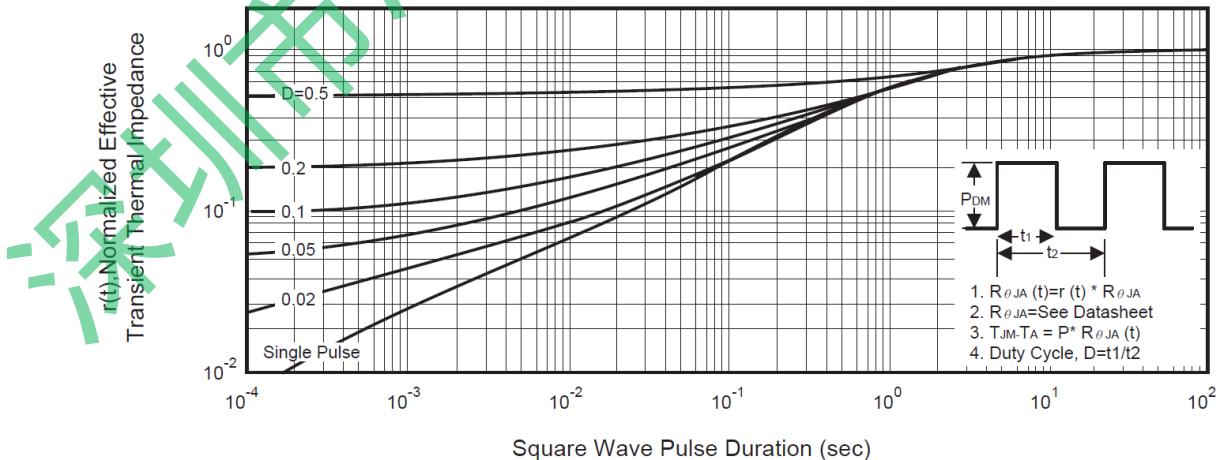


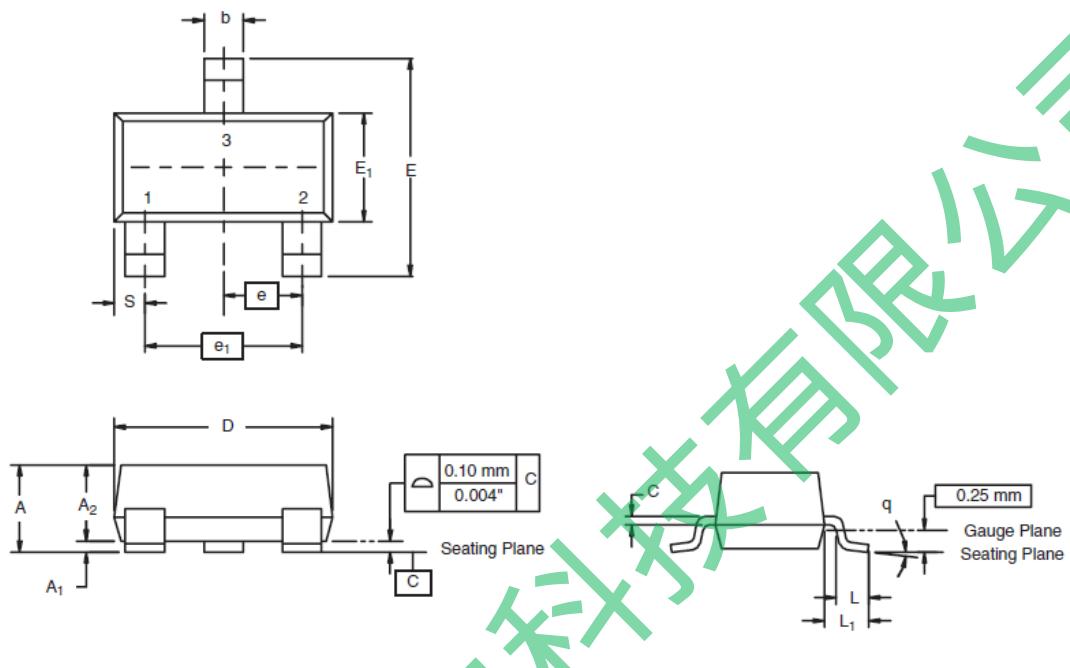
Figure 11. Normalized Thermal Transient Impedance Curve



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● Package Information



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.89	1.12	0.035	0.044
A ₁	0.01	0.10	0.0004	0.004
A ₂	0.88	1.02	0.0346	0.040
b	0.35	0.50	0.014	0.020
c	0.085	0.18	0.003	0.007
D	2.80	3.04	0.110	0.120
E	2.10	2.64	0.083	0.104
E ₁	1.20	1.40	0.047	0.055
e	0.95 BSC		0.0374 Ref	
e ₁	1.90 BSC		0.0748 Ref	
L	0.40	0.60	0.016	0.024
L ₁	0.64 Ref		0.025 Ref	
S	0.50 Ref		0.020 Ref	
q	3°	8°	3°	8°

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