SPECIFICATIONS

Customer	
Product Name	Wire Wound SMD Power Inductor
Sunlord Part Number	SWPA4030S331MT
Customer Part Number	

[New Released, Revised]

SPEC No.: SWPA1002220020

[This SPEC is total 12 pages.] [ROHS Compliant Parts]



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For Customer approval ualification Status:	Only】] Full Restricte	Date: d Rejected	
Approved By	Verified By	Re-checked By	Checked By
mments:			

[Version change history]

Rev.	Effective Date	Changed Contents	Change Reasons	Approved By
01	Jun.09,2022	New released	/	Qintian Hou

Caution

All products listed in this specification are developed, designed and intended for use in general electronics equipment. The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require especially high reliability, or whose failure, malfunction or trouble might directly cause damage to society, person, or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below. Please contact us for more details if you intend to use our products in the following applications.

- 1. Aircraft equipment
- 2. Aerospace equipment
- 3. Undersea equipment
- 4. nuclear control equipment
- 5. military equipment
- 6. Power plant equipment
- 7. Medical equipment
- 8. Transportation equipment (automobiles, trains, ships, etc.)
- 9. Traffic signal equipment
- 10. Disaster prevention / crime prevention equipment
- 11. Data-processing equipment
- 12. Applications of similar complexity or with reliability requirements comparable to the applications listed in the above

Τ

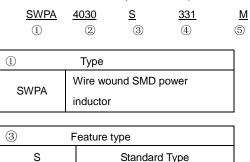
6)

1 Scope

2

This specification applies to the SWPA4030S331MT of wire wound SMD power inductor.

- Product Description and Identification (Part Number)
 - 1) Description: SWPA4030S331MT of Wire wound SMD power inductor.
 - 2) Product Identification (Part Number)



5	Inductance Tolerance						
	М		±20%				



② External Dimensions(L×W×H) [mm]					
	4030		4.0X4.0X 3.0		

4	Nominal	Inductance				
Example		Example				
331		330uH				

3. Shape and Dimensions

Dimensions and recommended PCB pattern for reflow soldering, please see Fig.3-1, Fig. 3-2 and Table 3-1.

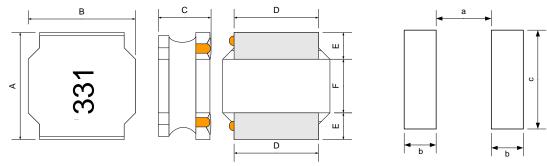


Fig.3-1

Series	А	В	С	D	E	F	а	b	С
SWPA4030S	4.0±0.2	4.0±0.2	3.0Max.	3.3±0.2	0.95±0.2	2.1±0.2	1.9Тур.	1.1Тур.	3.7Тур.
			↓ ↓	Fig.3-	2]			

Δf: Clearance between terminal and the surface of plate must be 0.1mm max when coil is placed on a flat plate.

4. Electrical Characteristics

Please refer to Item 6.

- 1) Operating and storage temperature range (individual chip without packing): -40°C ~ +125°C (Including Self-heating)
- 2) Storage temperature range (packaging conditions): -10 $^\circ\!\mathrm{C}$ -+40 $^\circ\!\mathrm{C}$ and RH 70% (Max.)

5. Test and Measurement Procedures

5.1 Test Conditions

5.1.1 Unless otherwise specified, the standard atmospheric conditions for measurement/test as:

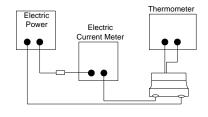
- a. Ambient Temperature: 20±15℃
- b. Relative Humidity: 65±20%
- c. Air Pressure: 86kPa to 106kPa
- 5.1.2 If any doubt on the results, measurements/tests should be made within the following limits:
 - a. Ambient Temperature: $20\pm2^{\circ}C$
 - b. Relative Humidity: 65±5%
 - c. Air Pressure: 86kPa to 106kPa

5.2 Visual Examination

Inspection Equipment: Visual.

5.3 Electrical Test

- 5.3.1 Inductance (L)
 - a. Refer to Item 6.Test equipment: WK3260B LCR meter or equivalent.
 - b. Test Frequency and Voltage: refers to Item 6.
- 5.3.2 Direct Current Resistance (DCR)
 - a. Refer to Item 6.
 - b. Test equipment: HIOKI 3540 or equivalent.
- 5.3.3 Saturation Current (Isat)
 - a. Refer to Item 6.
 - b. Test equipment: WK3260B LCR meter or equivalent.
- 5.3.4 Temperature rise current (Irms)
 - a. Refer to Item 6.
 - b. Test equipment (see Fig. 5.3.4-1, Fig. 5.3.4-2): Electric Power, Electric current meter, Thermometer.
 - c. Measurement method
 - 1. Set test current to be 0 mA.
 - 2. Measure initial temperature of choke surface.
 - 3. Gradually increase current and measure choke temperature for corresponding current.
 - 4. Definition of Temperature rise current: DC current that causes the temperature rise (Δ T) from ambient temperature



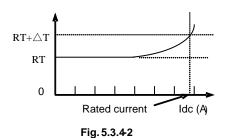


Fig. 5.3.4-1

5.3.5 Self-resonant frequency(SRF)

a. Refer to Item 6.

b.Test equipment: Agilent E4991A+16197or equivalent

6. Electrical Characteristics

 stomer	Part Number	Inductance Min. Self-resonant		DC Resistance		Saturation Current		Heat Rating Current		
P/N		0.1MHz/1V	frequency	Max.	Тур.	Max.	Тур.	Max.	Тур.	Marking
	Units	μH	MHz	Ω	Ω	А	А	А	А	
	Symbol	L	SRF	DC	DCR		at	Irms		-
	SWPA4030S331MT	330±20%	2.7	5.20	4.00	0.30	0.40	0.25	0.26	331

Note: **1 : Rated current: Isat (max.) or Irms (max.), whichever is smaller;

*2 : Saturation Current: Max. Value, DC current at which the inductance drops less than 30% from its value without current;

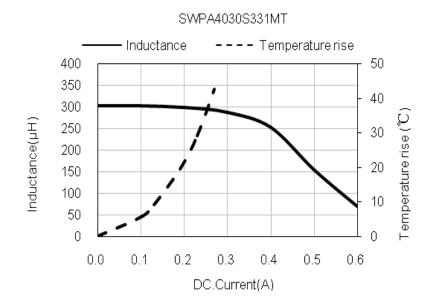
Typ. Value, DC current at which the inductance drops 30% from its value without current;

 $\times 3$: Irms: DC current that causes the temperature rise (ΔT) from 20°C ambient.

For Max. Value, $\Delta T \le 40^{\circ}$ C; for Typ. Value, ΔT is approximate 40° C.

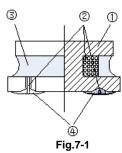
The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

Typical Electrical Characteristics:



7. Structure

The structure of SWPA4030S product, please refer to Fig.7-1 and Table 7-1.



No.	Components	Material
1	Ferrite Core	Ni-Zn Ferrite
2	Wire	Polyurethane system enameled copper wire
3	Magnetic Glue	Epoxy resin and magnetic powder
4	Electrodes	Sn Alloy

[Table 7-1]

8. Product Marking

Please refer to Fig. 8-1.

The content of marking please refers to Item 6.





Items	Requirements	Test Methods and Remarks
9.1 Terminal Strength	No removal or split of the termination or other defects shall occur.	 Solder the inductor to the testing jig (glass epoxy board shown in Fig.9.1-1) using eutectic solder. Then apply a force in the direction of the arrow. 10N force. Keep time: 5s
9.2 Resistance to Flexure	Fig.9.1-1 No visible mechanical damage.	 Solder the chip to the test jig (glass epoxy board) using eutectic solder. Then apply a force in the direction shown as Fig.9.2-1. Flexure: 2mm Pressurizing Speed: 0.5mm/sec Keep time: 30±1s Test board size: 100X40X1.0 Land dimension: Please see Fig.3-1
9.3 Vibration	Fig.9.2-1 ① No visible mechanical damage. ② Inductance change: Within ±10%	 Solder the chip to the testing jig (glass epoxy board shown as the following figure) using eutectic solder. The chip shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz. The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).
9.4 Temperature coefficient	Inductance change: Within ±20%	 Temperature: -40°C~+125°C With a reference value of +20°C, change rate shall be calculated
9.5 Solder ability	90% or more of electrode area shall be coated by new solder.	 The test samples shall be dipped in flux, and then immersed in molten solder. Solder temperature: 245±5°C Duration: 5±1 sec. Solder: Sn/3.0Ag/0.5Cu Flux: 25% resin and 75% ethanol in weight Immersion depth: all sides of mounting terminal shall be immersed
9.6 Resistance to Soldering Heat	 No visible mechanical damage. Inductance change: Within ±10% 	 Re-flowing Profile: Please refer to Fig. 9.6-1. Test board thickness: 1.0mm Test board material: glass epoxy resin The chip shall be stabilized at normal condition for 1~2 hours before measuring 260°C Peak.260°C max. Fig. 9.6-1

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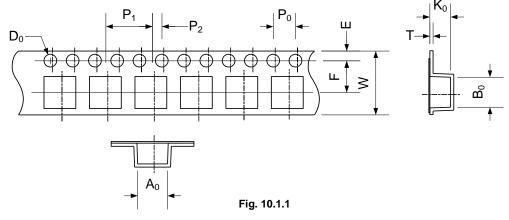
9.7	① No visible mechanical damage.	1 Temperature and time: -40±3°C for 30±3 min \rightarrow 125°C
Thermal Shock	(2) Inductance change: Within $\pm 10\%$ $125^{\circ}C$ 30 min. 30 min. Ambient 30 min. $-40^{\circ}C$ 30 min. 20 sec. (max.) Fig.9.7-1	 for 30±3min, please refer to Fig. 9.7-1. 2 Transforming interval: Max. 20 sec 3 Tested cycle: 100 cycles 4 The chip shall be stabilized at normal condition for 1~2 hours before measuring
9.8 Resistance to Low Temperature	 No visible mechanical damage Inductance change: Within ±10% 	 Temperature: -40±3°C Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring
9.9 Resistance to High Temperature	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 125±2℃ Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring.
9.10 Damp Heat	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 60±2°C Humidity: 90% to 95%RH Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring
9.11 Loading Under Damp Heat	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 60±2°C Humidity: 90% to 95% RH Applied current: Rated current Duration:1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring
9.12 Loading at High Temperature	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 85±2℃ Applied current: Rated current Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring

10. Packaging, Storage and Transportation

10.1 Tape and Reel Packaging Dimensions

10.1.1 **T**aping Dimensions (Unit: mm)

Please refer to Fig. 10.1.1 and Table 10.1.1.



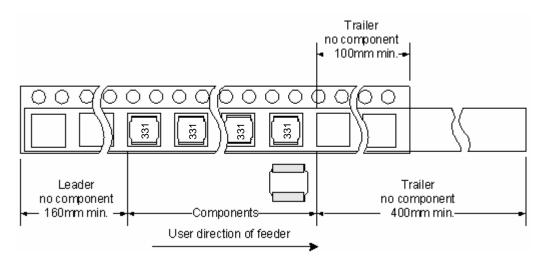


Series	A ₀	B ₀	W	E	F	P ₀	P ₁	P ₂	D ₀	Т	K ₀
SWPA4030S	4.3±0.1	4.3±0.1	12.0±0.3	1.75±0.1	5.5±0.1	4.0±0.1	8.0±0.1	2.0±0.1	1.5+0.1/-0.0	0.4±0.03	3.2±0.1

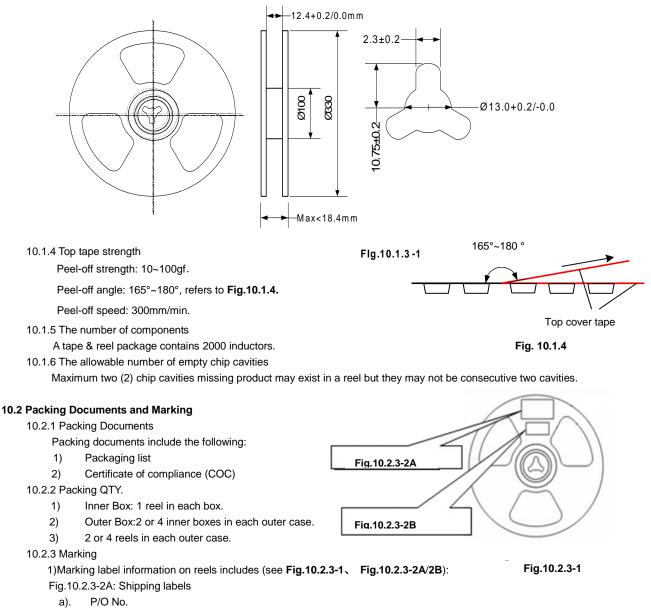
10.1.2 Direction of rolling

Please refer to Fig. 10.1.2





10.1.3 Reel Dimensions (Unit: mm) Please refer to **Fig. 10.1.3**.



- b). Customer Part No.
- c). Sunlord Part No.
- d). Quantity..
- e). Lot No.
- f). Date code

- g). Inspection stamp
- h). MFG address as 'Made In China'.
- Fig.10.2.3-2B: Production labels
- a). P/O No.
- b). Quantity..
- c). Lot No.
- d). Inspe No
- e). Inspection stamp
- f). MFG address as 'Made In China'.
- g). sequence number

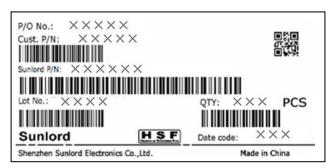
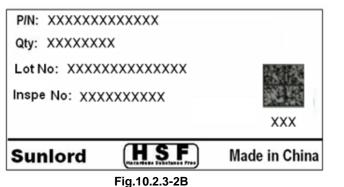


Fig.10.2.3-2A



Inner box please refers to Fig.10.2.3-3 and Table 10.2.3-1

1)Marking label information on inner box

Marking Label on inner box

Manufacturer: Sunlord ID:

Customer

Date code

C/No.

P/O No.

Quantity.

Customer Part No.

Sunlord Part No.

Inspection Stamp.

Of total 10 cases

Manufacturer

2)Marking on outer case (see Fig.10.2.3-4~6): Out case size pleases reefers to Table 10.2.3-2.

> "Shenzhen Sunlord Electronics Co., Ltd." Packing label include the following:

Example; "1/10" means that this case is the 1st one

ΟA

PASS

HSI

MADE IN CHINA

a). b).

a).

b).

i)

ii)

iii)

iv)

V)

vi) vii)

viii)

ix)

Sunlord Part No: XXXXXXXXXXXX

SHENZHEN SUNLORD ELECTRONICS CO., LTD.

P/O No: XXXXXXXX

Cust Part No: XXXXXXXX

Quantity: XXXXXXXXX

Sunlord

N/A

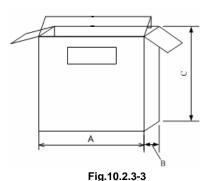
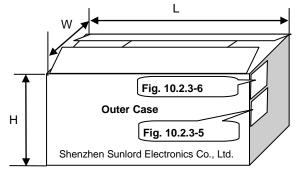


Fig. 10.2.3-3

	[Table 10.2.3-1]					
Packaging type	A(mm)	B(mm)	C(mm)			
Inner box	340	30	340			

[Table 10.2.3-2]

Packaging type	L(mm)	W(mm)	H(mm)	
Type1	380	380	250	
Type2	380	380	190	





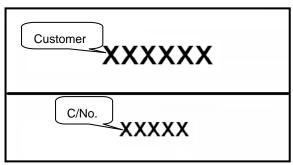


Fig.10.2.3-5

Data code: XXXXXXXX



11. Visual inspection standard of product						
File No:		o Wire Wound SMD Power Inductor Series				
Effective date:		Applied to	REV:01			
No.	Defect Item	Graphic Rejection identification		Acceptance		
1	Core defect		The defect length/width (I or <i>w</i>) more than L/6 or W/6, NG.	AQL=0.65		
2	Core crack		Visual cracks, NG.	AQL=0.65		
3	Starvation		 Resin starved length, <i>I</i>, more than L/2, NG. IF <i>W</i>>2mm, resin starved width, <i>w</i>, more than W/2, NG. IF <i>W</i>≤2mm, resin starved width, <i>w</i>, don't control. 	AQL=0.65		
4	Excessive glue		The length, width or height of product beyond specified value, NG.	AQL=0.65		
5	Cold solder		Cold solders I more than 1mm, NG.	AQL=0.65		
6	Solder icicle		 The height <i>H</i> of product beyond specified value, NG; The clearance <i>Δf</i> beyond specified value listed in Item 5, NG; 	AQL=0.65		
7	Electrode uneven	Δf	The clearance Δf beyond specified value listed in Item 5 , NG;	AQL=0.65		

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12. Recommended Soldering Technologies

- 12.1Re-flowing Profile:
- \triangle Preheat condition: 150 ~200 °C/60~120sec.
- \triangle Allowed time above 217°C: 60~90sec.
- △ Max temp: 260°C
- \triangle Max time at max temp: 5sec.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- △ Allowed Reflow time: 2x max Please refer to Fig. 12.1-1.

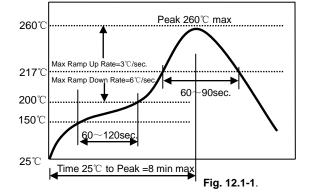
[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]

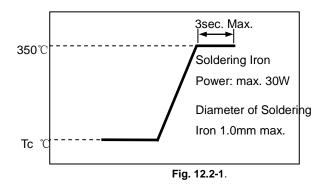
12.2 Iron Soldering Profile:

- △ Iron soldering power: Max. 30W
- \triangle Pre-heating: 150°C/60sec.
- \triangle Soldering Tip temperature: 350 °C Max.
- △ Soldering time: 3sec. Max.
- △ Solder paste: Sn/3.0Ag/0.5Cu
- \triangle Max.1 times for iron soldering

Please refer to Fig. 12.2-1.

[Note: Take care not to apply the tip of the soldering iron to the terminal electrodes.]





13. Precautions

13.1 Surface mounting

- Mounting and soldering condition should be checked beforehand.
- Applicable soldering process to this product is reflow soldering only.
- Recommended conditions for repair by soldering iron:
- Preheat the circuit board with product to repair at 150°C for about 1 minute.
- Put soldering iron on the land-pattern.
 - Soldering iron's temperature: 350 °C maximum/Duration: 3 seconds maximum/1 time for each terminal.
 - The soldering iron should not directly touch the inductor.
 - Product once removes from the circuit board may not be used again.

13.2 Handing

- Keep the products away from all magnets and magnetic objects.
- Be careful not to subject the products to excessive mechanical shocks.
- Please avoid applying impact to the products after mounted on pc board.
- Avoid ultrasonic cleaning.

13.3 Storage

- To maintain the solderability of terminal electrodes and to keep the packing material in good condition, temperature and humidity in the storage area should be controlled.
- Recommended conditions: -10°C~40°C, 70%RH (Max.)
- Even under ideal storage conditions, solderability of products electrodes may decrease as time passes. For this reason, product should be used with one year from the time of delivery.
- In case of storage over 6 months, solderability shall be checked before actual usage.

13.4 Regarding Regulations

- Any Class- I or Class- II ozone-depleting substance (ODS) listed in the Clean Air Act in US for regulation is not included in the products or applied to the products at any stage of whose manufacturing processes.
- Certain brominated flame retardants (PBBs,PBDEs) are not used at all.
- The products of this specification are not subject to the Export Trade Control Order in China or the Export Administration

Regulations in US.

13.5 Guarantee

The guaranteed operating conditions of the products are in accordance with the conditions specified in this specification.

Please note that Sunlord takes no responsibility for any failure and/or abnormality which is caused by use under other than the
aforesaid operating conditions.