SPECIFIC ATIONS

Customer	
Product Name	Wire Wound SMD Power Inductors
Volume Part Number	VEPD1770 Series
Customer Part Number	L052

[New Released, Revised]

SPEC No: VEPD1770-330M-A

[This SPEC is total 8 pages.] [ROHS Compliant Parts] PART NO: **FIA01770-01**

Approved By	Checked By	Issued By

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[For Customer approval Only]	Date:
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□Restricted

Qualification Status:

□Rejected

Verified By	Re-checked By	Checked By
	Verified By	Verified By Re-checked By

Comments:



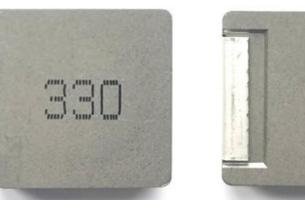
[Version change history]							
Rev.	Effective Date	Changed Contents	Change Reasons	Approved By			
01	/	New released	/	Luyong Han			

Applications:

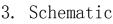
- •Laptops and PCs.
- $\bullet Switchs$ and servers.
- ●Base stations, DC/DC converters.

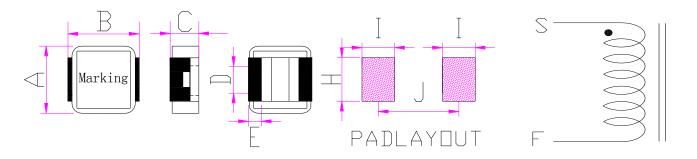
Features :

- ●RoHS, Halogen Free and REACH Compliance.
- ●High rated current.
- ●Ultra low buzz noise.
- 1. Physical drawing (Reference)



2. Dimensions in (mm)





А	В	С	D	Е	Н	Ι	J
17.0 ± 0.5	18.0 ± 1.0	7.0Max	11.9 ± 0.3	2.7 \pm 0.7	12.5	4.0	12.0

Characteristics :

- ●Saturation Current(Isat):The current when the inductance Becomes 30% lower than is initial value.(Ta=20℃).
- Temperature Rise Current(Irms): The current when the temperature of coil increases up to max. Δ T=40°C. (Ta=20°C).
- Operating temperature : -40° C \sim +125°C.
- lackstyle Storage temperature range (packaging conditions): -5 $^\circ\!\!C \sim$ +30 $^\circ\!\!C$ and RH 70% (Max.)

Test equipments :

- ●L&Q: HP 4285A or WK3260B, VR116/VR7220.
- ●DCR: Milli-ohm meter, VR131.
- \bullet Electrical specifications at 25°C.

Electrica	al Characte	eristics				
Part No.	Inductance (uH)	Test	RDC	Isat	Irms	Marking
		Frequency	$(m\Omega)$ Max	(A) Typ.	(A) Typ.	
VEPD1770-1ROM-A	$1.0 \pm 20\%$	100KHz/0.25V	1.9	32.0	55.5	1R0
VEPD1770-1R5M-A	1.5±20%	100KHz/0.25V	2.2	31.0	48.0	1R5
VEPD1770-2R2M-A	2.2±20%	100KHz/0.25V	2.5	26.5	43.5	2R2
VEPD1770-3R3M-A	3.3±20%	100KHz/0.25V	3.2	25.0	35.0	3R3
VEPD1770-4R7M-A	4.7 $\pm 20\%$	100KHz/0.25V	4.4	21.0	30.0	4R7
VEPD1770-6R8M-A	6.8±20%	100KHz/0.25V	6.5	18.0	22. 5	6R8
VEPD1770-100M-A	10±20%	100KHz/0.25V	18	15.0	19.0	100
VEPD1770-150M-A	$15 \pm 20\%$	100KHz/0.25V	20.0	12.0	14.0	150
VEPD1770-220M-A	$22 \pm 20\%$	100KHz/0.25V	22.0	9.5	12.0	220
VEPD1770-330M-A	33±20%	100KHz/0.25V	38.5	9.0	10.7	330
VEPD1770-470M-A	$47 \pm 20\%$	100KHz/0.25V	45.0	8.6	8.7	470
VEPD1770-560M-A	56±20%	100KHz/0.25V	60.5	5.2	7.2	560
VEPD1770-680M-A	68±20%	100KHz/0.25V	79.0	4.5	6.1	680
VEPD1770-101M-A	$100 \pm 20\%$	100KHz/0.25V	116.0	4.0	5.0	101

Packing method

1. Packing quantity of each reel: 300PCS/R (12-20PCS not packed at the beginning and end), affixed with the correct label, and then placed into a plastic bag (plastic bag size: 360x460mm), with a package of desiccant, sealing, reel and carrying tape dimensions. Fig. 1-1

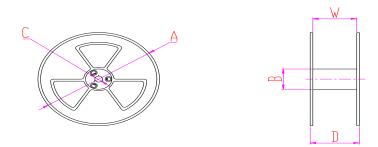
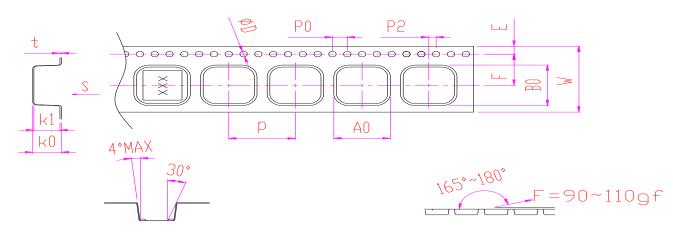


Fig .1-1

Reel dimensions , general tolerance $(\pm 0.5 \text{mm})$

А	В	С	W	D
Φ 330	Φ 100	Φ13.5	33.5	40.5

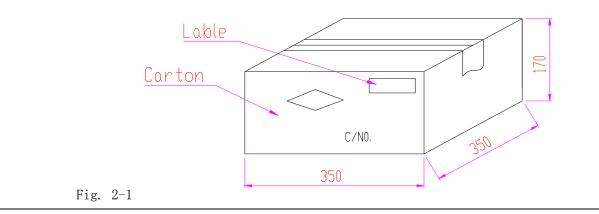


The general tolerance, of carrier size is $\pm < 0.2$ mm.

W	AO	В0	KO	K1	Р	F	Е	D	P0	P2	t
32.00	18.00	18.80	7.50	7.20	24.00	15.50	1.75	1.50	4.00	2.00	0.40

2. 600PCS(2volumes) per beer carton. (Beer carton size:338x338x78mm)

3. Each case is packed with 1800PCS(3boxes), sealed, labeled, and the shape and size of cartons. Fig. 2-1



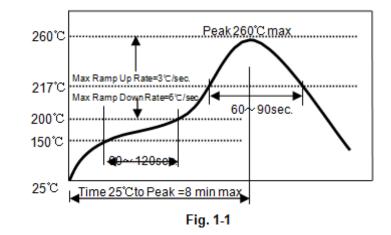
Reliability Test:							
Items	Requirements		Test Methods and Remarks				
1.Terminal	No removal or split of the termination or other defects shall	1	Solder the inductor to the testing jig (glass epoxy board				
Strength	occur. Y direct		shown in Fig.1-1) using eutectic solder. Then apply a				
			force in the direction of the arrow.				
	X direct	2	10N force.				
		3	Keep time: 5s				
	Fig.1-1						
2.Resistance to	No visible mechanical damage.	1	Solder the chip to the test jig (glass epoxy board) using				
Flexure	. 20.1		eutectic solder. Then apply a force in the direction				
			shown as Fig.2-1.				
	R230	2	Flexure: 2mm				
	fluxure 2mm	3	Pressurizing Speed: 0.5mm/sec				
	45[1.772] 45[1.772]	(4) (5)	Keep time: 30±1s Test board size: 100X40X1.0				
		6	Land dimension:				
	Fig.2-1		Please see				
3.Vibration	No visible mechanical damage.	1	Solder the chip to the testing jig (glass epoxy board				
	② Inductance change: Within ±10%	_	shown as the following figure) using eutectic solder.				
		2	The chip shall be subjected to a simple harmonic				
			motion having total amplitude of 1.5mm, the frequency				
			being varied uniformly between the approximate limits				
			of 10 and 55 Hz.				
		3	The frequency range from 10 to 55 Hz and return to 10				
			Hz shall be traversed in approximately 1 minute. This				
			motion shall be applied for a period of 2 hours in each				
		1	3 mutually perpendicular directions (total of 6 hours). Temperature: -40℃~+125℃				
4.Temperature	Inductance change: Within ±20%	2	With a reference value of $\pm 20^{\circ}$ C, change rate shall be				
coefficient			calculated				
5.Solderability	90% or more of electrode area shall be coated by new	1	The test samples shall be dipped in flux, and then immersed in molten solder.				
	solder.	2	Solder temperature: 245±5°C				
		3	Duration: 5±1 sec.				
		(4) (5)	Solder: Sn/3.0Ag/0.5Cu Flux: 25% resin and 75% ethanol in weight				
		6	Immersion depth: all sides of mounting terminal shall				
			be immersed				
6.Resistance to	① No visible mechanical damage.	1	Re-flowing Profile: Please refer to Fig. 3-1.				
Soldering Heat	② Inductance change: Within ±10%	2 3	Test board thickness: 1.0mm Test board material: glass epoxy resin				
		④	The chip shall be stabilized at normal condition for 1~2				
		0	hours before measuring				
		26	0°CPeak260°C max				
		20					
		21	7°C ^{Max Ramp Up Rate=3°C/sec.}				
		20					
		150					
			60~120sec				
		25	Time 25°C to Peak =8 min max				
			Fig. 3-1				
1		1					

7.Thermal Shock 8.Resistance to Low Temperature	 No visible mechanical damage. Inductance change: Within ±10% ¹⁰⁵°C ³⁰ min. ³⁰ min. ³⁰ min. ¹⁰⁵°C ³⁰ min. ¹⁰⁵°C ³⁰ min. ¹⁰⁵°C ³⁰ min. ¹⁰⁵°C ³⁰°C ³⁰°C ¹⁰⁰°C ¹⁰⁰	 Temperature and time: -30±3°C for 30±3 min→105°C for 30±3min, please refer to Fig. 4-1. Transforming interval: Max. 20 sec Tested cycle: 100 cycles The chip shall be stabilized at normal condition for 1~2 hours before measuring Temperature: -40±3°C Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for
		1~2 hours before measuring
9.Resistance to High Temperature	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 125±2°C Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring.
10.Damp Heat	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 60±2°C Humidity: 90% to 95%RH Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring
11.Loading Under Damp Heat	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 60±2°C Humidity: 90% to 95% RH Applied current: Rated current Duration:1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring
12.Loading at High Temperature	 No mechanical damage. Inductance change: Within ±10% 	 Temperature: 85±2°C Applied current: Rated current Duration: 1000^{±24} hours The chip shall be stabilized at normal condition for 1~2 hours before measuring

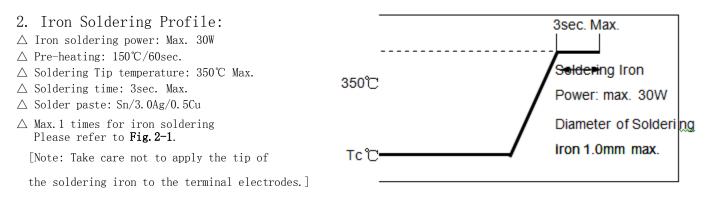
Recommended Soldering Technologies:

1. Re-flowing Profile:

- \triangle Preheat condition: 150 $^{\sim}200^{\circ}C/60^{\sim}120$ sec.
- \triangle Allowed time above 217°C: 60~90sec.
- \triangle Max temp: 260°C
- △ Max time at max temp: 5sec. Solder paste: Sn/3.0Ag/0.5Cu
- △ Allowed Reflow time: 2x max Please refer to **Fig. 1-1**.



[Note: The reflow profile in the above table is only for qualification and is not meant to specify board assembly profiles. Actual board assembly profiles must be based on the customer's specific board design, solder paste and process, and should not exceed the parameters as the Reflow profile shows.]





Supplier Information Supplier:

Shenzhen Volume Source Electronics Co., Ltd.

Manufacturer:

ShenzhenVolume Source Electronics Co., Ltd.

Manufacturing Address:

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