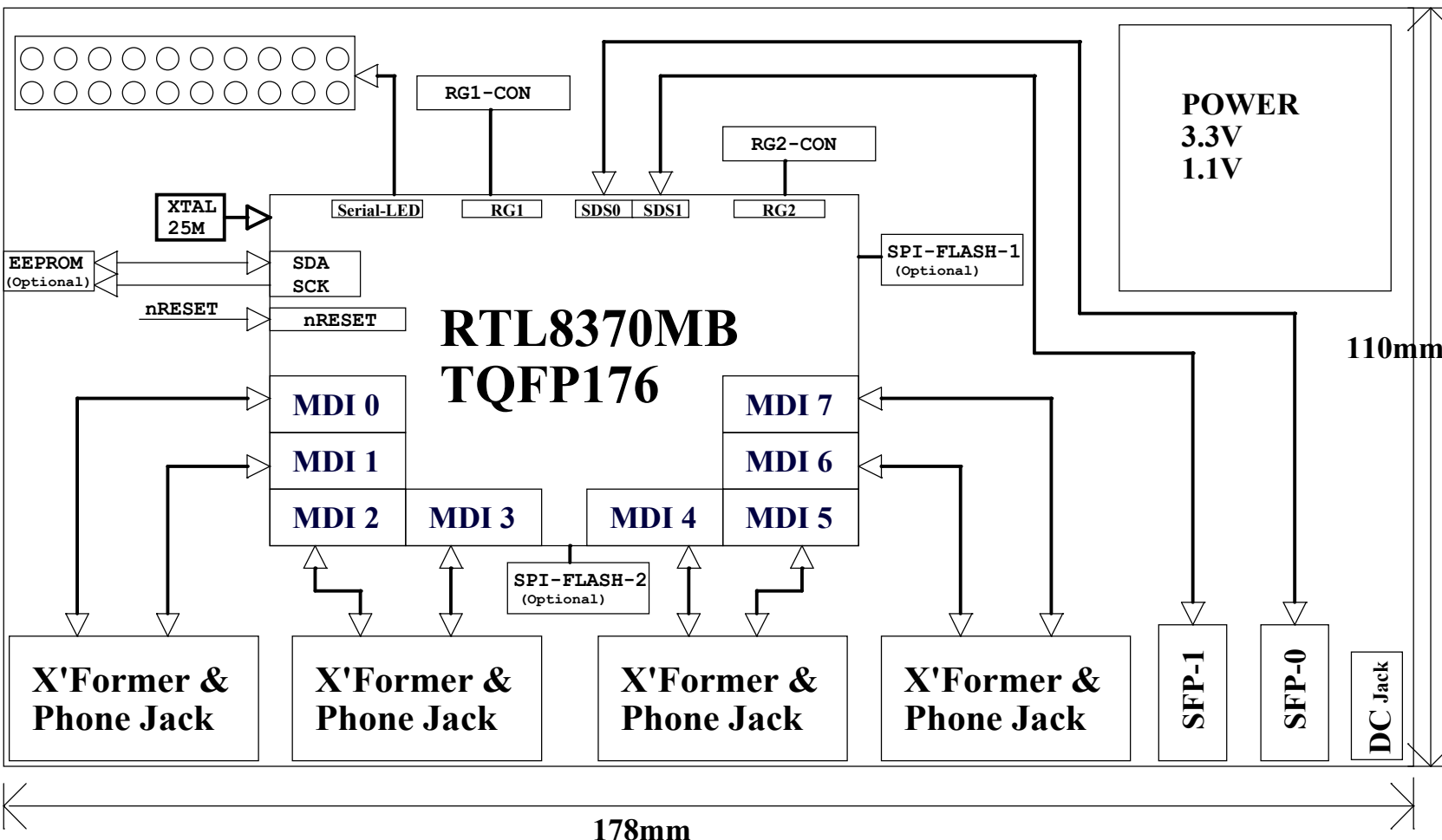
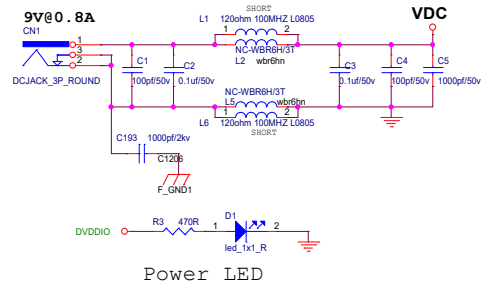


# RTL8370MB (TQFP176) Demo-Board Block Diagram



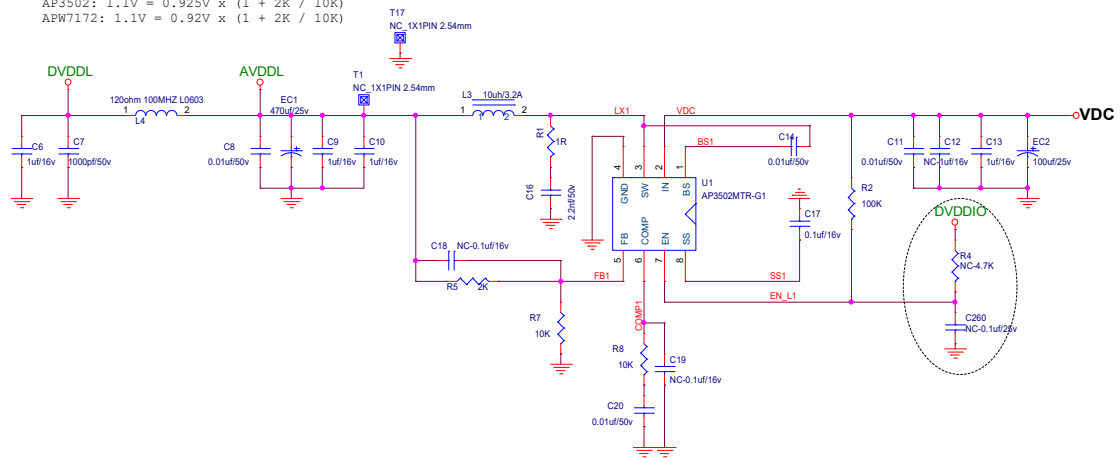
## REVISION HISTORY

Revision	Release Date	Summary
v1.0	2015/02/05	First Revision



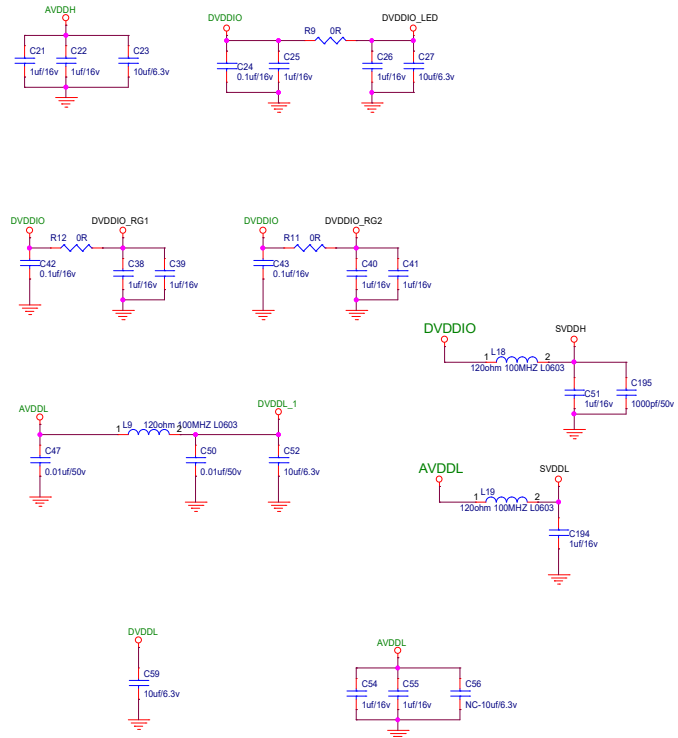
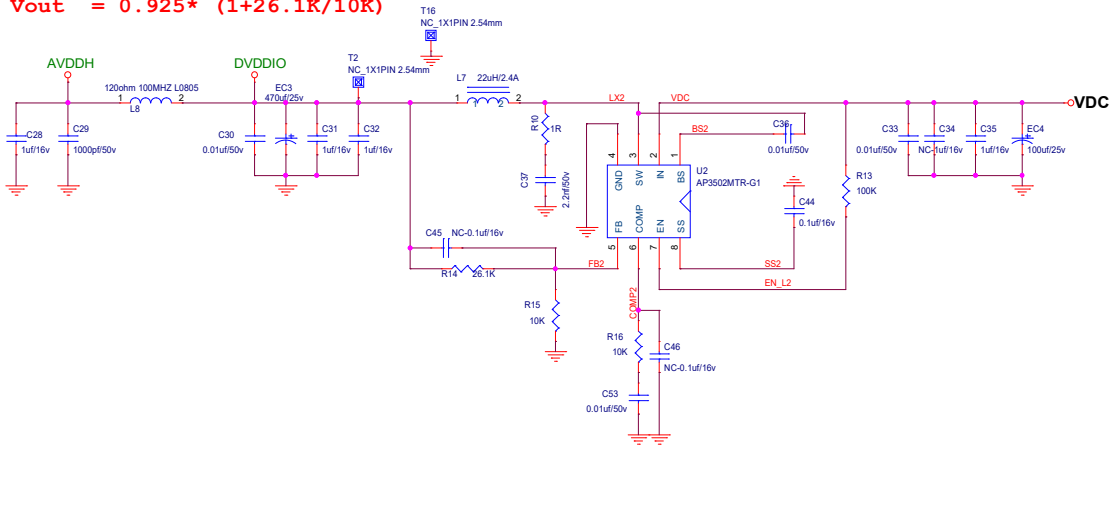
1.1v Max Load: ~2A

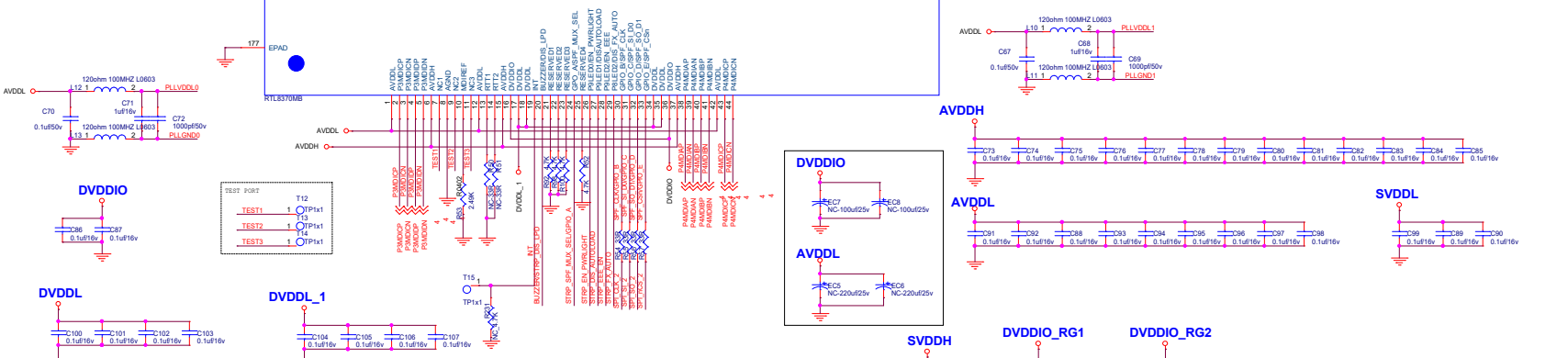
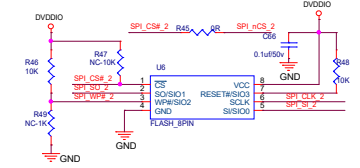
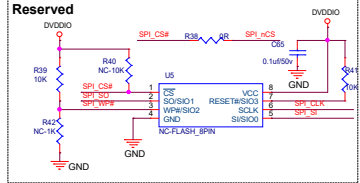
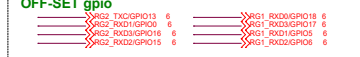
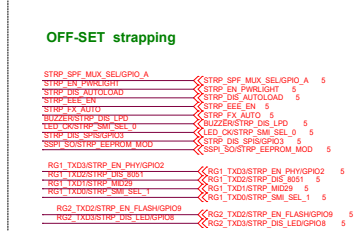
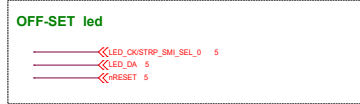
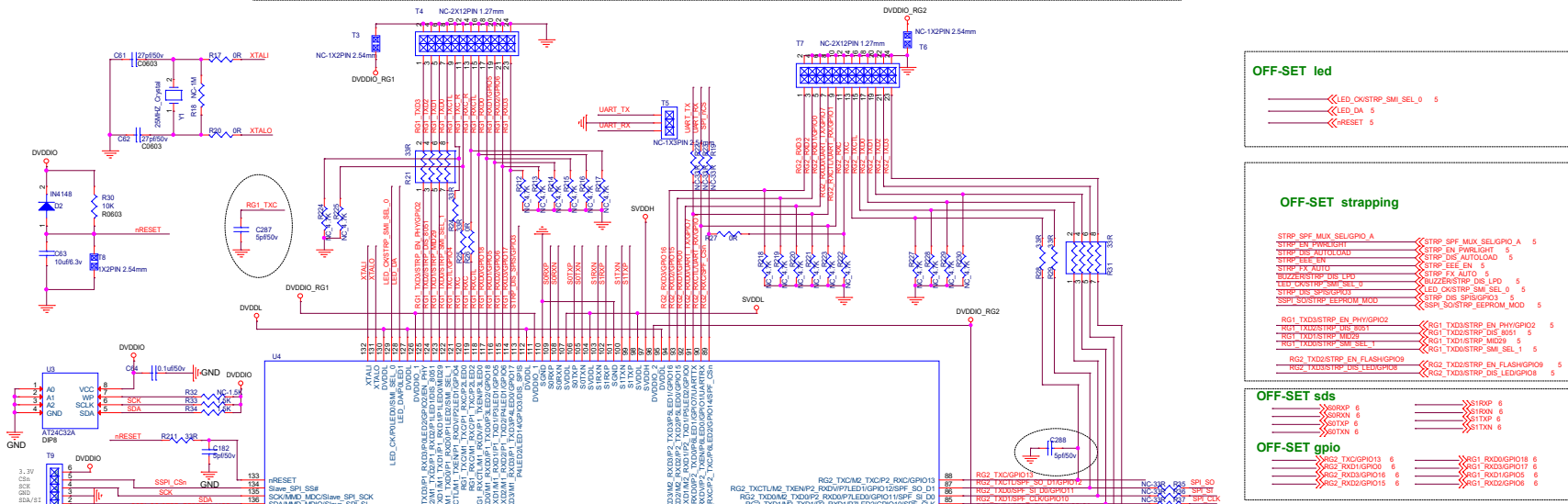
AP3502:  $1.1V = 0.925V \times (1 + 2K / 10K)$   
 APW7172:  $1.1V = 0.92V \times (1 + 2K / 10K)$

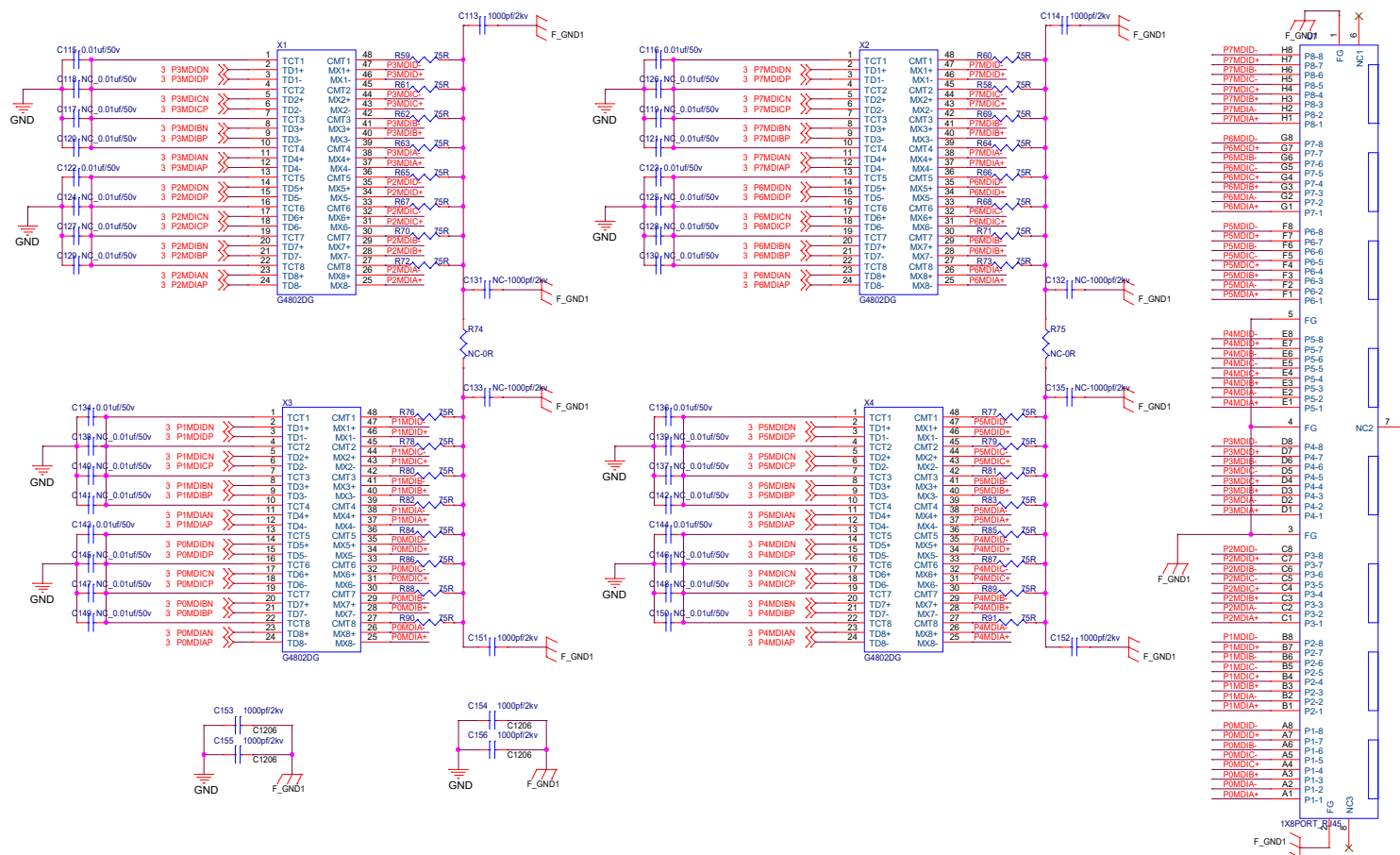


3.3v Max Load: ~1.5A

$V_{out} = 0.925 * (1 + 26.1K / 10K)$



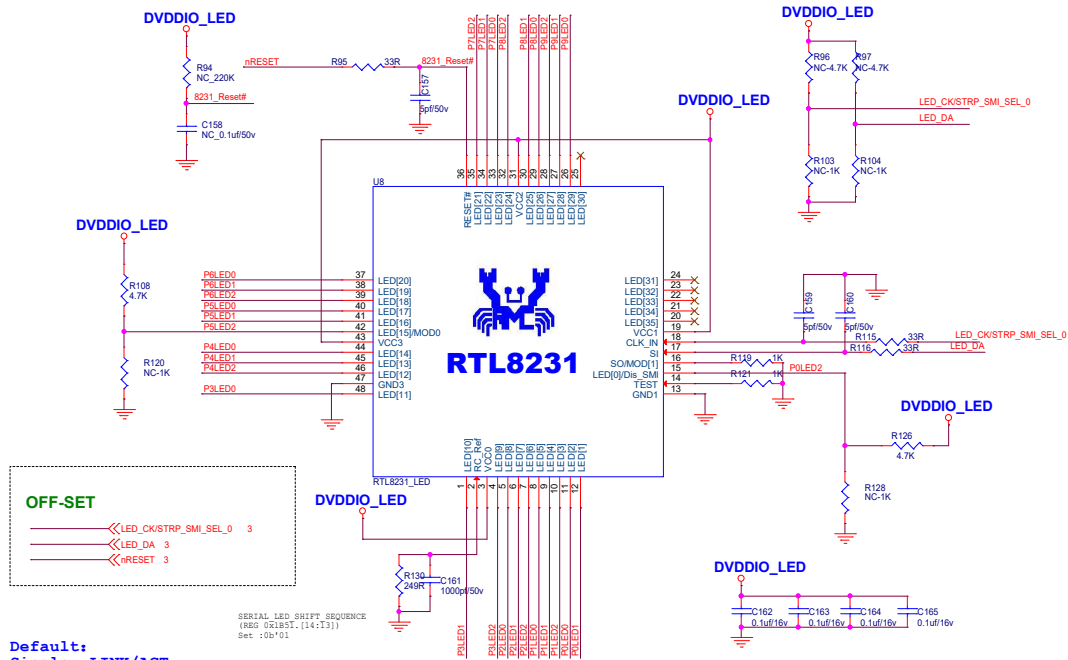




**Reserved For EMI/ESD TEST**

P2MDIAP	C186	NC-15pf/50v	P2MDIAP	C209	NC-15pf/50v	P2MDIAP	C217	NC-15pf/50v	P2MDIAP	C226	NC-15pf/50v
P2MDIAN	C187	NC-15pf/50v	P2MDIAN	C210	NC-15pf/50v	P2MDIAN	C218	NC-15pf/50v	P2MDIAN	C227	NC-15pf/50v
P2MDIBP	C188	NC-15pf/50v	P2MDIBP	C204	NC-15pf/50v	P2MDIBP	C213	NC-15pf/50v	P2MDIBP	C220	NC-15pf/50v
P2MDIBN	C189	NC-15pf/50v	P2MDIBN	C206	NC-15pf/50v	P2MDIBN	C214	NC-15pf/50v	P2MDIBN	C222	NC-15pf/50v
P2MDICP	C200	NC-15pf/50v	P2MDICP	C203	NC-15pf/50v	P2MDICP	C215	NC-15pf/50v	P2MDICP	C221	NC-15pf/50v
P2MDICN	C201	NC-15pf/50v	P2MDICN	C208	NC-15pf/50v	P2MDICN	C218	NC-15pf/50v	P2MDICN	C224	NC-15pf/50v
P2MDIDP	C202	NC-15pf/50v	P2MDIDP	C207	NC-15pf/50v	P2MDIDP	C213	NC-15pf/50v	P2MDIDP	C223	NC-15pf/50v
P2MDIDN	C203	NC-15pf/50v	P2MDIDN	C210	NC-15pf/50v	P2MDIDN	C218	NC-15pf/50v	P2MDIDN	C225	NC-15pf/50v
P2MDIDN	C233	NC-15pf/50v	P2MDIDN	C211	NC-15pf/50v	P2MDIDN	C249	NC-15pf/50v	P2MDIDN	C257	NC-15pf/50v
P2MDIDP	C234	NC-15pf/50v	P2MDIDP	C212	NC-15pf/50v	P2MDIDP	C250	NC-15pf/50v	P2MDIDP	C258	NC-15pf/50v
P2MDICN	C228	NC-15pf/50v	P2MDICN	C236	NC-15pf/50v	P2MDICN	C244	NC-15pf/50v	P2MDICN	C252	NC-15pf/50v
P2MDICP	C230	NC-15pf/50v	P2MDICP	C238	NC-15pf/50v	P2MDICP	C246	NC-15pf/50v	P2MDICP	C254	NC-15pf/50v
P2MDIBN	C229	NC-15pf/50v	P2MDIBN	C237	NC-15pf/50v	P2MDIBN	C245	NC-15pf/50v	P2MDIBN	C253	NC-15pf/50v
P2MDIBP	C232	NC-15pf/50v	P2MDIBP	C240	NC-15pf/50v	P2MDIBP	C248	NC-15pf/50v	P2MDIBP	C256	NC-15pf/50v
P2MDIAN	C231	NC-15pf/50v	P2MDIAN	C239	NC-15pf/50v	P2MDIAN	C247	NC-15pf/50v	P2MDIAN	C255	NC-15pf/50v
P2MDIAP	C234	NC-15pf/50v	P2MDIAP	C242	NC-15pf/50v	P2MDIAP	C250	NC-15pf/50v	P2MDIAP	C258	NC-15pf/50v

# Serial LED

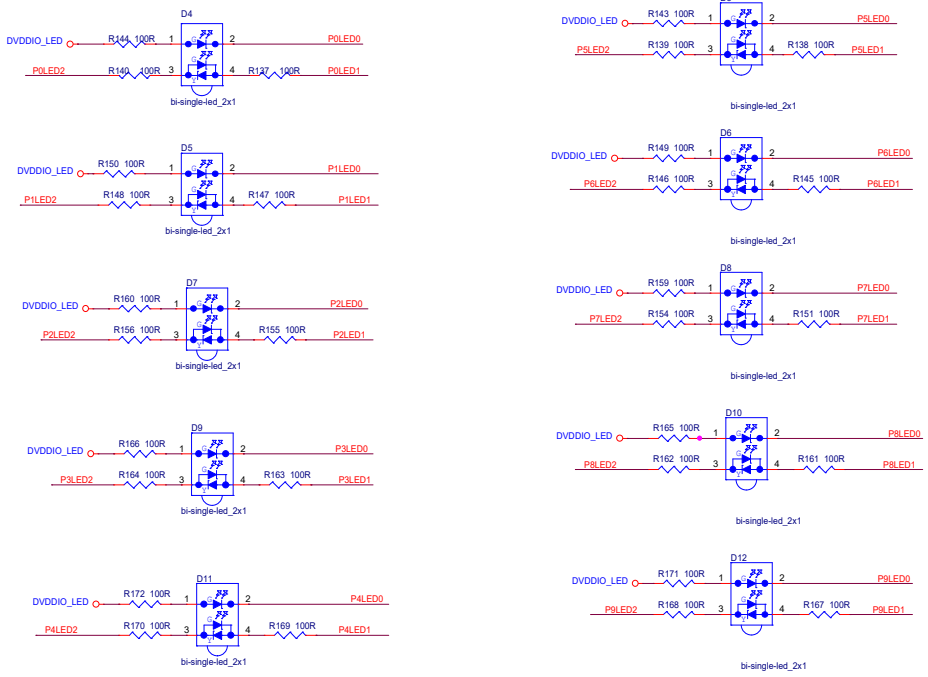


**OFF-SET**

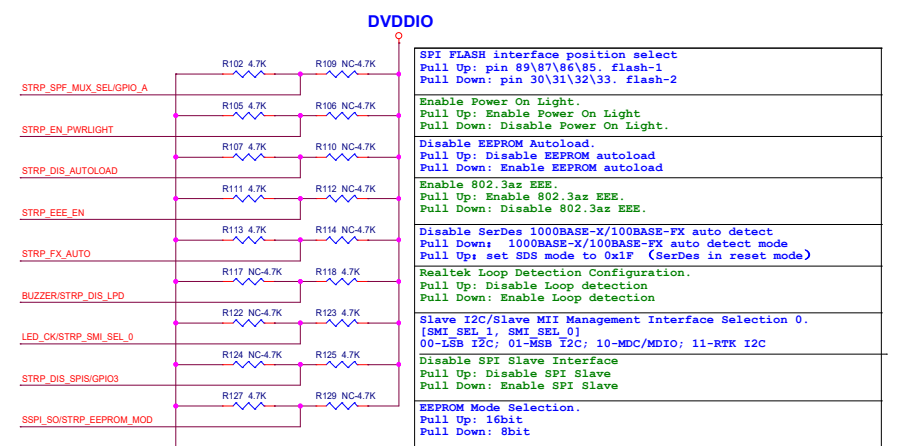
- LED\_CK/STRP\_SMI\_SEL\_0 3
- LED\_DA 3
- RESET 3

**Default:**  
**Single:** LINK/ACT  
**Green:** 1000M LINK  
**Yellow:** 100M\_LINK

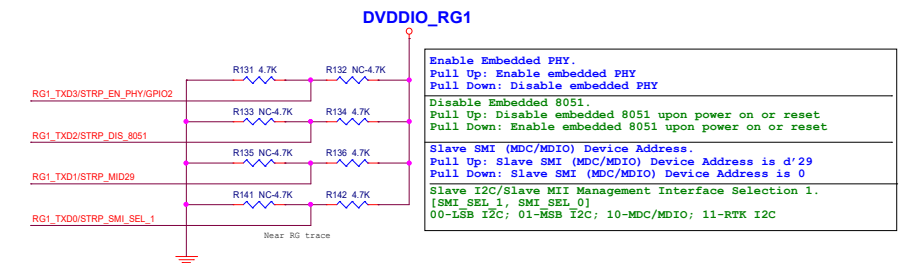
SERIAL\_LED\_SHIFT\_SEQUENCE  
 (REG 0X1B5T, [14:13])  
 Set: 0b011



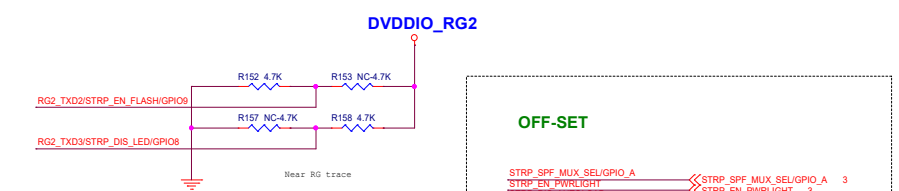
# STRAPPING



<b>SPI FLASH interface position select</b>	
Pull Up:	pin 89\87\86\85. flash-1
Pull Down:	pin 30\31\32\33. flash-2
<b>Enable Power On Light.</b>	
Pull Up:	Enable Power On Light
Pull Down:	Disable Power On Light.
<b>Disable EEPROM Autoload.</b>	
Pull Up:	Disable EEPROM autoload
Pull Down:	Enable EEPROM autoload
<b>Enable 802.3az EEE.</b>	
Pull Up:	Enable 802.3az EEE.
Pull Down:	Disable 802.3az EEE.
<b>Disable SerDes 1000BASE-X/100BASE-FX auto detect</b>	
Pull Down:	1000BASE-X/100BASE-FX auto detect mode
Pull Up:	set SDS mode to 0x1F (SerDes in reset mode)
<b>Realtek Loop Detection Configuration.</b>	
Pull Up:	Disable Loop detection
Pull Down:	Enable Loop detection
<b>Slave I2C/Slave MII Management Interface Selection 0.</b>	
[SMI_SEL_1, SMI_SEL_0]	00-LSB I2C; 01-MSB I2C; 10-MDC/MDIO; 11-RTK I2C
<b>Disable SPI Slave Interface</b>	
Pull Up:	Disable SPI Slave
Pull Down:	Enable SPI Slave
<b>EEPROM Mode Selection.</b>	
Pull Up:	16bit
Pull Down:	8bit



<b>Enable Embedded PHY.</b>	
Pull Up:	Enable embedded PHY
Pull Down:	Disable embedded PHY
<b>Disable Embedded 8051.</b>	
Pull Up:	Disable embedded 8051 upon power on or reset
Pull Down:	Enable embedded 8051 upon power on or reset
<b>Slave SMI (MDC/MDIO) Device Address.</b>	
Pull Up:	Slave SMI (MDC/MDIO) Device Address is d'29
Pull Down:	Slave SMI (MDC/MDIO) Device Address is 0
<b>Slave I2C/Slave MII Management Interface Selection 1.</b>	
[SMI_SEL_1, SMI_SEL_0]	00-LSB I2C; 01-MSB I2C; 10-MDC/MDIO; 11-RTK I2C

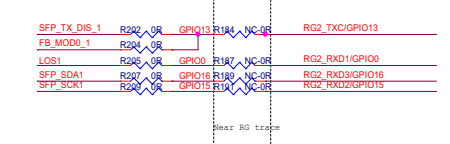
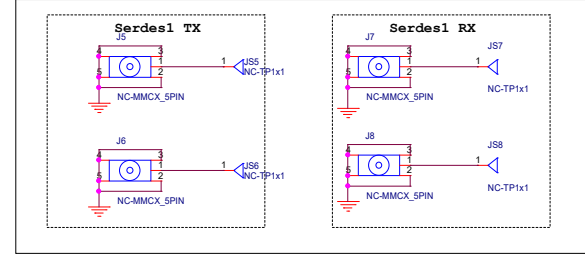
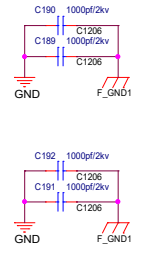
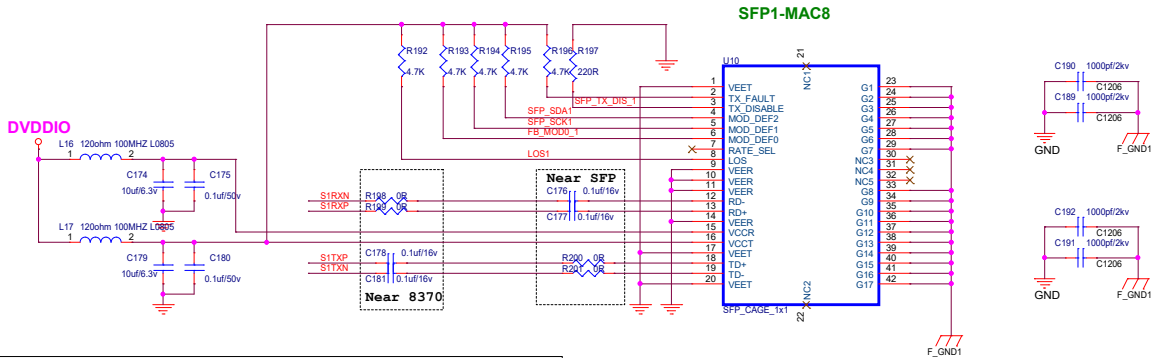
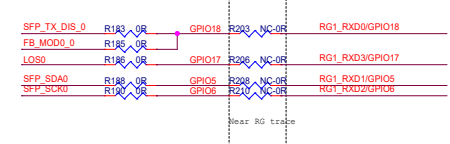
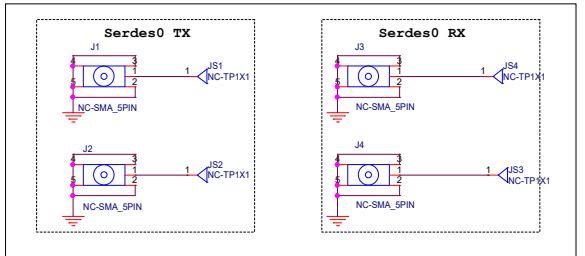
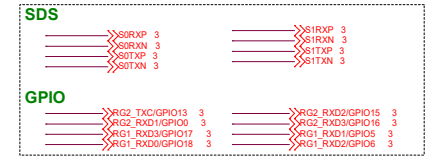
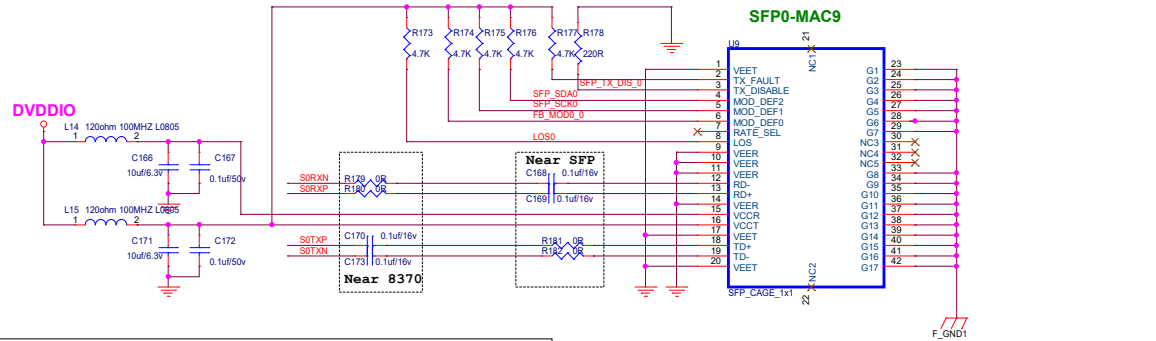


**OFF-SET**

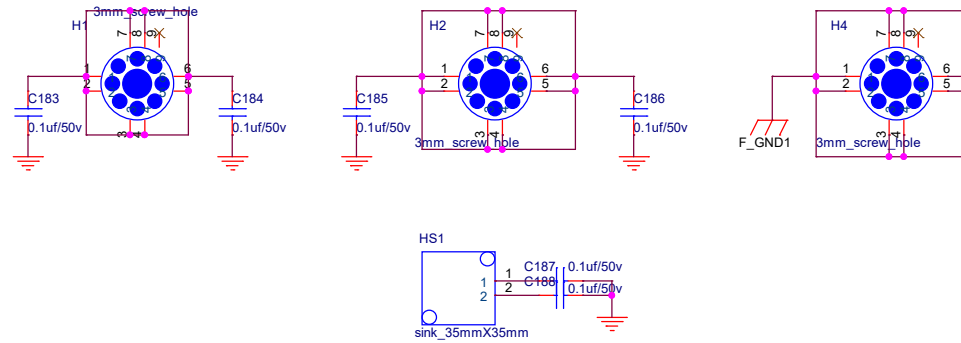
- STRP\_SPF\_MUX\_SEL/GPIO\_A 3
- STRP\_EN PWRLIGHT 3
- STRP\_DIS\_AUTOLOAD 3
- STRP\_EEE\_EN 3
- STRP\_FX\_AUTO 3
- BUZZER/STRP\_DIS\_LPD 3
- LED\_CK/STRP\_SMI\_SEL\_0 3
- STRP\_DIS\_SPS/GPIO3 3
- SSPI\_SO/STRP\_EEPROM\_MOD 3
- RG1\_TXD3/STRP\_EN\_PHY/GPIO2 3
- RG1\_TXD2/STRP\_DIS\_8051 3
- RG1\_TXD1/STRP\_MID29 3
- RG1\_TXD0/STRP\_SMI\_SEL\_1 3
- RG2\_TXD2/STRP\_EN\_FLASH/GPIO9 3
- RG2\_TXD3/STRP\_DIS\_LED/GPIO8 3

**Enable SPI FLASH Interface.**  
 Pull Up: Enable FLASH interface  
 Pull Down: Disable FLASH interface

**Disable/Enable LED function when power on.**  
 Pull Up: Enable LED  
 Pull Down: Disable LED.

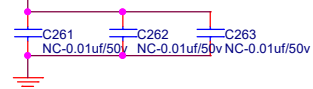


### Mechanical

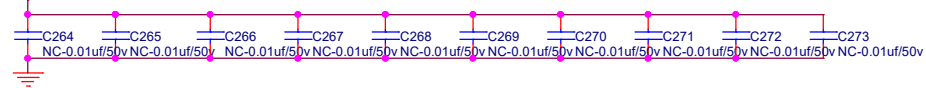


### Reserved For EMI/ESD TEST

#### VDC



#### AVDDL



#### DVDDIO

