# **VEMT2523SLX01**

## **Vishay Semiconductors**



# Silicon NPN Phototransistor



### DESCRIPTION

VEMT2523SLX01 is a silicon NPN epitaxial planar phototransistor in a miniature side looking, surface mount package (SMD) with clear epoxy dome lens. The device is sensitive to visible and near infrared radiation.

### **FEATURES**

- Package type: surface mount
- · Package form: side view
- Dimensions (L x W x H in mm): 2.3 x 2.55 x 2.3
- AEC-Q101 qualified
- High radiant sensitivity
- · Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity:  $\varphi = \pm 35^{\circ}$
- · Package matched with IR emitter series VSMB2943SLX01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- · Lead (Pb)-free reflow soldering
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

### **APPLICATIONS**

- · Detector in automotive applications
- Photo interrupters
- Miniature switches
- Counters
- Encoders
- Position sensors

PRODUCT SUMMARY			
COMPONENT	I <sub>ca</sub> (mA)	φ <b>(deg)</b>	λ <sub>0.1</sub> (nm)
VEMT2523SLX01	2.7	± 35	470 to 1090

#### Note

Test condition see table "Basic Characteristics"

ORDERING INFORMATI	ON			
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMT2523SLX01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel	Side view	

Note

• MOQ: minimum order quantity

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COMPLIANT HALOGEN FREE GREEN

(5-2008)





<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		V <sub>CEO</sub>	20	V	
Emitter collector voltage		V <sub>ECO</sub>	7	V	
Collector current		Ι <sub>C</sub>	50	mA	
Power power dissipation	$T_{amb} \le 75 \ ^{\circ}C$	Pv	100	mW	
Junction temperature		Тj	100	°C	
Operating temperature range		T <sub>amb</sub>	- 40 to + 100	°C	
Storage temperature range		T <sub>stg</sub>	- 40 to + 100	°C	
Soldering temperature	Acc. reflow profile fig. 8	T <sub>sd</sub>	260	°C	
Thermal resistance junction/ambient	Acc. J-STD-051	R <sub>thJA</sub>	250	K/W	

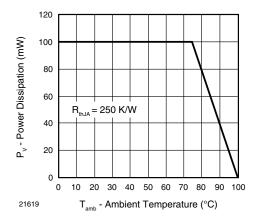


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I <sub>C</sub> = 0.1 mA	V <sub>CEO</sub>	20			V
Collector dark current	$V_{CE} = 5 V, E = 0$	I <sub>CEO</sub>		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 V, f = 1 MHz, E = 0$	C <sub>CEO</sub>		25		pF
Collector light current	$\begin{array}{l} E_{e} = 1 \text{ mW/cm}^2,  \lambda = 950 \text{ nm}, \\ V_{CE} = 5 \text{ V} \end{array}$	I <sub>ca</sub>	1.3	2.7	4.1	mA
Angle of half sensitivity		φ		± 35		deg
Wavelength of peak sensitivity		λρ		850		nm
Range of spectral bandwidth		λ <sub>0.1</sub>		470 to 1090		nm
Collector emitter saturation voltage	$I_{\rm C} = 0.05 \ {\rm mA}$	V <sub>CEsat</sub>			0.4	V
Temperature coefficient of Ica	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_{CE} = 5 \text{ V}$	Tk <sub>lca</sub>		1.1		%/K



## **BASIC CHARACTERISTICS** ( $T_{amb}$ = 25 °C, unless otherwise specified)

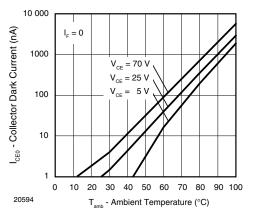


Fig. 2 - Collector Dark Current vs. Ambient Temperature

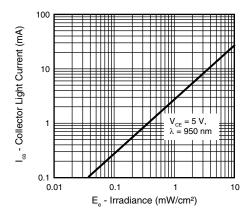


Fig. 3 - Collector Light Current vs. Irradiance

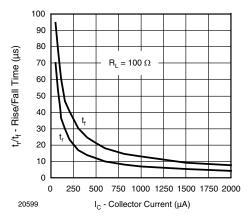


Fig. 4 - Rise/Fall Time vs. Collector Current

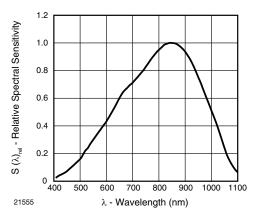


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

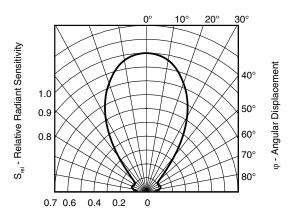


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

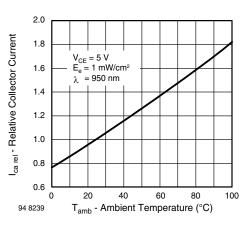


Fig. 7 - Relative Collector Current vs. Ambient Temperature

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### **REFLOW SOLDER PROFILE**

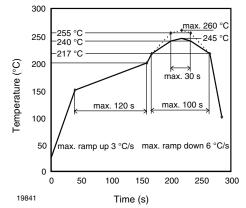


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

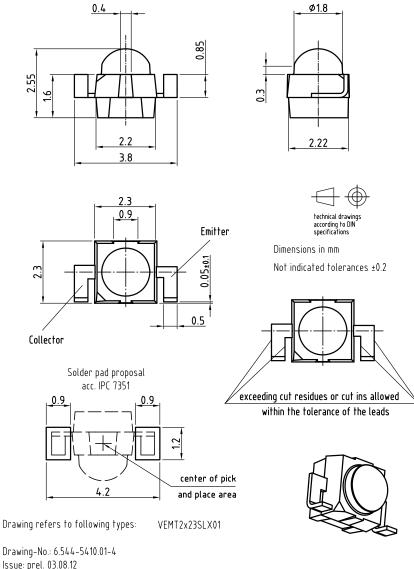
## **FLOOR LIFE**

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label: Floor life: 4 weeks Conditions:  $T_{amb} < 30$  °C, RH < 60 % Moisture sensitivity level 2a, acc. to J-STD-020.

#### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.

#### PACKAGE DIMENSIONS VEMT2523SLX01 in millimeters

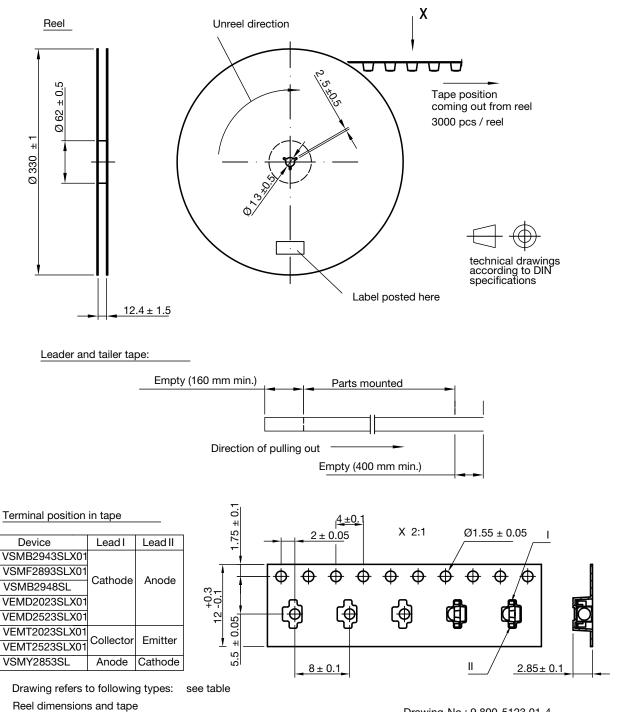


4 For technical questions, contact: <u>detectortechsupport@vishay.com</u> Document Number: 84167

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## TAPE AND REEL DIMENSIONS VEMT2523SLX01 in millimeters



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