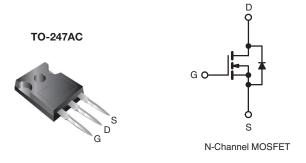




## **Power MOSFET**

PRODUCT SUMMA	RY		
V <sub>DS</sub> (V)	200		
R <sub>DS(on)</sub> (Ω)	$V_{GS} = 10 V$	0.085	
Q <sub>g</sub> (Max.) (nC)	140	)	
Q <sub>gs</sub> (nC)	28		
Q <sub>gd</sub> (nC)	74		
Configuration	Sing	le	



### **FEATURES**

- Dynamic dV/dt Rating
- Repetitive Avalanche Rated
- Isolated Central Mounting Hole
- Fast Switching
- Ease of Paralleling
- Simple Drive Requirements
- Compliant to RoHS Directive 2002/95/EC

### DESCRIPTION

Third generation Power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effictiveness.

The TO-220AB package is universially preferred for commercial-industrial applications where higher power levels preclude the use of TO-220AB devices. The TO-247AC is similar but superior to the earlier TO-218 package because of its isolated mounting hole. It also provides greater creepage distance between pins to meet the requirements of most safety specifications.

ORDERING INFORMATION	
Package	TO-247AC
Lead (Pb)-free	IRFP250PbF
Lead (FD)-hee	SiHFP250-E3
SnPb	IRFP250
	SiHFP250

### **ABSOLUTE MAXIMUM RATINGS** ( $T_{c} = 25 \,^{\circ}C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	200	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	Ň
Continuous Drain Current	$V_{GS}$ at 10 V $T_C = 25 \degree C$	1-	30	
$V_{GS} at 10 V T_{C} = 100 \text{ °C}$		ID	19	А
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	120	
Linear Derating Factor			1.5	W/°C
Single Pulse Avalanche Energy <sup>b</sup>		E <sub>AS</sub>	410	mJ
Repetitive Avalanche Current <sup>a</sup>		I <sub>AR</sub>	30	А
Repetitive Avalanche Energy <sup>a</sup>		E <sub>AR</sub>	19	mJ
Maximum Power Dissipation	T <sub>C</sub> = 25 °C	PD	190	W
Peak Diode Recovery dV/dt <sup>c</sup>		dV/dt	5.0	V/ns
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 150	°C
Soldering Recommendations (Peak Temperature) for 10 s			300 <sup>d</sup>	7
Mounting Torque	6.00 or M0 oprovi		10	lbf · in
Mounting Torque	6-32 or M3 screw		1.1	N · m

#### Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11). b.  $V_{DD} = 50 \text{ V}$ , starting  $T_J = 25 \text{ °C}$ ,  $L = 683 \mu\text{H}$ ,  $R_g = 25 \Omega$ ,  $I_{AS} = 30 \text{ A}$  (see fig. 12). c.  $I_{SD} \leq 30 \text{ A}$ , dl/dt  $\leq 190 \text{ A/}\mu\text{s}$ ,  $V_{DD} \leq V_{DS}$ ,  $T_J \leq 150 \text{ °C}$ .

d. 1.6 mm from case.

\* Pb containing terminations are not RoHS compliant, exemptions may apply

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RoHS COMPLIANT

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THERMAL RESISTANCE RATII	NGS							
PARAMETER	SYMBOL	TYP	·.	MAX.			UNIT	
Maximum Junction-to-Ambient	R <sub>thJA</sub>	-		40				
Case-to-Sink, Flat, Greased Surface	R <sub>thCS</sub>	0.24	1	-			°C/W	
Maximum Junction-to-Case (Drain)	R <sub>thJC</sub>	-		0.65				
<b>SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C, u	nless otherw	ise noted)						
PARAMETER	SYMBOL	1	T CONDITI	ONS	MIN.	TYP.	MAX.	UNIT
Static						1		1
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> =	= 0 V, I <sub>D</sub> = 2	50 µA	200	-	-	V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C,	I <sub>D</sub> = 1 mA	-	0.27	-	V/°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	= V <sub>GS</sub> , I <sub>D</sub> = 2	50 µA	2.0	-	4.0	V
Gate-Source Leakage	I <sub>GSS</sub>	,	V <sub>GS</sub> = ± 20 V	/	-	-	± 100	nA
Zaus Osta Visita na Dusin Orumant		V <sub>DS</sub> =	= 200 V, V <sub>GS</sub>	= 0 V	-	-	25	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 160 V	/, V <sub>GS</sub> = 0 V,	T <sub>J</sub> = 125 °C	-	-	250	μA
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS} = 10 V$	I <sub>D</sub>	= 18 A <sup>b</sup>	-	-	0.085	Ω
Forward Transconductance	9 <sub>fs</sub>	V <sub>DS</sub>	= 50 V, I <sub>D</sub> =	18 A	12	-	-	S
Dynamic								
Input Capacitance	C <sub>iss</sub>		$V_{GS} = 0 V_{,}$		-	2800	-	
Output Capacitance	C <sub>oss</sub>	-	$V_{GS} = 0 V$ , $V_{DS} = 25 V$ ,		-	780	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>	f = 1.	.0 MHz, see	fig. 5	-	250	-	·
Total Gate Charge	Q <sub>g</sub>				-	-	140	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$I_D = 30 A$	, V <sub>DS</sub> = 160 V, J. 6 and 13 <sup>b</sup>	-	-	28	nC
Gate-Drain Charge	Q <sub>gd</sub>	-	366 115	. o and 10	-	-	74	
Turn-On Delay Time	t <sub>d(on)</sub>				-	16	-	
Rise Time	t <sub>r</sub>	- 	= 100 V, I <sub>D</sub> =	20 4	-	86	-	
Turn-Off Delay Time	t <sub>d(off)</sub>			see fig. 10 <sup>b</sup>	_	70	_	ns
Fall Time	t <sub>f</sub>				_	62	_	
Internal Drain Inductance	L <sub>D</sub>	Between lead		D	_	5.0	_	
	-0	6 mm (0.25") 1 package and				0.0		nH
Internal Source Inductance	L <sub>S</sub>	die contact			-	13	-	
Drain-Source Body Diode Characteristic	s	1				. <u></u>		
Continuous Source-Drain Diode Current	IS	MOSFET sym showing the			-	-	30	
Pulsed Diode Forward Current <sup>a</sup>	I <sub>SM</sub>	integral revers p - n junction			-	-	120	A
Body Diode Voltage	V <sub>SD</sub>	T <sub>J</sub> = 25 °C	C, I <sub>S</sub> = 30 A,	V <sub>GS</sub> = 0 V <sup>b</sup>	-	-	2.0	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>				-	360	540	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C, I	<sub>F</sub> = 30 A, dl	/dt = 100 A/μs	_	4.6	6.9	μC
Forward Turn-On Time	t <sub>on</sub>			s negligible (turn				•

#### Notes

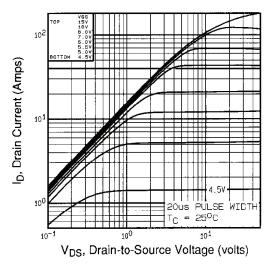
a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width  $\leq$  300 µs; duty cycle  $\leq$  2 %.

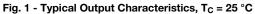
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# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



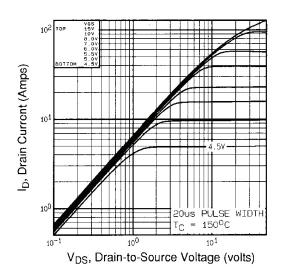
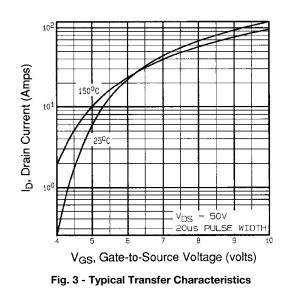


Fig. 2 -Typical Output Characteristics, T<sub>C</sub> = 150 °C



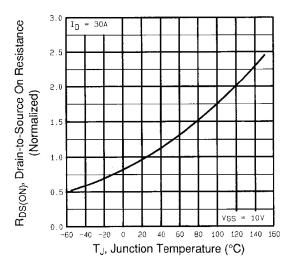


Fig. 4 - Normalized On-Resistance vs. Temperature

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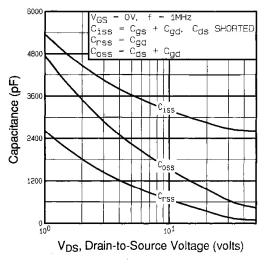


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

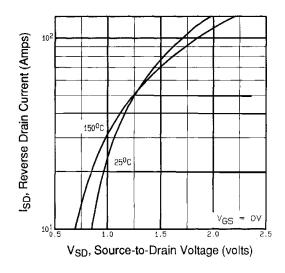


Fig. 7 - Typical Source-Drain Diode Forward Voltage

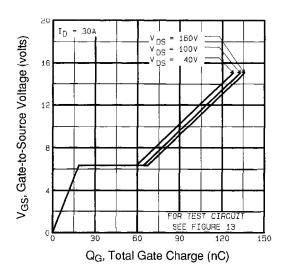


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

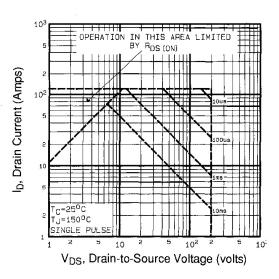


Fig. 8 - Maximum Safe Operating Area



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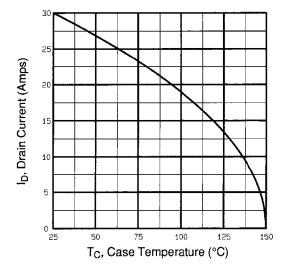


Fig. 9 - Maximum Drain Current vs. Case Temperature

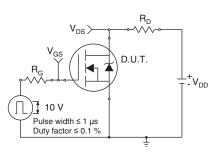


Fig. 10a - Switching Time Test Circuit

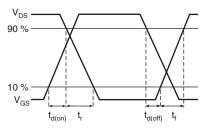


Fig. 10b - Switching Time Waveforms

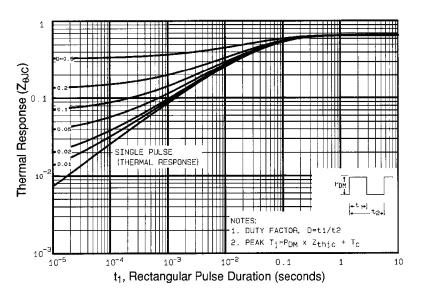


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

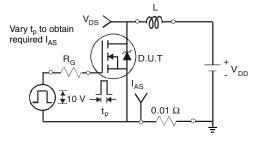


Fig. 12a - Unclamped Inductive Test Circuit

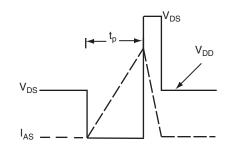


Fig. 12b - Unclamped Inductive Waveforms

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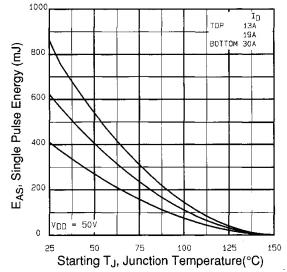


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

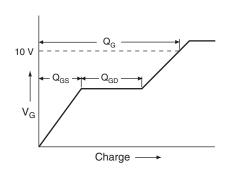


Fig. 13a - Basic Gate Charge Waveform

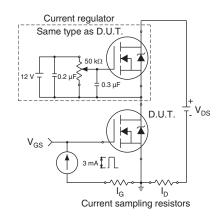
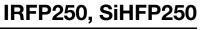


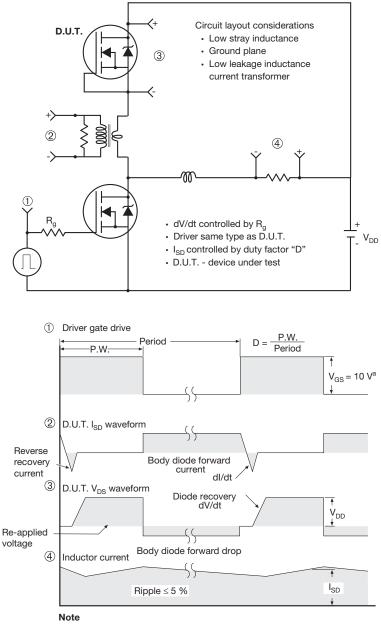
Fig. 13b - Gate Charge Test

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a.  $V_{GS} = 5 V$  for logic level devices

Fig. 14 - For N-Channel

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## TO-247AC (High Voltage)

### VERSION 1: FACILITY CODE = 9





Section C--C, D--D, E--E

	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
А	4.83	5.21	
A1	2.29	2.55	
A2	1.50	2.49	
b	1.12	1.33	
b1	1.12	1.28	
b2	1.91	2.39	6
b3	1.91	2.34	
b4	2.87	3.22	6, 8
b5	2.87	3.18	
С	0.55	0.69	6
c1	0.55	0.65	
D	20.40	20.70	4

	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
D1	16.25	16.85	5
D2	0.56	0.76	
E	15.50	15.87	4
E1	13.46	14.16	5
E2	4.52	5.49	3
e	5.44	BSC	
L	14.90	15.40	
L1	3.96	4.16	6
ØP	3.56	3.65	7
Ø P1	7.19	) ref.	
Q	5.31	5.69	
S	5.54	5.74	

### Notes

- <sup>(1)</sup> Package reference: JEDEC<sup>®</sup> TO247, variation AC
- (2) All dimensions are in mm
- <sup>(3)</sup> Slot required, notch may be rounded
- <sup>(4)</sup> Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(5)</sup> Thermal pad contour optional with dimensions D1 and E1
- (6) Lead finish uncontrolled in L1
- (7) Ø P to have a maximum draft angle of 1.5° to the top of the part with a maximum hole diameter of 3.91 mm
- (8) Dimension b2 and b4 does not include dambar protrusion. Allowable dambar protrusion shall be 0.1 mm total in excess of b2 and b4 dimension at maximum material condition

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### VERSION 2: FACILITY CODE = Y



	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
A	4.58	5.31	
A1	2.21	2.59	
A2	1.17	2.49	
b	0.99	1.40	
b1	0.99	1.35	
b2	1.53	2.39	
b3	1.65	2.37	
b4	2.42	3.43	
b5	2.59	3.38	
с	0.38	0.86	
c1	0.38	0.76	
D	19.71	20.82	
D1	13.08	-	

	MILLIN	IETERS	
DIM.	MIN.	MAX.	NOTES
D2	0.51	1.30	
E	15.29	15.87	
E1	13.72	-	
е	5.46	BSC	
Øk	0.2	254	
L	14.20	16.25	
L1	3.71	4.29	
ØΡ	3.51	3.66	
Ø P1	-	7.39	
Q	5.31	5.69	
R	4.52	5.49	
S	5.51	BSC	

### Notes

- <sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994
- <sup>(2)</sup> Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1
- <sup>(5)</sup> Lead finish uncontrolled in L1
- <sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- <sup>(7)</sup> Outline conforms to JEDEC outline TO-247 with exception of dimension c



### VERSION 3: FACILITY CODE = N



	MILLIN	IETERS		MILLIN	IETERS
DIM.	MIN.	MAX.	DIM.	MIN.	MAX.
А	4.65	5.31	D2	0.51	1.35
A1	2.21	2.59	E	15.29	15.87
A2	1.17	1.37	E1	13.46	-
b	0.99	1.40	е	5.46	BSC
b1	0.99	1.35	k	0.:	254
b2	1.65	2.39	L	14.20	16.10
b3	1.65	2.34	L1	3.71	4.29
b4	2.59	3.43	N	7.62	BSC
b5	2.59	3.38	Р	3.56	3.66
С	0.38	0.89	P1	-	7.39
c1	0.38	0.84	Q	5.31	5.69
D	19.71	20.70	R	4.52	5.49
D1	13.08	-	S	5.51	BSC

Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

<sup>(2)</sup> Contour of slot optional

(3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

<sup>(4)</sup> Thermal pad contour optional with dimensions D1 and E1

<sup>(5)</sup> Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")



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