

Industrial Inductive Load Driver

NUD3160, SZNUD3160

This micro-integrated part provides a single component solution to switch inductive loads such as relays, solenoids, and small DC motors without the need of a free-wheeling diode. It accepts logic level inputs, thus allowing it to be driven by a large variety of devices including logic gates, inverters, and microcontrollers.

Features

- Provides Robust Interface between D.C. Relay Coils and Sensitive Logic
- Capable of Driving Relay Coils Rated up to 150 mA at 12 V, 24 V or 48 V
- Replaces 3 or 4 Discrete Components for Lower Cost
- Internal Zener Eliminates Need for Free-Wheeling Diode
- Meets Load Dump and other Automotive Specs
- SZ Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

- Automotive and Industrial Environment
- Drives Window, Latch, Door, and Antenna Relays

Benefits

- Reduced PCB Space
- Standardized Driver for Wide Range of Relays
- Simplifies Circuit Design and PCB Layout
- Compliance with Automotive Specifications





SOT-23 CASE 318 STYLE 21 SC-74 CASE 318F STYLE 7

MARKING DIAGRAMS





JW8 = Specific Device Code

M = Date Code= Pb-Free Package

(Note: Microdot may be in either location)

JW8 = Specific Device Code

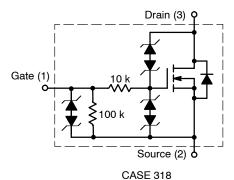
M = Date CodePb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NUD3160LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
SZNUD3160LT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
NUD3160DMT1G	SC-74 (Pb-Free)	3000 / Tape & Reel
SZNUD3160DMT1G	SC-74 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.



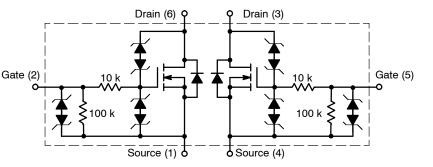


Figure 1. Internal Circuit Diagrams

1

MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise specified)

Symbol	Rating	Value	Unit
V_{DSS}	Drain-to-Source Voltage - Continuous (T _J = 125°C)	60	V
V _{GSS}	Gate-to-Source Voltage - Continuous (T _J = 125°C)	12	V
Ι _D	Drain Current – Continuous (T _J = 125°C) Minimum copper, double sided board, T _A = 80°C SOT-23 SC74 Single device driven SC74 Both devices driven 1 in ² copper, double sided board, T _A = 25°C SOT-23 SC74 Single device driven SC74 Both devices driven	158 157 132 ea 272 263 230 ea	mA
E _Z	Single Pulse Drain-to-Source Avalanche Energy (For Relay's Coils/Inductive Loads of 80 Ω or Higher) (T _J Initial = 85°C)	200	mJ
P _{PK}	Peak Power Dissipation, Drain-to-Source (Notes 1 and 2) (T _J Initial = 85°C)	20	W
E _{LD1}	Load Dump Pulse, Drain-to-Source (Note 3) $R_{SOURCE} = 0.5~\Omega,~T = 300~ms) \\ (For Relay's Coils/Inductive Loads of 80~\Omega or Higher)~(T_J Initial = 85°C)$	60	V
E _{LD2}	Inductive Switching Transient 1, Drain–to–Source (Waveform: R_{SOURCE} = 10 Ω , T = 2.0 ms) (For Relay's Coils/Inductive Loads of 80 Ω or Higher) (T_J Initial = 85°C)	100	V
E _{LD3}	Inductive Switching Transient 2, Drain–to–Source (Waveform: R_{SOURCE} = 4.0 Ω , T = 50 μ s) (For Relay's Coils/Inductive Loads of 80 Ω or Higher) (T _J Initial = 85°C)	300	V
Rev-Bat	Reverse Battery, 10 Minutes (Drain-to-Source) (For Relay's Coils/Inductive Loads of 80 Ω or more)	-14	V
Dual-Volt	Dual Voltage Jump Start, 10 Minutes (Drain-to-Source)	28	V
ESD	Human Body Model (HBM) According to EIA/JESD22/A114 Specification	2000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Symbol	Rating	Value	Unit
T _A	Operating Ambient Temperature	-40 to 125	°C
T_J	Maximum Junction Temperature	150	°C
T _{STG}	Storage Temperature Range	-65 to 150	°C
P _D	Total Power Dissipation (Note 4) SOT–23 Derating above 25°C	225 1.8	mW mW/°C
P _D	Total Power Dissipation (Note 4) SC–74 Derating above 25°C	380 3.0	mW mW/°C
$R_{ hetaJA}$	Thermal Resistance, Junction-to-Ambient Minimum Copper SC-74 One Device Powered SC-74 Both Devices Equally Powered	556	°C/W
	300 mm ² Copper SOT-23 SC-74 One Device Powered SC-74 Both Devices Equally Powered	420	

- 1. Nonrepetitive current square pulse 1.0 ms duration.
- For different square pulse durations, see Figure 12.
 Nonrepetitive load dump pulse per Figure 3.
- 4. Mounted onto minimum pad board.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Drain to Source Sustaining Voltage $(I_D = 10 \text{ mA})$	V _{BRDSS}	61	66	70	V
Drain to Source Leakage Current $ (V_{DS} = 12 \text{ V}, V_{GS} = 0 \text{ V}) \\ (V_{DS} = 12 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125^{\circ}\text{C}) \\ (V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}) \\ (V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 125^{\circ}\text{C}) $	I _{DSS}	- - - -	- - - -	0.5 1.0 50 80	μΑ
Gate Body Leakage Current $ (V_{GS} = 3.0 \text{ V}, V_{DS} = 0 \text{ V}) \\ (V_{GS} = 3.0 \text{ V}, V_{DS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}) \\ (V_{GS} = 5.0 \text{ V}, V_{DS} = 0 \text{ V}) \\ (V_{GS} = 5.0 \text{ V}, V_{DS} = 0 \text{ V}, T_{J} = 125^{\circ}\text{C}) $	I _{GSS}	- - -	- - -	60 80 90 110	μΑ
ON CHARACTERISTICS					
Gate Threshold Voltage $(V_{GS} = V_{DS}, I_D = 1.0 \text{ mA})$ $(V_{GS} = V_{DS}, I_D = 1.0 \text{ mA}, T_J = 125^{\circ}\text{C})$	V _{GS(th)}	1.3 1.3	1.8 -	2.0 2.0	V
Drain to Source On–Resistance $ \begin{aligned} &(I_D=150 \text{ mA}, V_{GS}=3.0 \text{ V}) \\ &(I_D=150 \text{ mA}, V_{GS}=3.0 \text{ V}, T_J=125^\circ\text{C}) \\ &(I_D=150 \text{ mA}, V_{GS}=5.0 \text{ V}) \\ &(I_D=150 \text{ mA}, V_{GS}=5.0 \text{ V}, T_J=125^\circ\text{C}) \end{aligned} $	R _{DS(on)}	- - - -	- - - -	2.4 3.7 1.8 2.9	Ω
Output Continuous Current $(V_{DS} = 0.3 \text{ V}, V_{GS} = 5.0 \text{ V})$ $(V_{DS} = 0.3 \text{ V}, V_{GS} = 5.0 \text{ V}, T_J = 125^{\circ}\text{C})$	I _{DS(on)}	150 100	200 -	_ _	mA
Forward Transconductance $(V_{DS} = 12 \text{ V, I}_D = 150 \text{ mA})$	9FS	-	400	_	mmho
DYNAMIC CHARACTERISTICS	<u> </u>	-	-	-	-
Input Capacitance $(V_{DS} = 12 \text{ V}, V_{GS} = 0 \text{ V}, f = 10 \text{ kHz})$	C _{iss}	-	30	_	pf
Output Capacitance $(V_{DS} = 12 \text{ V}, V_{GS} = 0 \text{ V}, f = 10 \text{ kHz})$	C _{oss}	-	14	_	pf
Transfer Capacitance $(V_{DS} = 12 \text{ V}, V_{GS} = 0 \text{ V}, f = 10 \text{ kHz})$	C _{rss}	-	6.0	-	pf
SWITCHING CHARACTERISTICS	_	_	_		
Propagation Delay Times: High to Low Propagation Delay; Figure 2, $(V_{DS} = 12 \text{ V}, V_{GS} = 3.0 \text{ V})$ Low to High Propagation Delay; Figure 2, $(V_{DS} = 12 \text{ V}, V_{GS} = 3.0 \text{ V})$	t _{PHL} t _{PLH}	-	918 798	-	ns
High to Low Propagation Delay; Figure 2, $(V_{DS} = 12 \text{ V}, V_{GS} = 5.0 \text{ V})$ Low to High Propagation Delay; Figure 2, $(V_{DS} = 12 \text{ V}, V_{GS} = 5.0 \text{ V})$	t _{PHL} t _{PLH}	- -	331 1160	<u>-</u> -	
Transition Times: Fall Time; Figure 2, $(V_{DS} = 12 \text{ V}, V_{GS} = 3.0 \text{ V})$ Rise Time; Figure 2, $(V_{DS} = 12 \text{ V}, V_{GS} = 3.0 \text{ V})$	t _f t _r	<u>-</u> -	2290 618	- -	ns
Fall Time; Figure 2, (V_{DS} = 12 V, V_{GS} = 5.0 V) Rise Time; Figure 2, (V_{DS} = 12 V, V_{GS} = 5.0 V)	t _f t _r	<u>-</u> -	622 600	<u> </u>	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL WAVEFORMS

(T_J = 25°C unless otherwise specified)

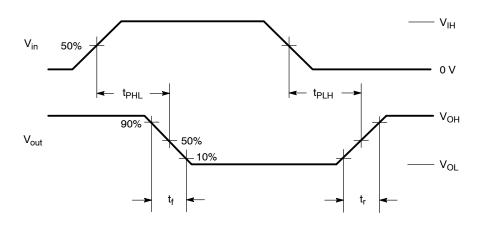


Figure 2. Switching Waveforms

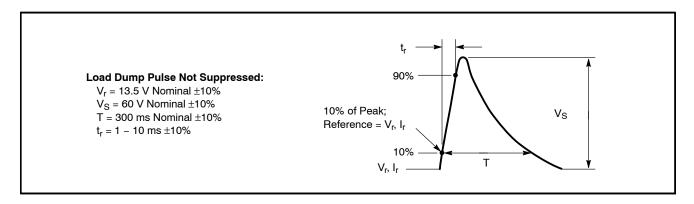
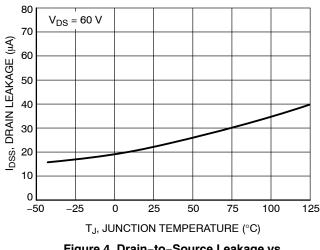


Figure 3. Load Dump Waveform Definition

TYPICAL PERFORMANCE CURVES

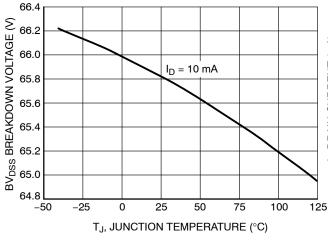
(T_J = 25°C unless otherwise specified)



80 I_{GSS} GATE LEAKAGE (µA) 60 $V_{GS} = 5 V$ 50 40 $V_{GS} = 3 V$ 30 20 -25 25 75 100 -50 50 125 T_J, JUNCTION TEMPERATURE (°C)

Figure 4. Drain-to-Source Leakage vs. Junction Temperature

Figure 5. Gate-to-Source Leakage vs. Junction Temperature



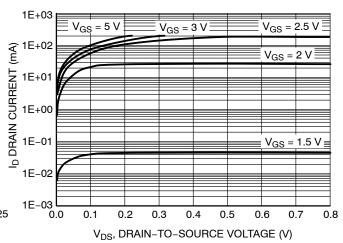
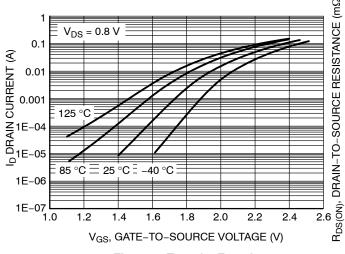


Figure 6. Breakdown Voltage vs. Junction Temperature

Figure 7. Output Characteristics



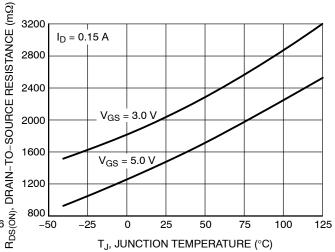


Figure 8. Transfer Function

Figure 9. On Resistance Variation vs Junction Temperature

TYPICAL PERFORMANCE CURVES

(T_J = 25°C unless otherwise specified)

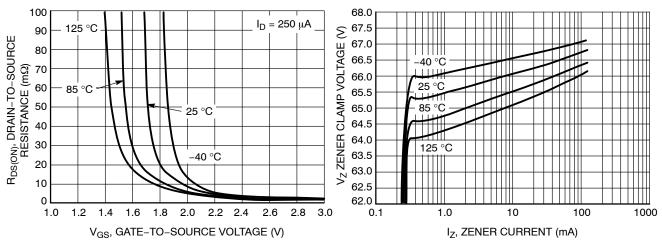


Figure 10. On Resistance Variation vs. Gate-to-Source Voltage

Figure 11. Zener Clamp Voltage vs. Zener Current

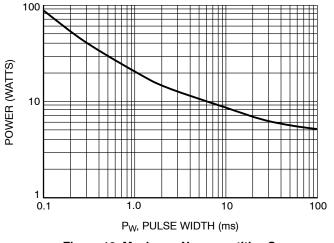


Figure 12. Maximum Non-repetitive Surge Power vs. Pulse Width

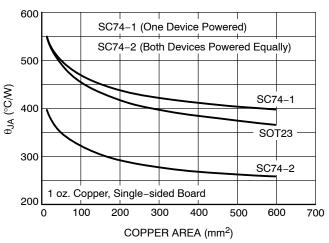


Figure 13. Thermal Performance vs. Board Copper Area

APPLICATIONS INFORMATION

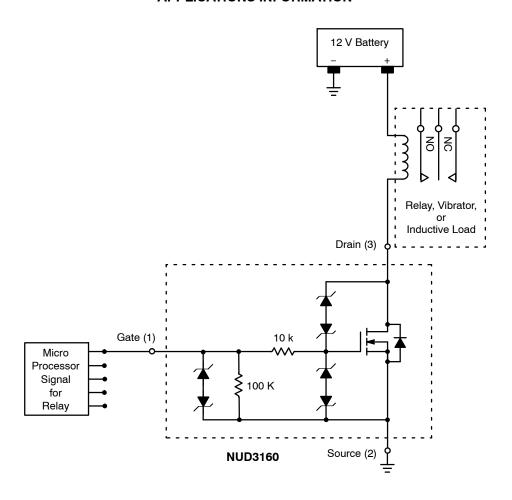


Figure 14. Applications Diagram

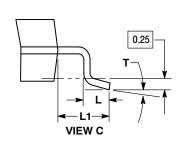


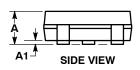
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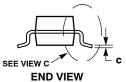
DATE 30 JAN 2018

SCALE 4:1 D - 3X b

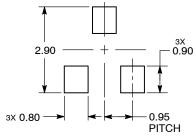
TOP VIEW







RECOMMENDED SOLDERING FOOTPRINT



DIMENSIONS: MILLIMETERS

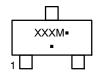
3. ANODE

NOTES:

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
 MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS				INCHES	
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
С	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
е	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	O٥		100	O٥		10°

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

= Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE		
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12:	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. SOURCE	PIN 1. CATHODE
2. ANODE	2. SOURCE	2. CATHODE	2. CATHODE	2. DRAIN	2. GATE
3. CATHODE	3. GATE	3. CATHODE-ANODE	3. ANODE	3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	PIN 1. CATHODE	PIN 1. CATHODE
2. CATHODE	2. CATHODE	2. ANODE	2. CATHODE	2. ANODE	2. ANODE
3. ANODE	3. CATHODE	3. CATHODE	3. ANODE	3. CATHODE-ANODE	3. GATE
STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
2. SOURCE	2. OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3. DRAIN	3. INPUT	3. CATHODE	3. SOURCE	3. GATE	3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE				

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3. CATHODE





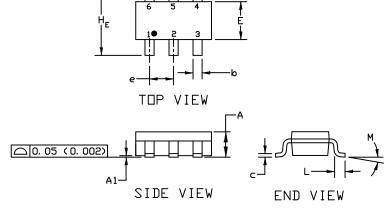
SC-74 CASE 318F ISSUE P

DATE 07 OCT 2021

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994
- 2. CONTROLLING DIMENSION: INCHES
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.

	MILLIMETERS				INCHES	
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
A	0. 90	1. 00	1. 10	0. 035	0. 039	0. 043
A1	0. 01	0. 06	0. 10	0. 001	0. 002	0. 004
ھ	0. 25	0. 37	0. 50	0. 010	0. 015	0. 020
С	0.10	0. 18	0. 26	0. 004	0. 007	0. 010
D	2. 90	3. 00	3. 10	0. 114	0. 118	0. 122
Ε	1. 30	1. 50	1. 70	0. 051	0. 059	0. 067
е	0. 85	0. 95	1. 05	0. 034	0. 037	0. 041
Η _E	2. 50	2. 75	3. 00	0. 099	0. 108	0. 118
L	0. 20	0. 40	0. 60	0. 008	0. 016	0. 024
М	0*		10*	0*		10*



GENERIC MARKING DIAGRAM*

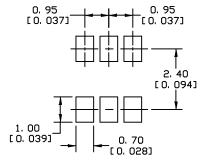


XXX = Specific Device Code

M = Date Code ■ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



For additional information on our Pb-Free strategy and soldering details, please download the UN Seniconductor Soldering and Mounting Techniques Reference Manual, SULDERRM/D.

SOLDERING FOOTPRINT

STYLE 1: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. ANODE 6. CATHODE	STYLE 2: PIN 1. NO CONNECTION 2. COLLECTOR 3. EMITTER 4. NO CONNECTION 5. COLLECTOR 6. BASE	STYLE 3: PIN 1. EMITTER 1 2. BASE 1 3. COLLECTOR 2 4. EMITTER 2 5. BASE 2 6. COLLECTOR 1	STYLE 4: PIN 1. COLLECTOR 2 2. EMITTER 1/EMITTER 2 3. COLLECTOR 1 4. EMITTER 3 5. BASE 1/BASE 2/COLLECTOR 3 6. BASE 3	STYLE 5: PIN 1. CHANNEL 1 2. ANODE 3. CHANNEL 2 4. CHANNEL 3 5. CATHODE 6. CHANNEL 4	STYLE 6: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE
STYLE 7: PIN 1. SOURCE 1 2. GATE 1 3. DRAIN 2 4. SOURCE 2 5. GATE 2 6. DRAIN 1	STYLE 8: PIN 1. EMITTER 1 2. BASE 2 3. COLLECTOR 2 4. EMITTER 2 5. BASE 1 6. COLLECTOR 1	STYLE 9: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	STYLE 10: PIN 1. ANODE/CATHODE 2. BASE 3. EMITTER 4. COLLECTOR 5. ANODE 6. CATHODE	STYLE 11: PIN 1. EMITTER 2. BASE 3. ANODE/CATHOD 4. ANODE 5. CATHODE 6. COLLECTOR	E

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