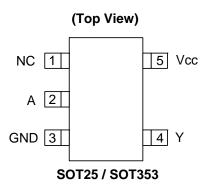


Description

The 74AHC1GU04 is a single inverter gate with a standard totem pole output. The device is designed for operation with a power supply range of 2.0V to 5.5V. The inverter can be used in analog circuits such as crystal oscillators.

Pin Assignments



Features

- Supply Voltage Range from 2.0V to 5.5V
- ± 6 mA Output Drive at 5.0V
- CMOS low power consumption
- Unbuffered Output
- ESD Protection Exceeds JESD 22
- 200-V Machine Model (A115-A)
- 2000-V Human Body Model (A114-A)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- SOT25 and SOT353: Assembled with "Green" Molding Compound (no Br, Sb)
- Lead Free Finish / RoHS Compliant (Note 1)

Applications

- Crystal Oscillators, Analog Inverters
- Wide array of products such as.
 - o PCs, networking, notebooks, netbooks, PDAs
 - o Computer peripherals, hard drives, CD/DVD ROM
 - o TV, DVD, DVR, set top box
 - o Personal Navigation / GPS
 - MP3 players ,Cameras, Video Recorders

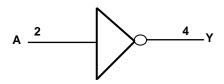
Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html.



Pin Descriptions

Pin Name	Pin NO.	Description			
NC	1	No Connection			
Α	2	Data Input			
GND	3	Ground			
Y	4	Data Output			
V _{CC}	5	Supply Voltage			

Logic Diagram



Function Table

Inputs	Output
Α	Υ
Н	L
L	Н



Absolute Maximum Ratings (Note 2)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD MM	Machine Model ESD Protection	200	V
V_{CC}	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state	-0.5 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I <0	-20	mA
lok	Output Clamp Current (V _O < 0 or V _O > V _{CC})	±20	mA
I _O	Continuous output current (V _O = 0 to V _{CC})	±25	mA
I _{CC}	Continuous current through V _{CC}	50	mA
I _{GND}	I _{GND} Continuous current through GND		mA
TJ	Operating Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

Recommended Operating Conditions (Note 3)

Symbol		Parameter	Min	Max	Unit
V_{CC}	Operating Voltage		2	5.5	V
		$V_{CC} = 2V$	1.7		
V_{IH}	High-level Input Voltage	$V_{CC} = 3V$	2.4		V
		$V_{CC} = 5.5V$	4.4		
		$V_{CC} = 2V$		0.3	
V_{IL}	Low-level input voltage	$V_{CC} = 3V$		0.6	V
		$V_{CC} = 5.5V$		1.1	
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V _{CC}	V
		$V_{CC} = 2V$		-50	uA
I _{OH}	High-level output current	$V_{CC} = 3.3V \pm 0.3V$		-3	m A
		$V_{CC} = 5V \pm 0.5V$		-6	mA
		$V_{CC} = 2V$		50	uA
I _{OL}	Low-level output current	$V_{CC} = 5V \pm 0.5V$		3	m A
		$V_{CC} = 3V$		6	mA
T _A	Operating free-air temperature		-40	85	°C

Notes: 3. Unused inputs should be held at $V_{\mbox{CC}}$ or Ground.



Electrical Characteristics

		T (O	.,		25°C		-40°C t	o 85°C	-40°C to	o 125ºC	
Symbol	Parameter	Test Conditions	V _{CC}	Min	Тур.	Max	Min	Max	Min	Max	Unit
			2V	1.8	2		1.75		1.75		
	High Level	$I_{OH} = -50\mu A$	3V	2.7	3		2.65		2.65		
V _{OH}	Output		4.5V	4.0	4.5		3.9		3.9		V
	Voltage	$I_{OH} = -3mA$	3V	2.58			2.5		2.5		
		$I_{OH} = -6mA$	4.5V	3.94			3.8		3.8		
			2V			0.2		0.2		0.2	
	Low Level	$I_{OL} = 50\mu A$	3V			0.3		0.3		0.3	
V _{OL}	Output		4.5V			0.5		0.5		0.5	V
	Voltage	$I_{OL} = 3mA$	3V			0.36		0.44		0.55	
		$I_{OL} = 6mA$	4.5V			0.36		0.44		0.55	
II	Input Current	$V_I = 5.5V$ or GND	0 to 5.5V			± 0.1		± 1		± 2	μA
I _{CC}	Supply Current	$V_I = 5.5V$ or GND $I_{O}=0$	5.5V			1		10		40	μA
Cı	Input Capacitance	$V_I = V_{CC} - \text{or GND}$	5.5V		2.0	10		10		10	pF
θ_{JA}	Thermal Resistance	SOT25	(Note 4)		195						°C/W
OJA	Julicuon-to-	SOT353	(14010-1)		430						. O, W
	Thermal Resistance	SOT25	(NI=4= 4)		58						0000
$\theta_{ m JC}$	Junction-to- Case	SOT353	(Note 4)		155						°C/W

Note: 4. Test conditions for SOT25, and SOT353: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout

Switching Characteristics

$V_{CC} = 3.3V \pm 0.3$ (see Figure 1)

Parameter	From	ТО			25°C		-40ºC t	o 85ºC	-40°C to	o 125ºC	Unit
Parameter	(Input) (OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Offic	
	۸		C _L =15pF	0.6	3.4	7.1	0.6	8.5	0.6	10.0	ns
t _{pd} A	Y	C _L =50pF	0.6	4.9	10.6	0.6	12.0	0.6	13.0	ns	

$V_{CC} = 5V \pm 0.5V$ (see Figure 1)

Doromotor	From TO				25°C		-40°C t	o 85ºC	-40°C to	125ºC	Unit
Parameter ((Input)	(OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Unit
4	^	V	C _L =15pF	0.6	2.6	5.5	0.6	6.0	0.6	7.0	ns
^l pd	A	r	C _L =50pF	0.6	3.6	7.0	0.6	8.0	0.6	9.0	ns

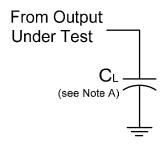


Operating Characteristics

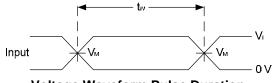
 $T_A = 25 \, {}^{\circ}C$

Parameter		Test Conditions	V _{CC} = 5V Typ.	Unit
C _{pd}	Power dissipation capacitance	f = 1 MHz No Load	8	pF

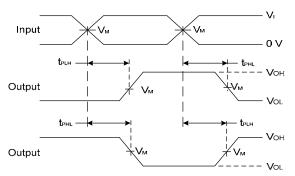
Parameter Measurement Information



V	Inp		V	
V _{CC}	VI	t _r /t _f	V _M	CL
3.3V±0.3V	V _{CC}	≤3ns	V _{CC} /2	15pF
5V±0.5V	V _{CC}	≤3ns	V _{CC} /2	15pF
3.3V±0.3V	V _{CC}	≤3ns	V _{CC} /2	50pF
5V±0.5V	V _{CC}	≤3ns	V _{CC} /2	50pF



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Figure 1. Load Circuit and Voltage Waveforms

Notes: A. Includes test lead and test apparatus capacitance.

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
- C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD}.



Ordering Information

74AHC1G U04 XX - 7 Logic Device **Function** Package **Packing** 74: Logic Prefix U04: 1-Input W5: SOT25

AHC: 2 to 5.5V

Family 1G: One gate

Unbuffered Inverter - Gate **SE: SOT353**

7: Tape & Reel

6	20
,	وح
(F	b)

	Device	Package Packag		7" Tape	and Reel	
	Device	Code	(Note 5)	Quantity	Part Number Suffix	
-	74AHC1GU04W5-7	W5	SOT25	3000/Tape & Reel	-7	
-	74AHC1GU04SE-7	SE	SOT353	3000/Tape & Reel	-7	

Notes: 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

Marking Information

(Top View)

5 4

XX Y WX

XX: Identification code Y: Year 0~9

 \underline{W} : Week: $A^{\sim}Z$: $1^{\sim}26$ week;

a~z: 27~52 week; z represents 52 and 53 week

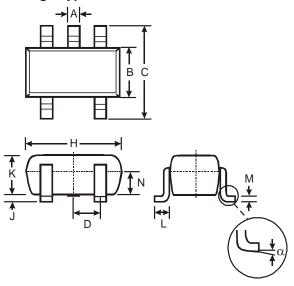
X: A~Z: Internal code 3

Part Number	Package	Identification Code
74AHC1GU04W5	SOT25	YP
74AHC1GU04SE	SOT353	YP



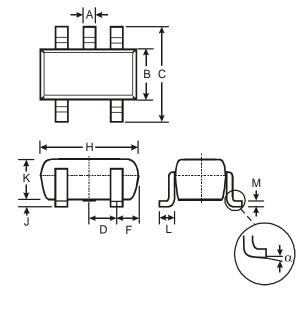
Package Outline Dimensions (All Dimensions in mm)

(1) Package Type: SOT25



SOT25				
Dim	Min	Max	Тур.	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
C	2.70	3.00	2.80	
D			0.95	
H	2.90	3.10	3.00	
7	0.013	0.10	0.05	
K	1.00	1.30	1.10	
L	0.35	0.55	0.40	
М	0.10	0.20	0.15	
N	0.70	0.80	0.75	
α	0°	8°		
All Dimensions in mm				

(2) Package Type: SOT353



SOT353				
Dim	Min	Max		
Α	0.10	0.30		
В	1.15	1.35		
C	2.00	2.20		
D	0.65 Typ			
F	0.40	0.45		
I	1.80	2.20		
7	0	0.10		
K	0.90	1.00		
L	0.25	0.40		
М	0.10	0.22		
α	0°	8°		
All Dimensions in mm				



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