



## ESD08V02D-CKN

#### Description

The ESD08V02D-CKN is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

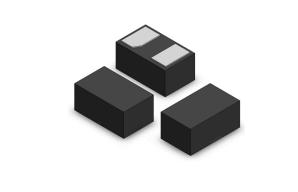
#### Feature

- Protects One Bidirectional I/O Line
- Low Clamping Voltage
- Surface mount package.
- Ultra small SMD package.
- Stand-off Voltage: 8.0 V
- Low leakage current
- ♦ 55 Watts Peak Pulse Power per Line (tp=8/20µs)
- IEC61000-4-5 (LIGHTING) 3.5A (8/20µs)
- Provides ESD protection to IEC61000-4-2(ESD):
  - ±20kV (air discharge)
  - ±15kV (contact discharge);

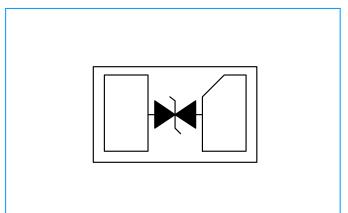
#### Applications

- Cell Phone Handsets and Accessories
- I<sup>2</sup>C Bus Protection
- Personal Digital Assistants (PDA)
- Notebooks, Desktops, and Servers
- Micro controller Input Protection
- Peripherals
- Parallel & Serial Port Protection

#### **Mechanical Characteristics**



#### **Functional Diagram**



#### **Mechanical Data**

- Case: 0201/DFN0603 package,molded plastic.
- Molding Compound Flammability Rating : UL 94V-O
- Weight 0.3 Milligrams (Approximate)
- Mounting position: Any

Symbol	Parameter	Value	Units		
P <sub>PP</sub>	Peak Pulse Power (tp=8/20µs waveform)	55	Watts		
ΤL	Lead Soldering Temperature	260 (10 sec.)	°C		
Тѕтс	Storage Temperature Range	-55 to +150	°C		
TJ	Operating Junction Temperature Range	-40 to +125	℃		

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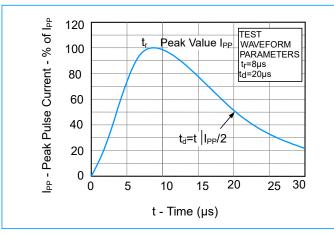


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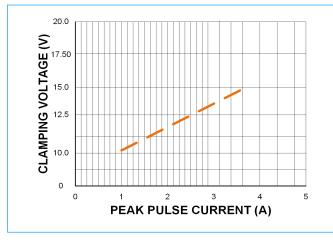
Electrical Characteristics (@ 25°C Unless Otherwise Specified )						
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Reverse Working Voltage	V <sub>RWM</sub>				8.0	V
Reverse Breakdown Voltage	$V_{BR}$	I⊤=1mA	8.5			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =8.0V, T=25°C;			1.0	μA
Junction capacitance	CJ	V <sub>R</sub> =0V, f=1MHz;		7.5		pF
	Vc	I <sub>PP</sub> =1.0A, T <sub>P</sub> =8/20μs;			10.6	V
Positive Clamping Voltage		I <sub>PP</sub> =2.0A, T <sub>P</sub> =8/20μs;			12.5	V
		I <sub>PP</sub> =3.5A, T <sub>P</sub> =8/20µs;		14.0	15.7	V
		I <sub>PP</sub> =1A		10.5		V
TLP Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> =8A		14.5		V
		I <sub>PP</sub> =16A		16.5		V

#### **Characteristic Curves**

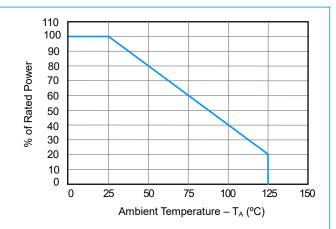
#### Fig1. 8/20µs Pulse Waveform



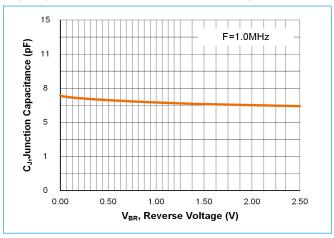
#### Fig3. Clamping Voltage vs. Peak Pulse Current



#### Fig2. Power Rating Derating Curve



#### Fig4. Typic Capacitance vs. Reverse Voltage



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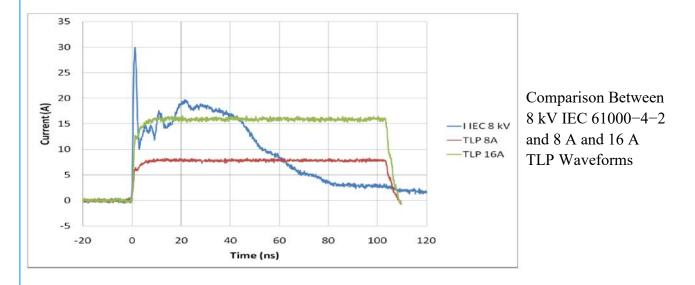




## ESD08V02D-CKN

#### Transmission Line Pulse (TLP)

Transmission Line Pulse (TLP) is a measurement technique used in the Electrostatic Discharge (ESD) arena to characterize performance attributes of devices under ESD stresses. TLP is able to obtain current versus voltage (I–V) curves in which each data point is obtained with a 100 ns long pulse, with currents up to 40 A. TLP was first used in the ESD field to study human body model (HBM) in integrated circuits, but it is an equally valid tool in the field of system level ESD. The applicability of TLP to system level ESD is illustrated in Figure 1, which compares an 8 KV IEC 61000–4–2 current waveform with TLP current pulses of 8 and 16 A. The current levels and time duration for the pulses are similar and the initial rise time for the TLP pulse is comparable to the rise time of the IEC 61000–4–2's initial current spike. This application note will give a basic introduction to TLP measurements and explain the datasheet parameters extracted from TLP for SDI Technology's protection products.



Comparison of a Current Waveform of IEC 61000-4-2 with TLP Pulses at 8 and 16 A.

The IEC 61000-4-2 ESD waveforms is true to the Standard and is shown here as captured on an oscilloscope. The points A, B, and C show the points on the waveforms specified in IEC 61000-4-2. Transmission Line Pulse (TLP) Version.

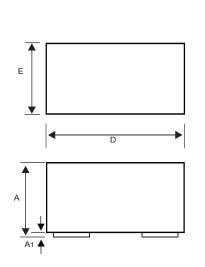




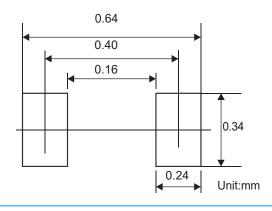
0201/DFN0603

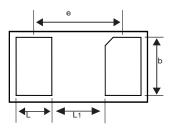
# ESD08V02D-CKN

#### 0201/DFN0603 Package Outline & Dimensions



Suggested PAD Layout





Symbol	Millimeters			
Symbol	Min	Nom	Max	
Α	0.270	0.300	0.340	
A1	0	0.020	0.050	
D	0.550	0.600	0.650	
E	0.250	0.300	0.350	
е	0.340REF			
L	0.140	0.180	0.240	
b	0.200	0.250	0.300	
L1	0.150REF			

#### **Ordering Information**

Device Marking		Package	Quantity	Reel Size
ESD08V02D-CKN	J	0201/DFN0603	10,000pcs/Reel	7 inch