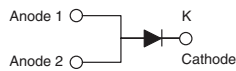
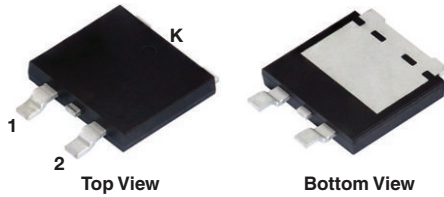


## Hyperfast Rectifier, 16 A FRED Pt<sup>®</sup>

### eSMP<sup>®</sup> Series SMPD (TO-263AC)



### LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS	
$I_{F(AV)}$	16 A
$V_R$	200 V
$V_F$ at $I_F$	0.75 V
$t_{rr}$	32 ns
$T_J$ max.	175 °C
Package	SMPD (TO-263AC)
Circuit configuration	Single

### FEATURES

- Hyperfast recovery time, reduced  $Q_{rr}$ , and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



### DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, telecom, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

### MECHANICAL DATA

**Case:** SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating  
Halogen-free, RoHS-compliant

**Terminals:** matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	$V_{RRM}$		200	V
Average rectified forward current	$I_{F(AV)}$	$T_{solder\ pad} = 153\text{ °C}$	16	A
Non-repetitive peak surge current	$I_{FSM}$	$T_J = 25\text{ °C}$ , 6 ms square pulse	250	

ELECTRICAL SPECIFICATIONS ( $T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}, V_R$	$I_R = 100\ \mu A$	200	-	-	V
Forward voltage	$V_F$	$I_F = 16\text{ A}$	-	0.91	1.0	
		$I_F = 16\text{ A}$ , $T_J = 150\text{ °C}$	-	0.75	0.84	
Reverse leakage current	$I_R$	$V_R = V_R$ rated	-	-	15	$\mu A$
		$T_J = 150\text{ °C}$ , $V_R = V_R$ rated	-	20	500	
Junction capacitance	$C_T$	$V_R = 200\text{ V}$	-	60	-	pF

DYNAMIC RECOVERY CHARACTERISTICS (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 1 A, di <sub>F</sub> /dt = 50 A/μs, V <sub>R</sub> = 30 V	-	32	-	ns
		I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A, I <sub>rr</sub> = 0.25 A	-	-	32	
		T <sub>J</sub> = 25 °C	-	26	-	
		T <sub>J</sub> = 125 °C	-	40	-	
Peak recovery current	I <sub>R</sub> RM	T <sub>J</sub> = 25 °C	-	2.8	-	A
		T <sub>J</sub> = 125 °C	-	6	-	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	-	37	-	nC
		T <sub>J</sub> = 125 °C	-	125	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	+175	°C
Thermal resistance, junction to mount	R <sub>thJM</sub>		-	1.1	1.4	°C/W
Approximate weight			0.55			g
			0.02			oz.
Marking device		Case style SMPD (TO-263AC)	16EDH02			

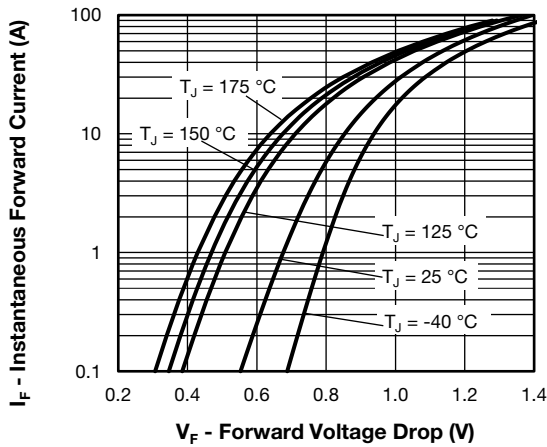


Fig. 1 - Typical Forward Voltage Drop Characteristics

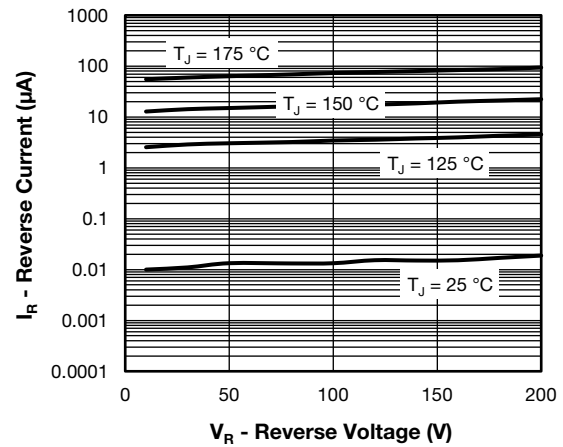


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

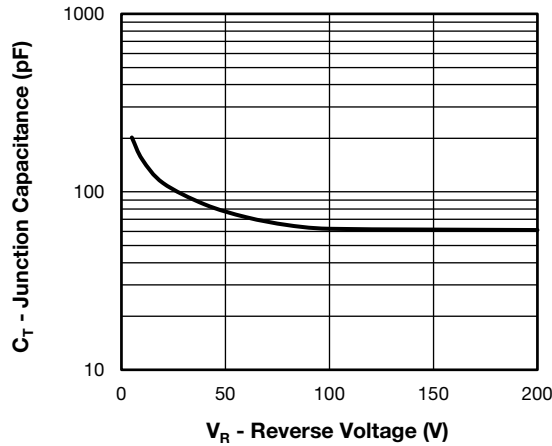


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

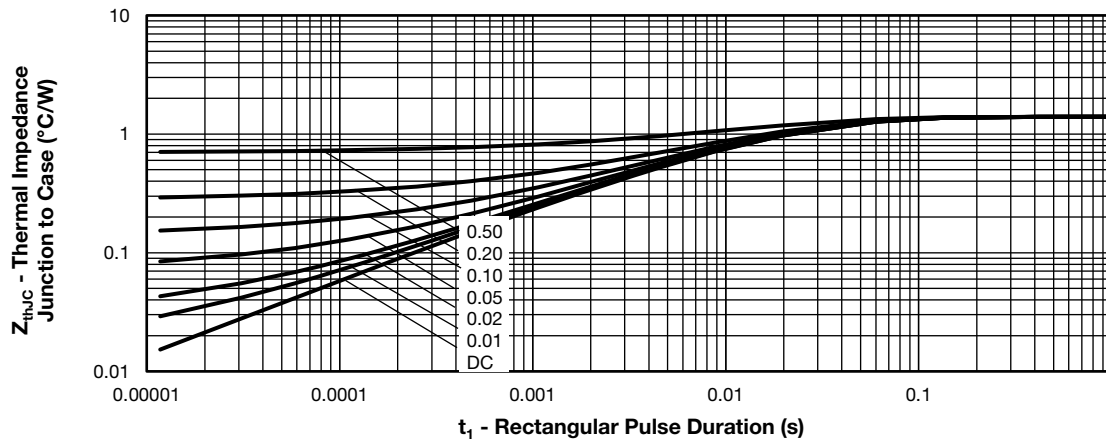


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

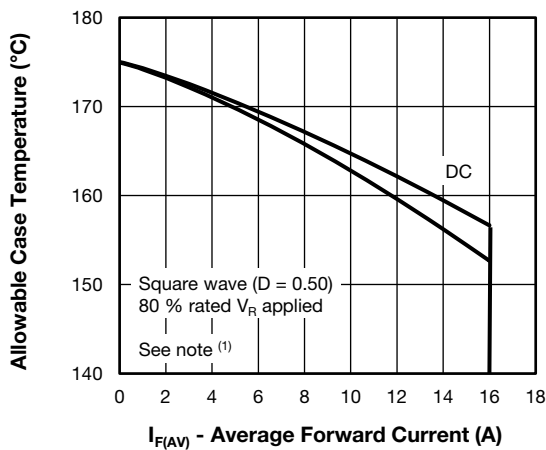


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

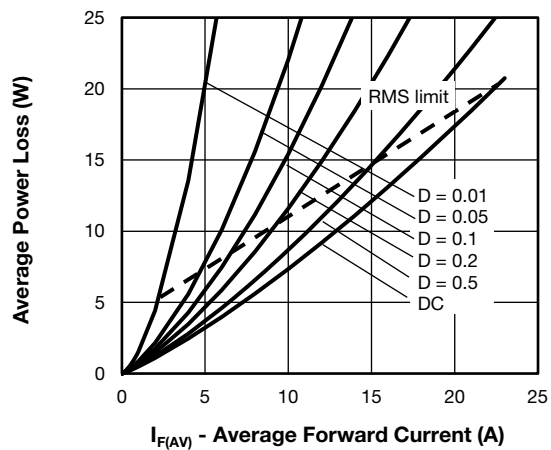


Fig. 6 - Forward Power Loss Characteristics

**Note**

- (1) Formula used:  $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$ ;  
 $P_d$  = forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 5);  
 $P_{dREV}$  = inverse power loss =  $V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1}$  = rated  $V_R$

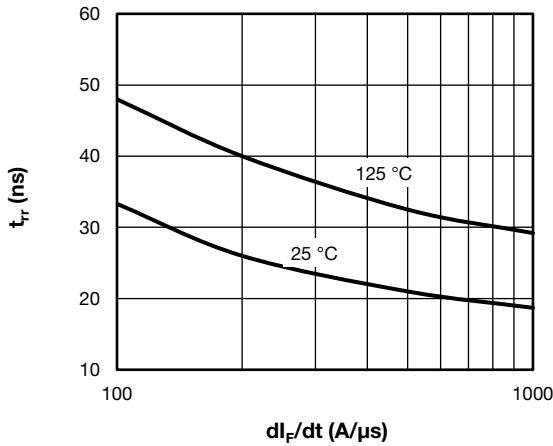


Fig. 7 - Typical Reverse Recovery Time vs.  $di_F/dt$

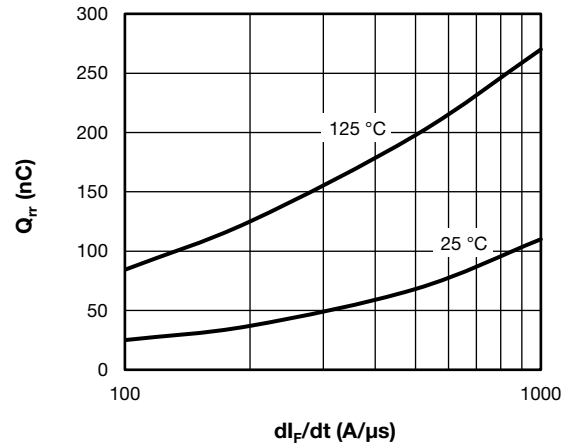


Fig. 8 - Typical Stored Charge vs.  $di_F/dt$

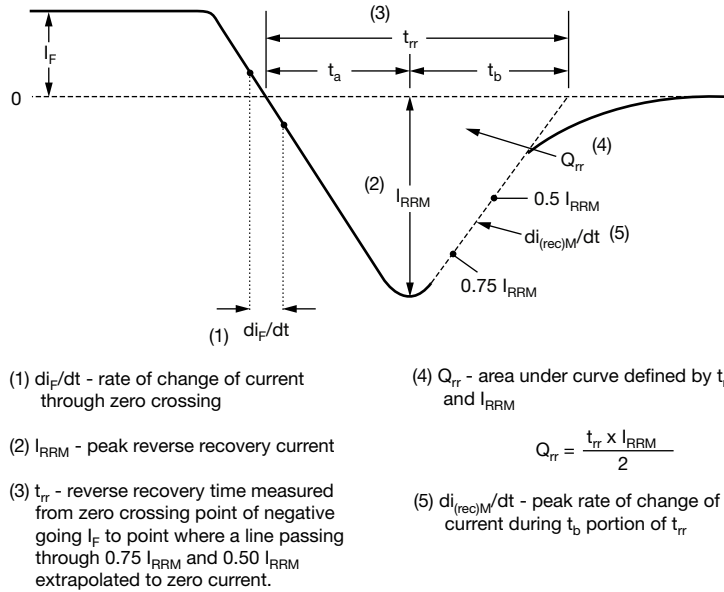
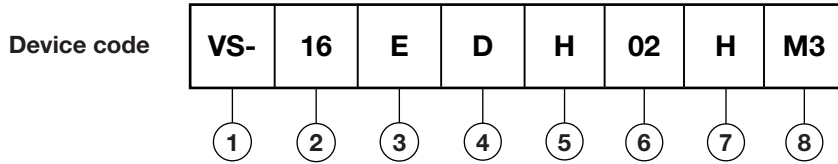


Fig. 9 - Reverse Recovery Waveform and Definitions



**ORDERING INFORMATION TABLE**



- 1** - Vishay Semiconductors product
- 2** - Current rating (16 A)
- 3** - Circuit configuration:  
E = single die
- 4** - D = SMPD package
- 5** - Process type,  
H = hyperfast recovery
- 6** - Voltage code (02 = 200 V)
- 7** - H = AEC-Q101 qualified
- 8** - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

<b>ORDERING INFORMATION</b> (Example)			
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-16EDH02HM3/I	2000	2000	13" diameter plastic tape and reel

<b>LINKS TO RELATED DOCUMENTS</b>	
Dimensions	<a href="http://www.vishay.com/doc?95604">www.vishay.com/doc?95604</a>
Part marking information	<a href="http://www.vishay.com/doc?95566">www.vishay.com/doc?95566</a>
Packaging information	<a href="http://www.vishay.com/doc?88869">www.vishay.com/doc?88869</a>
SPICE model	<a href="http://www.vishay.com/doc?96785">www.vishay.com/doc?96785</a>



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