

Silicon Carbide Power Schottky Diode

Features

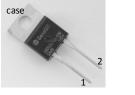
- 1200 V Schottky rectifier
- 175 °C maximum operating temperature
- Temperature independent switching behavior
- Superior surge current capability
- Positive temperature coefficient of V_{F}
- Extremely fast switching speeds
- Superior figure of merit Q_C/I_F

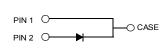
Advantages

- Improved circuit efficiency (Lower overall cost)
- Low switching losses
- Ease of paralleling devices without thermal runaway
- · Smaller heat sink requirements
- Low reverse recovery current
- Low device capacitance
- Low reverse leakage current at operating temperature

Package

RoHS Compliant





TO - 220AC

Applications

- Power Factor Correction (PFC)
- Switched-Mode Power Supply (SMPS)
- Solar Inverters
- Wind Turbine Inverters
- Motor Drives
- Induction Heating
- Uninterruptible Power Supply (UPS)
- High Voltage Multipliers

Maximum Ratings at T_i = 175 °C, unless otherwise specified

Parameter	Symbol	Conditions	Values	Unit
Repetitive peak reverse voltage	V _{RRM}		1200	V
Continuous forward current	I _F	T _c ≤ 155 °C	5	А
RMS forward current	F(RMS)	T _c ≤ 155 °C	8	А
Surge non-repetitive forward current, Half Sine Wave	I _{F,SM}	T _C = 25 °C, t _P = 10 ms T _C = 155 °C, t _P = 10 ms	32 26	А
Non-repetitive peak forward current	I _{F,max}	T _C = 25 °C, t _P = 10 μs	120	А
² t value	∫i² dt	T _C = 25 °C, t _P = 10 ms T _C = 155 °C, t _P = 10 ms	5 3.4	A ² s
Power dissipation	P _{tot}	T _C = 25 °C	117	W
Operating and storage temperature	T _j , T _{stg}		-55 to 175	°C

Electrical Characteristics at T_j = 175 °C, unless otherwise specified

Symbol V _F		25 °C	min.	typ. 1.6	max. 1.9	Unit
	I _F = 5 A, T _j = 1			1.6	1.9	
		/5°C		~ ~	~ ~	V
1		I _F = 5 A, T _j = 175 °C		2.6	3.0	
I _R	V _R = 1200 V, T _j = 25 °C V _R = 1200 V, T _i = 175 °C		5 10	50 100	μA	
Q _C	I _F ≤ I _{F,MAX}	V _R = 400 V V _R = 960 V		21 35		nC
ts	$T_j = 175 °C$	V _R = 400 V V _R = 960 V		< 25		ns
С	V _R = 400 V, f = 1 MH	z, T _j = 25 °C		260 25 20		pF
R _{thJC}				1.4		°C/W
N.4				0.6		Nm
	Q _C t _s	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	$\begin{array}{c c c c c c c c c c c c c c c c c c c $

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V_{RRM}

Qc

I_{F (Tc = 25°C)}

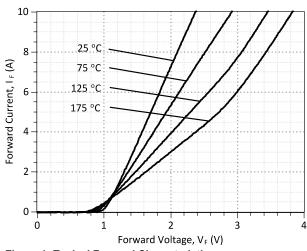
1200 V

12 A

21 nC



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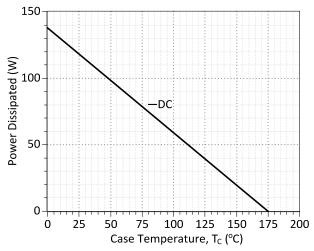


Figure 3: Power Derating Curve

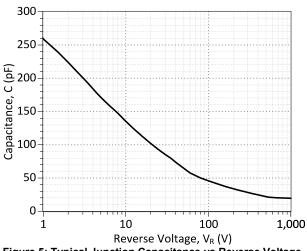
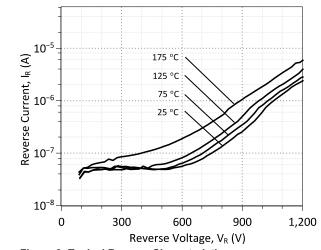
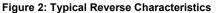


Figure 5: Typical Junction Capacitance vs Reverse Voltage Characteristics





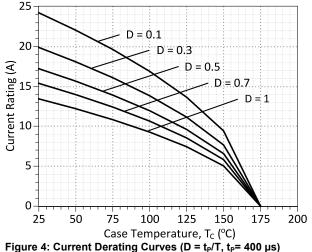
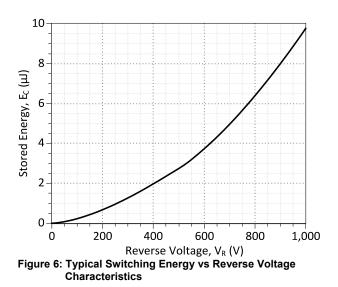
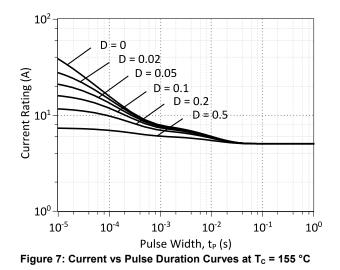


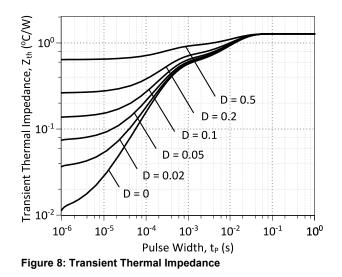
Figure 4: Current Derating Curves (D = t_P/T , t_P = 400 µs) (Considering worst case Z_{th} conditions)



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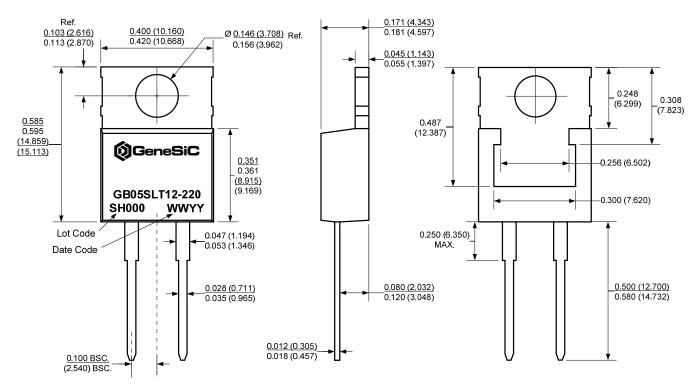
GeneSiC SEMICONDUCTOR



Package Dimensions:



PACKAGE OUTLINE



NOTE

1. CONTROLLED DIMENSION IS INCH. DIMENSION IN BRACKET IS MILLIMETER.

2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS



GB05SLT12-220

Revision History							
Date	Revision	Comments	Supersedes				
2014/08/26	3	Updated Electrical Characteristics					
2013/02/05	2	Second generation update					
2012/05/22	1	Second generation release					
2010/12/14	0	Initial release					

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SPICE Model Parameters

Copy the following code into a SPICE software program for simulation of the GB05SLT12-220 device.

```
*
     MODEL OF GeneSiC Semiconductor Inc.
*
*
    $Revision: 1.0
                                $
*
     $Date: 04-SEP-2013
                               $
*
    GeneSiC Semiconductor Inc.
*
*
     43670 Trade Center Place Ste. 155
*
    Dulles, VA 20166
*
    http://www.genesicsemi.com/index.php/sic-products/schottky
    COPYRIGHT (C) 2013 GeneSiC Semiconductor Inc.
*
*
    ALL RIGHTS RESERVED
* These models are provided "AS IS, WHERE IS, AND WITH NO WARRANTY
* OF ANY KIND EITHER EXPRESSED OR IMPLIED, INCLUDING BUT NOT LIMITED
* TO ANY IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A
* PARTICULAR PURPOSE."
* Models accurate up to 2 times rated drain current.
* Start of GB05SLT12-220 SPICE Model
.SUBCKT GB05SLT12 ANODE KATHODE
R1 ANODE INT R=((TEMP-24)*0.0015); Temperature Dependant Resistor
D1 INT KATHODE GB05SLT12 25C; Call the 25C Diode Model
D2 ANODE KATHODE GB05SLT12 PIN; Call the PiN Diode Model
.MODEL GB05SLT12 25C D
+ IS 5.83E-18
                                     0.1276
                          RS
+ N
         1
                         IKF
                                     602
         1.2
+ EG
                         XTI
                                     3
+ CJO
                                    0.419
         3.00E-10
                         VJ
+ M
         1.6
                         FC
                                    0.5
+ TT
        1.00E-10
1.00E-03
                         BV
                                     1200
+ IBV
                         VPK
                                    1200
                                    SiC Schottky
+ IAVE
         5
                          TYPE
+ MFG GeneSiC Semiconductor
.MODEL GB05SLT12 PIN D
      3.50 E-12
                                   0.3648
+ IS
                        RS
+ N
         4.409
                                     73
                         IKF
+ EG
         3.23
                         XTI
                                    -6
+ FC
         0.5
                         TT
                                     0
+ BV
         1200
                         IBV
                                    1.00E-03
+ VPK
         1200
                         IAVE
                                     1
+ TYPE SiC_PiN
.ENDS
* End of GB05SLT12-220 SPICE Model
```

Mouser Electronics

Authorized Distributor

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