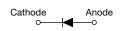
FREE

Vishay Semiconductors

High Performance Schottky Rectifier, 1.0 A



www.vishay.com



SMB (DO-214AA)

PRIMARY CHARACTERISTICS					
I _{F(AV)}	1.0 A				
V _R	60 V				
V _F at I _F	0.42 V				
I _{RM}	8 mA at 125 °C				
T _J max.	150 °C				
E _{AS}	2.0 mJ				
Package	SMB (DO-214AA)				
Circuit configuration	Single				

FEATURES

- · Low forward voltage drop
- Guard ring for enhanced ruggedness and long RoHS term reliability COMPLIANT HALOGEN
- · Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- · Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-10BQ060HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS					
I _{F(AV)}	Rectangular waveform	1.0	А				
V _{RRM}		60	V				
I _{FSM}	t _p = 5 μs sine	700	А				
V _F	1.0 A _{pk} , T _J = 125 °C	0.42	V				
TJ	Range	-55 to +150	°C				

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10BQ060HM3	UNITS		
Maximum DC reverse voltage	V _R	60	V		
Maximum working peak reverse voltage	V _{RWM}	80	v		

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 116 °C, rectangular waveform		1.0	А		
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated	700			
non-repetitive surge current	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	42	A		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 4 mH		2.0	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А		

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			
	V _{FM} ⁽¹⁾	1 A	T.I = 25 °C	0.49	V	
Maximum forward voltage drop See fig. 1		2 A	1J=25 C	0.60		
		1 A	T 105 %C	0.42		
		2 A	T _J = 125 °C	0.56		
Maximum reverse leakage current		T _J = 25 °C	V Deted V	0.1	mA	
See fig. 2	I _{RM}	T _J = 125 °C	V _R = Rated V _R	8.0		
Typical junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		80	pF	
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH	
Maximum voltage rate of charge	dV/dt	Rated V _R 10 000 V/µ			V/µs	

Note

 $^{(1)}\,$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	36	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/W	
A			0.10	g	
Approximate weight			0.003	oz.	
Marking device		Case style SMB (DO-214AA)	1H		

Notes

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$ (1)

(2) Mounted 1" square PCB



VS-10BQ060HM3

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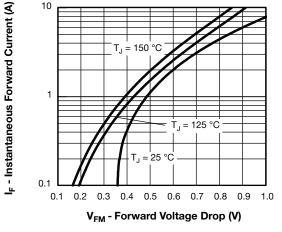


Fig. 1 - Maximum Forward Voltage Drop Characteristics

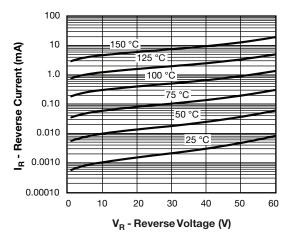


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

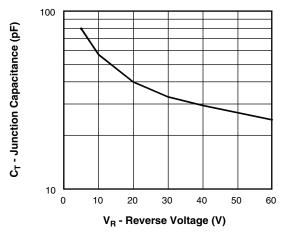
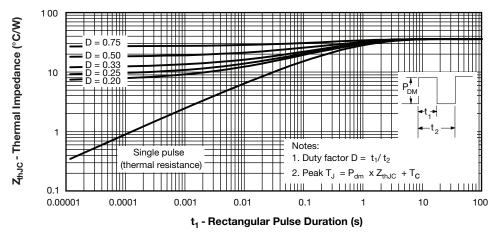


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage





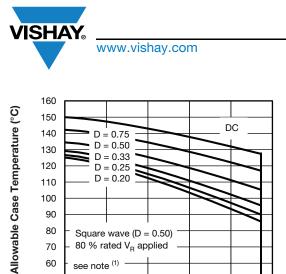
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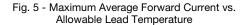
I_{F(AV)} - Average Forward Current (A)

0.9

1.2

1.5

0.6



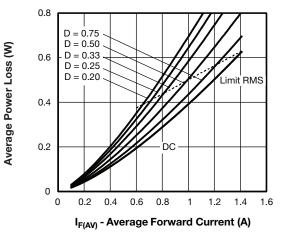


Fig. 6 - Maximum Average Forward Dissipation vs. Average Forward Current

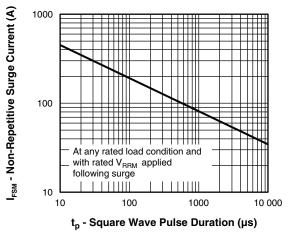


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

50

0.0

0.3

- (1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
 - Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6);

Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE

Device code	VS-	10	в	Q	060	н	М3
		2	3	4	5	6	7
	1 .	· Vis	hay Sen	niconduc	ctors pro	oduct	
	2 -	- Cur	rent rati	ng			
	3 -	• В=	SMB				
	4	- Q =	Schott	ky "Q" se	eries		
	5 -	- Vol	tage rati	ng (060	= 60 V))	
	6 -	• Н=	AEC-Q	101 qua	lified		
	7 -	- Env	vironmer	ntal digit	:		
		М3	= halog	en-free,	RoHS o	complia	nt and t

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION						
VS-10BQ060HM3/5BT	5BT	3200	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95401			
Part marking information	www.vishay.com/doc?95536			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?95638			

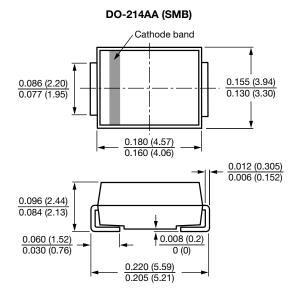


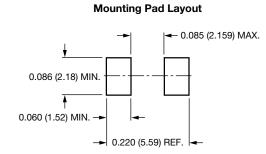
Outline Dimensions

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SMB

DIMENSIONS in inches (millimeters)







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