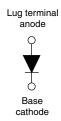


# HEXFRED® Ultrafast Soft Recovery Diode, 180 A





Single diode

HAI	F_D	٨K	(D-67)
	F	$\Delta$	(10-01)

Circuit configuration

PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	180 A				
$V_{R}$	400 V				
I <sub>F(DC)</sub> at T <sub>C</sub>	200 A at 100 °C				
Package	HALF-PAK (D-67)				

#### **FEATURES**

- Very low Q<sub>rr</sub> and t<sub>rr</sub>
- Designed and qualified for industrial level



- UL approved file E222165
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### **BENEFITS**

- · Reduced RFI and EMI
- Reduced snubbing

#### **DESCRIPTION**

HEXFRED® diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and dI<sub>F</sub>/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motors drives and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Cathode to anode voltage	$V_R$		400	V	
Continuous forward current	I <sub>F</sub>	T <sub>C</sub> = 25 °C	395		
Continuous forward current		T <sub>C</sub> = 100 °C	200	Α	
Single pulse forward current	I <sub>FSM</sub>	Limited by junction temperature	1200		
Non-repetitive avalanche energy	E <sub>AS</sub>	$L = 100 \mu H$ , duty cycle limited by maximum $T_J$	1.4	mJ	
Maximum power dissipation	P <sub>D</sub>	$T_{\rm C} = 25 {\rm ^{\circ}C}$ 657		W	
		T <sub>C</sub> = 100 °C	263	VV	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C	

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	$V_{BR}$	Ι <sub>R</sub> = 100 μΑ		400	-	-	
		I <sub>F</sub> = 180 A		-	1.08	1.46	V
Maximum forward voltage	$V_{FM}$	I <sub>F</sub> = 360 A	See fig. 1	-	1.22	1.8	
		I <sub>F</sub> = 180 A, T <sub>J</sub> = 125 °C		-	0.99	1.34	
Maximum reverse leakage current	I <sub>RM</sub>	T <sub>J</sub> = 125 °C, V <sub>R</sub> = 400 V See fig		-	-	4	mA
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 200 V	See fig. 3	-	370	500	pF
Series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		-	6.0	-	nΗ



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time See fig. 5	t <sub>rr</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 135 A dI <sub>F</sub> /dt = 200 A/μs V <sub>R</sub> = 200 V	-	90	140	ns
		T <sub>J</sub> = 125 °C		-	280	440	
Peak recovery current See fig. 6	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	9	16	A nC
		T <sub>J</sub> = 125 °C		-	18	32	
Reverse recovery charge See fig. 7	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	300	950	
		T <sub>J</sub> = 125 °C		-	2650	6300	
Peak rate of recovery current See fig. 8	dl <sub>(rec)M</sub> /dt	T <sub>J</sub> = 25 °C		-	300	-	A/µs
		T <sub>J</sub> = 125 °C		-	290	-	ΑνμS

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to +150	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation See fig. 4	0.19	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.05	C/VV	
A construction while				30	g	
Approximate weight				1.06	OZ.	
Mounting torque	minimum			3 (26.5)		
Mounting torque	maximum			4 (35.4)	N⋅m	
Terminal torque	minimum			3.4 (30)	(lbf·in)	
	maximum			5 (44.2)		
Case style			HALF-PAK (D-6	7)		

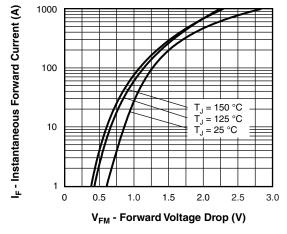


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

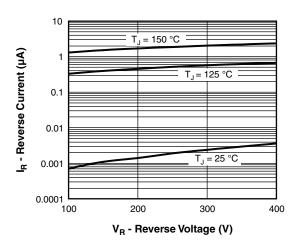


Fig. 2 - Typical Reverse Current vs. Reverse Voltage



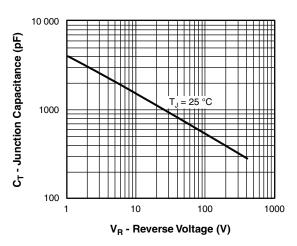


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

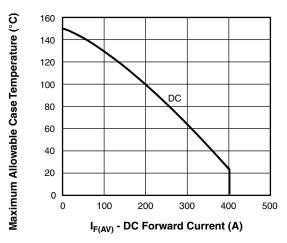


Fig. 4 - Maximum Allowable Case Temperature vs. DC Forward Current

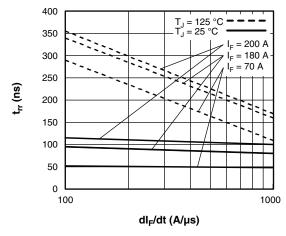


Fig. 5 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

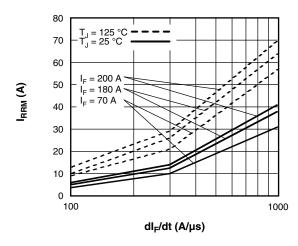


Fig. 6 - Typical Recovery Current vs. dI<sub>F</sub>/dt

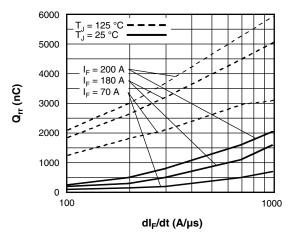


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

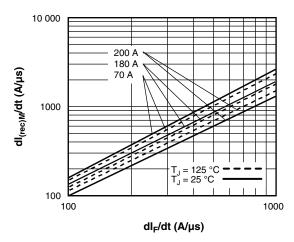


Fig. 8 - Typical dl<sub>(rec)M</sub>/dt vs. dl<sub>F</sub>/dt

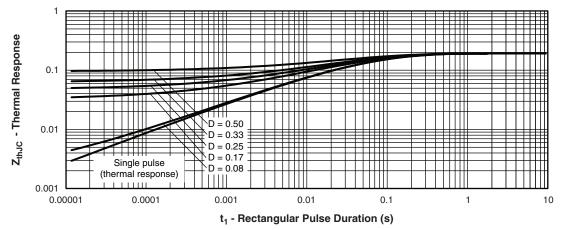


Fig. 9 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

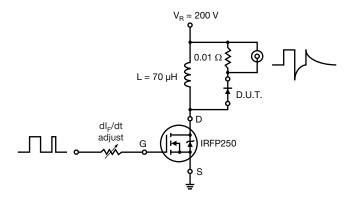
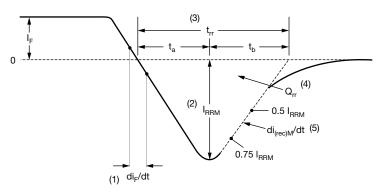


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2) I<sub>RRM</sub> peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_{rr}$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during  $t_b$  portion of  $t_{rr}$ 

Fig. 11 - Reverse Recovery Waveform and Definitions



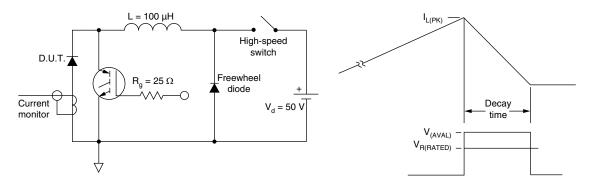
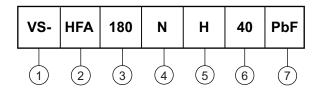


Fig. 12 - Avalanche Test Circuit and Waveforms

#### **ORDERING INFORMATION TABLE**





1 - Vishay Semiconductors product

2 - HEXFRED® family, electron irradiated

Average current rating

4 - N = not isolated

5 - H = HALF-PAK (D-67)

6 - Voltage rating (400 V)

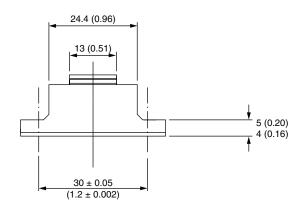
7 - Lead (Pb)-free

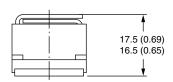
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95020			

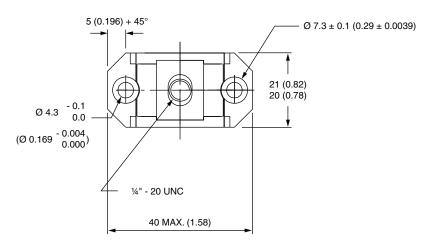


### **D-67 HALF-PAK**

#### **DIMENSIONS** in millimeters (inches)









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