HALOGEN

FREE



Vishay General Semiconductor

Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I _{F(AV)}	7.0 A		
V _{RRM}	45 V		
I _{FSM}	120 A		
V_F at $I_F = 7.0$ A $(T_A = 125 ^{\circ}C)$	0.40 V		
T _J max.	150 °C		
Package	SMB (DO-214AA)		
Circuit configuration	Single		

FEATURES

- Low profile package
- · Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- · Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMB (DO-214AA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test **Polarity:** color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	VSSB7L45	UNIT	
Device marking code		7L45		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current	I _F ⁽¹⁾	7.0	Α	
	I _F ⁽²⁾	3.8		
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	120	А	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Notes

- (1) Mounted on 3 cm x 3 cm pad areas, 2 oz. PCB
- (2) Free air, mounted on recommended copper pad area



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	$I_F = 3.5 A$		V _F ⁽¹⁾	0.43	-	V
	I _F = 7.0 A			0.49	0.57	
	I _F = 3.5 A	T _A = 125 °C		0.32	-	
	I _F = 7.0 A			0.40	0.48	
Reverse current	V _R = 45 V	T _A = 25 °C T _A = 125 °C	I _R ⁽²⁾	-	1.6	- mA
	$V_R = 45 \text{ V}$ $T_A = 125 \text{ °C}$	IR (=/	10	30] IIIA	
Typical junction capacitance	4.0 V, 1 MHz		CJ	1068	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise specified)				
PARAMETER SYMBOL VSSB7L45			UNIT	
Typical thermal resistance	R _{0JA} (1)	90	°C/W	
	R _{0JM} (2)	10		

Notes

 $^{(1)}\,$ Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient

 $^{(2)}$ Units mounted on 3 cm x 3 cm Aluminum, 2 oz. pad area; thermal resistance $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N UNIT WEIGHT (g) PREFERRED PACKAGE CODE		BASE QUANTITY	DELIVERY MODE		
VSSB7L45-M3/52T	0.096	52T	750	7" diameter plastic tape and reel	
VSSB7L45-M3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel	

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

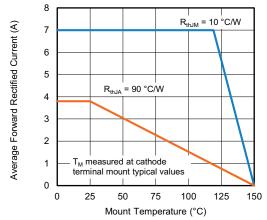


Fig. 1 - Maximum Forward Current Derating Curve

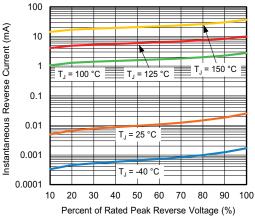


Fig. 4 - Typical Reverse Characteristics

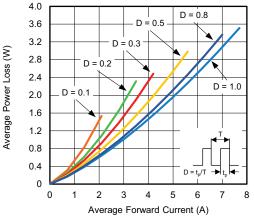


Fig. 2 - Forward Power Loss Characteristics

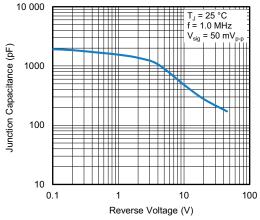


Fig. 5 - Typical Junction Capacitance

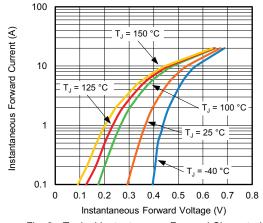


Fig. 3 - Typical Instantaneous Forward Characteristics

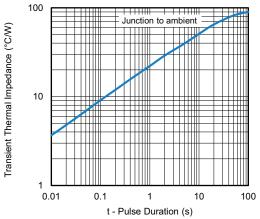


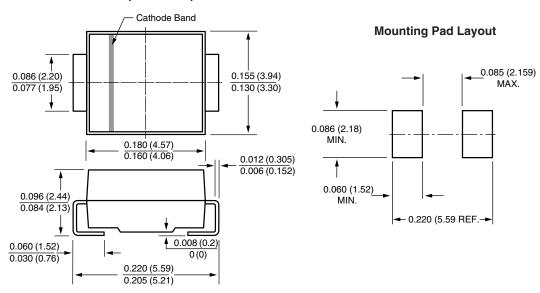
Fig. 6 - Typical Transient Thermal Impedance



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SMB (DO-214AA)





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