440L Series



Vishay Cera-Mite

EMI Suppression Capacitor, Ceramic Disc, Class X1, 760 V_{AC}, Class Y1, 500 V_{AC}



LINKS TO ADDITIONAL RESOURCES





QUICK REFERENCE DATA					
DESCRIPTION	VALUE				
Ceramic class	-	1 2			
Ceramic dielectric	C0G, C0G, U2J, U2J, P3K, P3K, R3L R3L		X7R, Y5U	X7R, Y5U	
Voltage (V _{AC})	500	760	500	760	
Min. capacitance (pF)) 10 68			8	
Max. capacitance (pF)	47 20 000			000	
Mounting	Radial				

INSULATION RESISTANCE

Min. 1000 ΩF

TOLERANCE ON CAPACITANCE

± 10 %; ± 20 %

DISSIPATION FACTOR

2.0 % max. at 1 kHz; 1 V

CERAMIC DIELECTRIC

C0G, U2J, P3K, R3L (class 1) X7R, Y5U (class 2)

CLIMATIC CATEGORY ACC. TO EN 60068-1 25/125/21

OPERATING TEMPERATURE RANGE

-30 °C to +125 °C (1)

Note

⁽¹⁾ For explanation about the difference of operating temperature range and temperature characteristic of capacitance please see www.vishav.com/doc?48299)

FEATURES

- Complies with IEC 60384-14
- Complies with IEC 60384-14 annex I humidity robustness grade IIIB (highest)
- · High reliability
- Radial leads
- High capacitance up to 20 nF
- Singlelayer AC disc safety capacitors
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- X1, Y1 according to IEC 60384-14
- Line-to-line filtering (Class X)
- Line-to-ground filtering (Class Y)
- Primary and secondary coupling (SMPS)
- Applications at large temperature and / or high humidity
- EMI / RFI suppression and filtering

DESIGN

The capacitors consist of a ceramic disc of which both sides are silver-plated. Connection leads are made of tinned copper having a diameter of 0.032" (0.81 mm). The capacitors may be supplied with radial kinked or straight leads having a lead spacing of 0.375" (9.5 mm). The standard tolerances are ± 10 % or ± 20 %. Coating is made of flame-retardant epoxy resin in accordance with "UL 94 V-0."

CAPACITANCE RANGE

10 pF to 20 nF

RATED VOLTAGE

IEC 60384-14:

- X1: 760 V_{AC}, 50 Hz
- Y1: 500 V_{AC}, 50 Hz

DIELECTRIC STRENGTH BETWEEN LEADS

Component test, 100 % test at production line: 4000 V_{AC}, 50 Hz, 2 s

As repeated test at customer side admissible only once with: 3600 V_{AC}, 50 Hz, 2 s

Random sampling test (destructive test): 4000 V_{AC}, 50 Hz, 60 s

DIELECTRIC STRENGTH OF BODY INSULATION

4000 V_{AC}, 50 Hz, 60 s (destructive test)



COMPLIANT

For technical questions, contact: ceramitesupport@vishay.com

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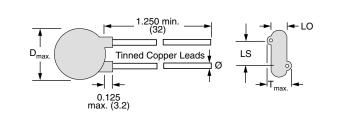


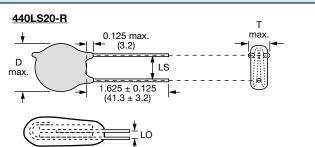
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DIMENSIONS in inches (millimeters)

ISHA





		р	т	W	IRE SIZE	LS	LO	
C (pF)	TOL. (%)	D _{max.} DIAMETER INCH (mm)	T _{max.} THICKNESS INCH (mm)	AWG	INCH (mm)	LEAD SPACE INCH (mm) ± 1 mm	LEAD OFFSET INCH (mm) ± 0.5 mm	ORDERING CODE
COG				_				
10	± 10	0.330 (8.4)	0.195 (5.0)	20	0.032 (0.81)	0.375 (9.5)	0.098 (2.5)	440LQ10-F
U2J								
15	± 10	0.330 (8.4)	0.210 (5.3)	20	0.032 (0.81)	0.375 (9.5)	0.110 (2.8)	440LQ15-F
P3K								
22	± 10	0.330 (8.4)	0.190 (4.8)	20	0.032 (0.81)	0.375 (9.5)	0.094 (2.4)	440LQ22-F
R3L								
33	± 10	0.330 (8.4)	0.200 (5.1)	20	0.032 (0.81)	0.375 (9.5)	0.102 (2.6)	440LQ33-F
47	± 10	0.330 (8.4)	0.180 (4.6)	20	0.032 (0.81)	0.375 (9.5)	0.083 (2.1)	440LQ47-F
X7R	1 1		0.000 (7.0)					4401.000 -
68	4		0.220 (5.6)	_			0.122 (3.1)	440LQ68-F
100			0.220 (5.6)				0.122 (3.1)	440LT10-R
150	± 10	0.330 (8.4)	0.235 (6.0)	20	0.032 (0.81)	0.375 (9.5)	0.138 (3.5)	440LT15-R
220	_		0.235 (6.0)	_			0.138 (3.5)	440LT22-R
330			0.225 (5.7)				0.126 (3.2)	440LT33-R
Y5U	1 1		0.000 (5.0)	1	[1	0, 100, (0, 0)	
470	-	0.330 (8.4)	0.230 (5.8)	_			0.130 (3.3)	440LT47-R
560	-	0.330 (8.4)	0.230 (5.8)	_			0.130 (3.3)	440LT56-F
680		0.330 (8.4)	0.235 (6.0)	4			0.138 (3.5)	440LT68-R
1000		0.365 (9.3)	0.225 (5.7)	4			0.126 (3.2)	440LD10-F
1500		0.365 (9.3)	0.220 (5.6)	4			0.118 (3.0)	440LD15-F
2000	-	0.400 (10.2)	0.220 (5.6)	4			0.118 (3.0)	440LD20-F
2200 2700	-	0.430 (10.9)	0.225 (5.7)	-			0.126 (3.2)	440LD22-R
	-	0.460 (11.7)	0.225 (5.7)	-			0.126 (3.2)	440LD27-R
2800 3000		0.460 (11.7) 0.490 (12.4)	0.220 (5.6) 0.225 (5.7)	-			0.122 (3.1)	440LD28-F 440LD30-F
3000		0.490 (12.4)	0.225 (5.7)	4			0.126 (3.2) 0.122 (3.1)	440LD30-F 440LD32-F
3200	± 20	0.490 (12.4)	0.220 (5.6)	20	0.032 (0.81)	0.375 (9.5)	0.122 (3.1)	440LD32-F
3900	± 20	0.530 (12.4)	0.220 (5.6)	20	0.032 (0.01)	0.373 (9.3)	0.122 (3.1)	440LD33-F 440LD39-F
4000		0.530 (13.5)	0.220 (5.6)	+			0.122 (3.1)	440LD39-F
4700		0.620 (15.7)	0.230 (5.8)	+			0.122 (3.1)	440LD40-F 440LD47-F
5000		0.620 (15.7)	0.225 (5.7)	-			0.126 (3.2)	440LD47-F
5500		0.680 (17.3)	0.223 (5.7)	+			0.134 (3.4)	440LD30-F
5600		0.680 (17.3)	0.230 (5.8)	+			0.134 (3.4)	440LD35-F
6800	-	0.720 (18.3)	0.235 (6.0)	+			0.138 (3.5)	440LD68-F
8000	-	0.720 (18.3)	0.220 (5.6)	+			0.122 (3.1)	440LD80-F
9000		0.790 (20.1)	0.225 (5.7)	4			0.126 (3.2)	440LD00-F
10 000		0.850 (21.6)	0.230 (5.8)	4			0.134 (3.4)	440LS10-F
20 000		0.850 (21.6)	0.355 (9.0)	4	1		0.134 (3.4)	440LS20-R

Notes

Alternate lead spacings are available bulk or tape and reel on request

• Minimum lead clearance according to IEC 60384-14: 0.315" (8 mm)

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Vishay Cera-Mite

WIRE LEAD OPTIONS - BULK PACKAGED

Radial leaded capacitors may be ordered with various wire lead options by adding appropriate suffix code to the catalog part number.

Example: 440LD22UG-R (suffix code) specifies:

#20 AWG wire; LS = 0.375"; inline wire; long lead length, bulk packed

			LEAD SPACING							
WIRE FORM	LEAD	0.375" (9.5 mm)		0.400" (10.0 mm)		0.500" (12.7 mm)				
DESCRIPTION	The.	LENGTH	#20 AWG	#22 AWG	#20 AWG	#22 AWG	#20 AWG	#22 AWG		
Straight wire	1	Long "LL"	AJ	BJ	AD	BL	AM	BM		
Inline wire	2	Long "LL"	UG	UJ	UL	UM	UQ	US		
Inime wire	2	Cut "CL"	UH	UK	UN	UP	UR	UT		

Notes

Popular wire lead form options are described above; consult factory for other available forms <u>www.vishay.com/doc?23140</u>

Practical consideration may limit wire options depending on capacitor size - verify special requirements with factory

For detailed information and figures see general information: <u>www.vishay.com/doc?23140</u>

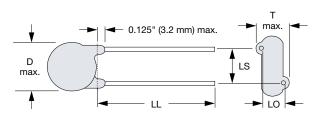
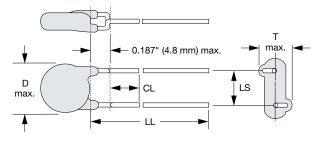


Fig. 1 - Straight wire



Minimum CL = 0.120" (3 mm) inline wire, LO = 0

Fig. 2 - Inline wire

WIRE INFORMATION	
#20 AWG	0.032" (0.81) tinned copper wire
#22 AWG	0.025" (0.64) tinned copper wire

LEAD LENGTH INFORMATION

- Standard long lead "LL" length = 1.250" (32 mm) minimum
- Cut lead "CL" length may be user specified; if unspecified, Vishay Cera-Mite supplies 0.187" (4.8 mm) EIA standard
- Cut lead lengths are measured from bottom of wire seating plane (wire support point on circuit board)
- Cut lead length tolerance: + 0.031" / 0.015" (+ 0.8 mm / 0.4 mm)

TAPE AND REEL OPTIONS

Radial leaded parts may be ordered with tape and reel packaging by adding appropriate suffix code to part number, like for wire lead options.

TAPE AND REEL PACKAGING PART NUMBER SUFFIX			TAPE AND REEL SUFFIX CODES FOR VARIOUS WIRE FORMS AND SIZES					
TAPE AND REEL	LS	MAX. DISC DIAMETER		TAPE AND REEL	STRAIGHT WIRE FIG. 1		INLINE WIRE FIG. 2	
FIG.	(mm)	(in)	(mm)	STANDARD	#20 AWG	#22 AWG	#20 AWG	#22 AWG
C (1)	10	0.708	18.0	EIA	AP	QX	XK	XT

Note

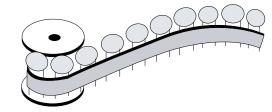
⁽¹⁾ Figure can be found in general information <u>www.vishay.com/doc?23140</u>, section Tape and Reel Options



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REELING STANDARD EIA-468B

EIA lead spacings for tape and reel are based on multiples of 0.100" (2.5 mm) to coordinate with automatic insertion machinery and boards using 0.100" grid convention.



Notes

- Popular tape and reel options are described above; consult factory for other available options <u>www.vishay.com/doc?23140</u>
- Practical consideration may limit tape and reel options on capacitor size verify special requirements with factory
- For detailed information and figures see general information: <u>www.vishay.com/doc?23140</u>

APPROVALS						
IEC 60384-14 - Safety tests This approval together with CB test certificate sub	stitutes all national approva	ls.				
CB Certificate (<u>www.vishay.com/doc?22237</u>)				•		
Y1-capacitor: CB test certificate:	DE1-63481	10 pF to 20 nF	500 V _{AC}			
X1-capacitor: CB test certificate:	DE1-63481	10 pF to 20 nF	760 V_{AC}			
VDE (www.vishay.com/doc?22238)				•		
Y1-capacitor: VDE marks approval:	40003985	10 pF to 20 nF	500 V _{AC}			
X1-capacitor: VDE marks approval:	40003985	10 pF to 20 nF	400 V _{AC}	$\zeta D \in \mathcal{F}$		
DIN EN 60384-14 VDE 0565-1-1 - Safety tests, annex H (humidity robustness grade IIIB)						
Underwriters Laboratories Inc. (<u>www.vishay.co</u>	<u>m/doc?22239</u>					
Y1-capacitor: UL test certificate:	E99264	10 pF to 20 nF	500 V _{AC}	R R		
X1-capacitor: UL test certificate:	E99264	10 pF to 20 nF	760 V _{AC}			
UL 60384-14, CSA E60384-1, CSA E60384-14						
Fixed capacitors for electromagnetic interference suppression and connection to the supply mains.						



Notes

- Marking IEC 60384-14 not shown on $\emptyset \le 9$ mm
- "XX XXX" is a placeholder for date code and lot number:
- "XX -" is the year and month according to IEC 60062 "- XXX" is the last 3 digits of the lot number

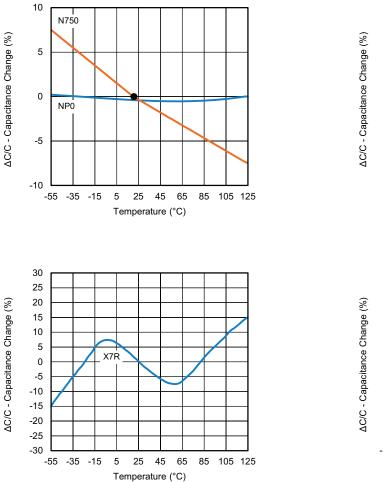
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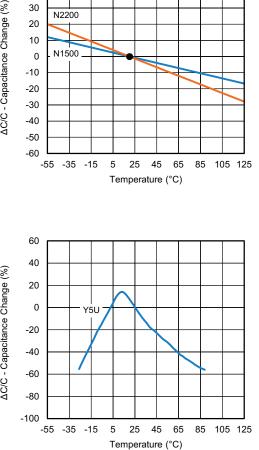
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CAPACITANCE CHANGE VS. TEMPERATURE (TYPICAL)





50

40

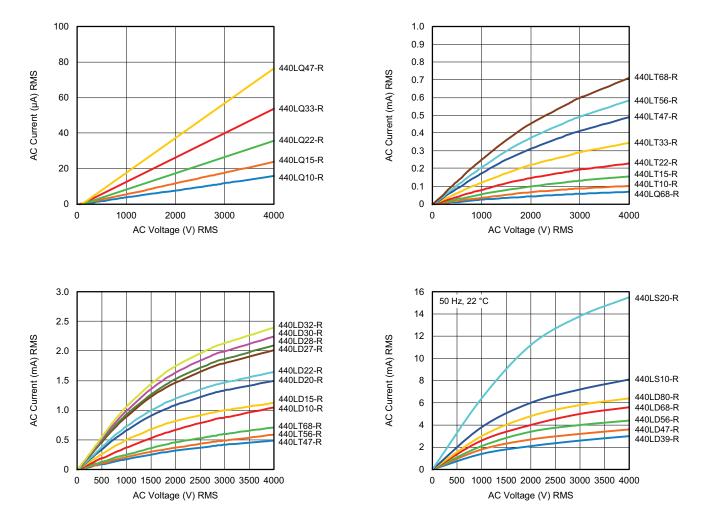
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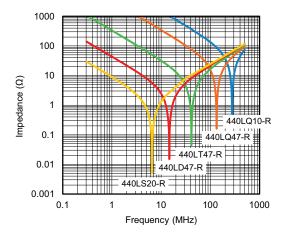
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AC CURRENT VS. VOLTAGE



IMPEDANCE VS. FREQUENCY (Wire Length 10 mm)



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STORAGE

The capacitors must not be stored in a corrosive atmosphere, where sulphide or chloride gas, acid, alkali or salt are present. Exposure of the components to moisture, should be avoided. The solderability of the leads is not affected by storage of up to 24 months (temperature +10 °C to +40 °C, relative humidity up to 60 % RH). Class 2 ceramic dielectric capacitors are also subject to aging see general information (www.vishay.com/doc?23140).

SOLDERING

SOLDERING SPECIFICATIONSSoldering test for capacitors with wire leads: (according to IEC 60068-2-20, solder bath method)Soldering temperatureSoldering temperatureRESISTANCE TO SOLDERING HEATSoldering temperature $(235 \pm 5) \degree C$ $(260 \pm 5) \degree C$ Soldering duration $(2 \pm 0.5) \$$ $(10 \pm 1) \$$ Distance from component body $\ge 2 mm$ $\ge 5 mm$

SOLDERING RECOMMENDATIONS

Ceramic capacitors are very sensitive to rapid changes in temperature (thermal shock) therefore the solder heat resistance specification (see table above) should not be exceeded. Exposing the capacitor to excessive heating may result in thermal shocks that can crack the ceramic body. Similarly, excessive heating can cause the internal solder junction to melt.

When soldering radial leaded ceramic capacitors with a soldering iron, it should be performed under the following conditions and should not exceed:

- Maximum temperature of iron-tip: 400 °C
- Maximum soldering iron wattage: 50 W
- Maximum soldering time: 3.5 s

Failure to follow the above cautions may result, in worst case, in short circuit or cause fuming or thermo-mechanical damage when the product is used.

Leaded ceramic capacitors are not designed for reflow process or dipping the body into a solder melt.

CLEANING

The components should be cleaned immediately following the soldering operation with vapor degreasers.

CLEANING (ULTRASONIC CLEANING)

To perform ultrasonic cleaning, observe the following conditions:

- Maximum rinse bath capacity output: 20 W/liter
- Maximum rinsing time: 300 s
- Do not vibrate the PCB/PWB directly
- Excessive ultrasonic cleaning may lead to mechanical damage

SOLVENT RESISTANCE

The coating and marking of the capacitors are resistant to the following test method:

IEC 60068-2-45 (method XA)

MOUNTING

We do not recommend modifying the lead terminals, e.g. bending or cropping. This action could break the coating or crack the ceramic insert. In order to avoid such failures we are offering different lead wire designs (e.g. straight, inline, inside crimp, outside crimp etc.) If however, the lead must be modified in any way, we recommend support of the lead with a clamping fixture next to the coating. If a defined product stop is required for mounting on a PCB, a mechanically formed product stop or a mounting tool should be used.



OPERATING VOLTAGE

In case the voltage is applied to the circuit, starting as well as stopping, may generate irregular voltage for a transit period because of resonance or switching. Be sure to use a capacitor with a rated voltage range that includes these irregular voltages.

OPERATING TEMPERATURE AND SELF-GENERATED HEAT

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high frequency, pulse, or similar application, it may have self-generated heat due to dielectric dissipation.

Temperature increase due to self-generated heating should not exceed 20 °C while operating at an atmosphere temperature of 25 °C.

When measuring, the surface temperature, make sure that the capacitor is not affected by radiant, conductive and convective heat by its surroundings. Excessive heat may lead to thermo-mechanical deterioration of the capacitor's characteristics and reliability.

RELATED DOCUMENTS					
General Information	www.vishay.com/doc?23140				
CB Test Certificate	www.vishay.com/doc?22237				
VDE Marks Approval	www.vishay.com/doc?22238				
UL Test Certificate	www.vishay.com/doc?22239				



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