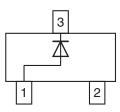
IMBD4448-G

Vishay Semiconductors



Small Signal Switching Diode





DESIGN SUPPORT TOOLS click logo to get started



MECHANICAL DATA

Case: SOT-23

Weight: approx. 8.1 mg

Packaging codes / options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

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- Silicon epitaxial planar diode
- Fast switching diode in case SOT-23, especially suited for automatic insertion.
- AEC-Q101 qualified available (part number on request)
- Base P/N-G3 green, commercial grade
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





COMPLIANT HALOGEN

GREEN (5-2008)

PARTS TAB	LE			
PART	ORDERING CODE	CIRCUIT CONFIGURATION	TYPE MARKING	REMARKS
IMBD4448-G	IMBD4448-G3-08 or IMBD4448-G3-18	Single	AJ	Tape and reel

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	SYMBOL VALUE		
Reverse voltage		V _R	75	V	
Peak reverse voltage		V _{RM}	100	V	
Rectified current (average) half wave rectification with resistive load ⁽¹⁾	f ≥ 50 Hz	I _{F(AV)}	150	mA	
Surge forward current	$t < 1$ s and $T_j = 25$ °C	I _{FSM}	500	mA	
Power dissipation ⁽¹⁾		P _{tot}	350	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air ⁽¹⁾		R _{thJA}	450	K/W	
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-65 to +150	°C	
Operating temperature range		T _{op}	-55 to +150	°C	

Note

⁽¹⁾ Device on fiberglass substrate, see layout on next page

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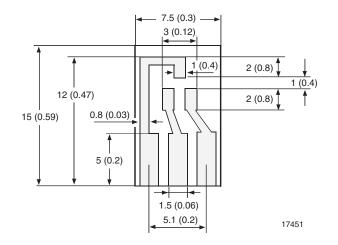
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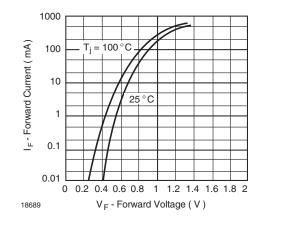
ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 5 mA	V _F	0.62		0.72	V
Forward voltage	I _F = 100 mA	V _F			1	V
	V _R = 70 V	I _R			2500	nA
Leakage current	$V_R = 70 \text{ V}, \text{ T}_j = 150 ^\circ\text{C}$	I _R			50	μA
	V _R = 25 V, T _j = 150 °C	I _R			30	μA
Diode capacitance	$V_{\rm F} = V_{\rm R} = 0 \ V$	CD			4	pF
Reverse recovery time	$\label{eq:IF} \begin{array}{l} I_F = 10 \text{ mA}, i_R = 1 \text{ mA}, \\ V_R = 6 \text{ V}, R_L = 100 \Omega \end{array}$	t _{rr}			4	ns

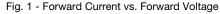
LAYOUT FOR R_{thJA} TEST

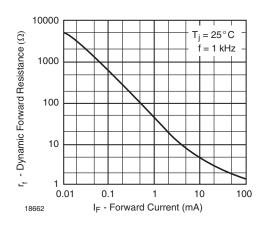
Thickness: Fiberglass 1.5 mm (0.059 in.) Copper leads 0.3 mm (0.012 in.)



TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)







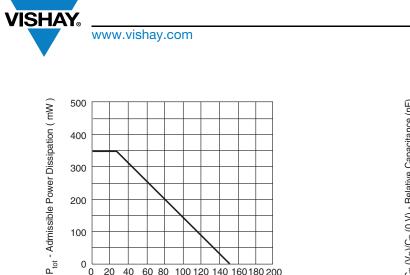


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Document Number: 85883

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60 80 100 120 140 160 180 200

Fig. 3 - Admissible Power Dissipation vs. Ambient Temperature

 T_{amb} - Ambient Temperature ($^\circ C$)

200

100

18663

0 ∟ 0

20

40

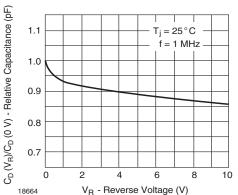


Fig. 4 - Relative Capacitance vs. Reverse Voltage

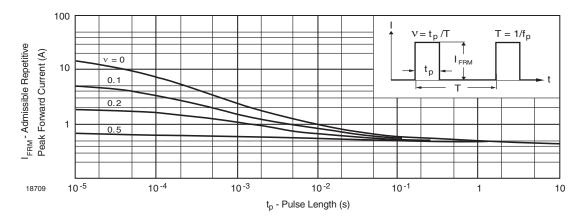


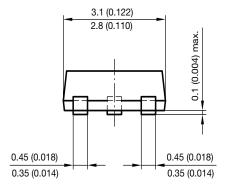
Fig. 5 - Admissible Repetitive Peak Forward Current vs. Pulse Duration

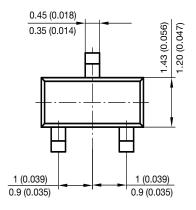
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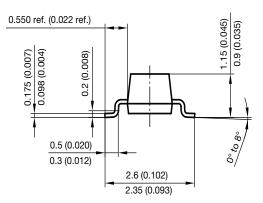


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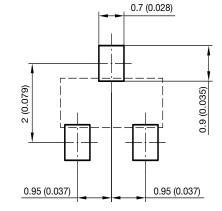
PACKAGE DIMENSIONS in millimeters (inches): SOT-23







Foot print recommendation:



Document no.: 6.541-5014.01-4 Rev. 8 - Date: 23.Sept.2009 17418



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