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Vishay Semiconductors

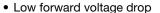
High Performance Schottky Rectifier, 2 A



SMA (DO-214AC)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 A			
V_R	100 V			
V _F at I _F	0.72 V			
I _{RM}	1 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	1.0 mJ			
Package	SMA (DO-214AC)			
Circuit configuration	Single			

FEATURES





 Guard ring for enhanced ruggedness and long term reliability

COMPLIANT HALOGEN

FREE

• Small foot print, surface mountable

- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-20MQ100HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	2	A		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	120	A		
V _F	2 A _{pk} , T _J = 125 °C	0.72	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-20MQ100HM3	UNITS
Maximum DC reverse voltage	V_{R}	100	V
Maximum working peak reverse voltage	V_{RWM}	100	V

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current		50 % duty cycle at T _L = 113 °C, re On PC board 9 mm ² island (0.013	•	2.1	۸
See fig. 4	I _{F(AV)}	50 % duty cycle at T_L = 116 °C, re On PC board 9 mm ² island (0.013	_	2	Α
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	120	
non-repetitive surge current See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	30	Α
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 0.5 \text{A}, L = 8 \text{mH}$		1.0	mJ
Repetitive avalanche current	I _{AR}			0.5	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		2 A		0.91	V
		1.5 A	T _J = 25 °C	0.85	
Maximum forward voltage drop	V (1)	1 A		0.78	
See fig. 1	V _{FM} ⁽¹⁾	2 A		0.72	
		1.5 A	T _J = 125 °C	0.68	
		1 A		0.63	
Maximum reverse leakage current	,	T _J = 25 °C	V Dated V	0.1	^
See fig. 2	I _{RM}	$T_J = 125 ^{\circ}\text{C}$ $V_R = \text{Rated } V_R$		1	mA
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.52	V
Forward slope resistance	r _t			78.4	mΩ
Typical junction capacitance	C _T	V _R = 10 V _{DC} , T _J = 25 °C, test signal = 1 MHz		38	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}$ Pulse width = 300 μ s, duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W
Approximate weight			0.07	g
Approximate weight			0.002	oz.
Marking device		Case style SMA (DO-214AC) (similar D-64)	2	J

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

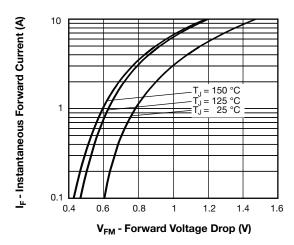


Fig. 1 - Maximum Forward Voltage Drop Characteristics

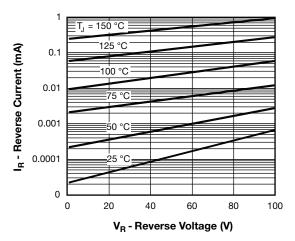


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

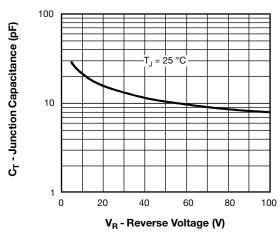
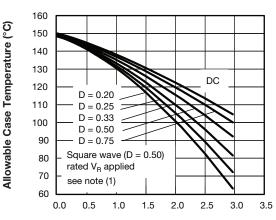
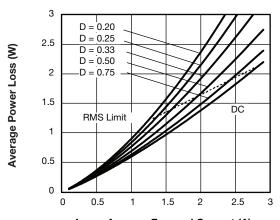


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



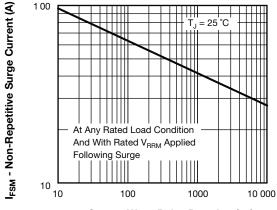
I_{F(AV)} - Average Forward Current (A)

Fig. 4 - Maximum Average Forward Current vs.
Allowable Lead Temperature



I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Average Forward Dissipation vs. Average Forward Current



t - Square Wave Pulse Duration (µs)

Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

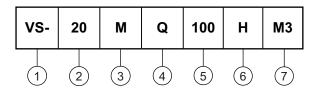
Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R$ (1 - D); I_R at $V_{R1} = 80$ % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Current rating

3 - M = SMA

4 - Q = Schottky "Q" series

5 - Voltage rating (100 = 100 V)

6 - H = AEC-Q101 qualified

7 - Environmental digit:

M3 = Halogen-free, RoHS-compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	ED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTI				
VS-20MQ100HM3/5AT	5AT	7500	13" diameter plastic tape and reel		

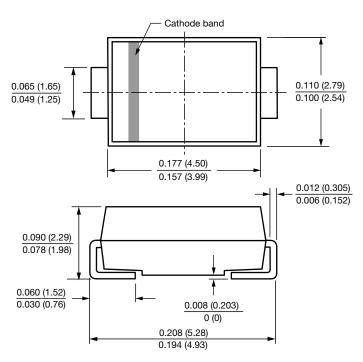
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95400</u>				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			



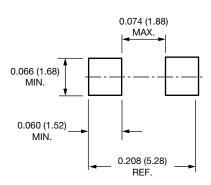
SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)



Mounting Pad Layout





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