

High Performance Schottky Rectifier, 3.0 A



SMC (DO-214AB)

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	3.0 A			
V_R	60 V			
V _F at I _F	0.52 V			
I _{RM}	20 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	5.0 mJ			
Package	SMC (DO-214AB)			
Circuit configuration	Single			

FEATURES

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability



- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-30BQ060HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MECHANICAL DATA

Case: SMC (DO-214AB)

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per J-STD-002

Polarity: color band denotes cathode end

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	3.0	A		
V _{RRM}		60	V		
I _{FSM}	t _p = 5 μs sine	1200	А		
V _F	3.0 A _{pk} , T _J = 125 °C	0.52	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-30BQ060HM3	UNITS	
Maximum DC reverse voltage	V_{R}	60	V	
Maximum working peak reverse voltage	V_{RWM}	00	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDIT	TIONS	VALUES	UNITS
Marin and Control of the Control of		50 % duty cycle at T _L = 123 °C, rectangular waveform		3.0	
waximum average forward current	aximum average forward current $I_{F(AV)}$		50 % duty cycle at T _L = 113 °C, rectangular waveform		
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated	1200	Α
non-repetitive surge current at T _C = 25 °C	I _{FSM}	10 ms sine or 6 ms rect. pulse	load condition and with rated V _{RRM} applied	130	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1.0 \text{A}, L = 10 \text{mH}$		5.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	Α



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	3 A	T _J = 25 °C	0.58	V
Maximum forward voltage drop		6 A		0.76	
waxiindiii lolward voltage drop		3 A	T _J = 125 °C	0.52	
		6 A		0.66	
Maximum reverse leakage current I _{RM}	I	T _J = 25 °C	V_{R} = Rated V_{R}	0.5	mA
	'RM	T _J = 125 °C	VR = nateu VR	20	IIIA
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to1 MHz), 25 °C		180	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000 V/ _I		V/µs	

Note

 $^{(1)}\,$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction temperature range	T _J ⁽¹⁾		-55 to +150	°C
Maximum storage temperature range	T _{Stg}		-55 to +150	C
Maximum thermal resistance, junction to lead	R _{thJL} (2)	DC operation	12	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	C/VV
Approximate weight			0.24	g
Approximate weight			0.008	oz.
Marking device		Case style SMC (DO-214AB)	31	+

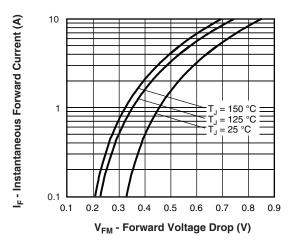
Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB







100 $T_J = 150 \, ^{\circ}C$ I_R - Reverse Current (mA) 10 $T_J = 125$ °C = 100 °C 0.1 T_J = 75 °C = 50 °C 0.01 0.001 20 30 50 10 40 60 V_R - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs.Reverse Voltage (Per Leg)

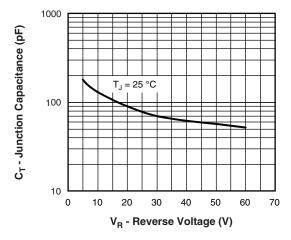


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

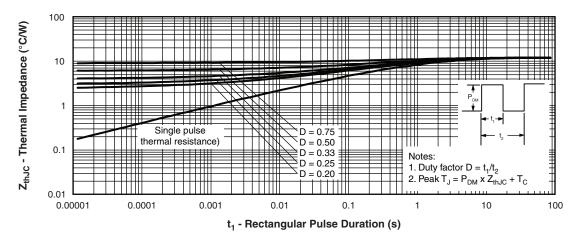


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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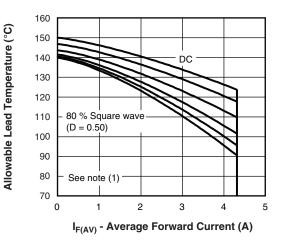


Fig. 5 - Maximum Average Forward Current vs.
Allowable Lead Temperature

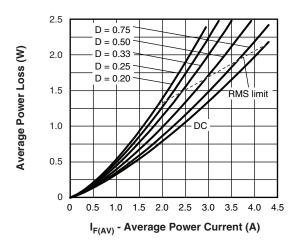


Fig. 6 - Maximum Average Forward Dissipation vs.

Average Forward Current

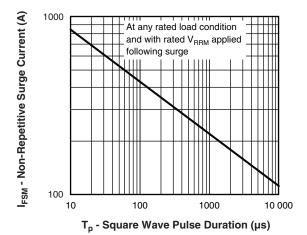


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

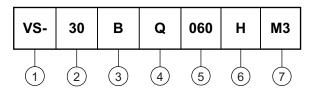
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product suffix

2 - Current rating

3 - B = SMC

4 - Q = Schottky "Q" series

5 - Voltage rating (060 = 60 V)

6 - H = AEC-Q101 qualified

7 - Environmental digit:

M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-30BQ060HM3/9AT	9AT	3500	13" diameter plastic tape and reel		

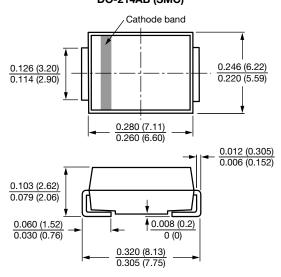
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95402		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		
SPICE model	www.vishay.com/doc?96996		



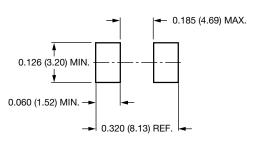
SMC

DIMENSIONS in inches (millimeters)

DO-214AB (SMC)



Mounting Pad Layout





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