FREE

Vishay Semiconductors

High Performance Schottky Rectifier, 3.0 A



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SMC (DO-214AB)

PRIMARY CHARACTERISTICS				
I _{F(AV)}	3.0 A			
V _R	40 V			
V _F at I _F	0.46 V			
I _{RM}	30 mA at 125 °C			
T _J max.	150 °C			
E _{AS}	6.0 mJ			
Package	SMC (DO-214AB)			
Circuit configuration	Single			

FEATURES

- Very low forward voltage drop
- Guard ring for enhanced ruggedness and long RoHS
 term reliability
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-30BQ040HM3 surface-mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	VALUES	UNITS				
I _{F(AV)}	Rectangular waveform	3.0	A				
V _{RRM}		40	V				
I _{FSM}	t _p = 5 μs sine	1600	A				
V _F	3.0 A _{pk} , T _J = 125 °C	0.46	V				
TJ	Range	-55 to +150	°C				

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-30BQ040HM3	UNITS			
Maximum DC reverse voltage	V _R	40	V			
Maximum working peak reverse voltage	V _{RWM}	40	v			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	1	50 % duty cycle at T_L = 115 °C, r	rectangular waveform	3.0		
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 104 °C, rectangular waveform		4.0		
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	1600	A	
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	90		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.0 A, L = 12 mH		6.0	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А	

Revision: 18-Apr-2019 1 Document Number: 94843 For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>

VS-30BQ040HM3



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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CC	VALUES	UNITS		
		3 A	T 05.00	0.57	v	
Maximum forward valtage drep	V (1)	6 A	– T _J = 25 °C	0.76		
Maximum forward voltage drop	V _{FM} ⁽¹⁾	3 A	T 105 %O	0.46		
		6 A	– T _J = 125 °C	0.64		
Maximum reverse leakage current		T _J = 25 °C	$V_{\rm B} = \text{Rated } V_{\rm B}$	0.5	mA	
Maximum reverse leakage current	I _{RM}	T _J = 125 °C	$v_{\rm R} = naleu v_{\rm R}$	30		
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 $^{\circ}\mathrm{C}$		230	pF	
Typical series inductance	L _S	Measured lead to lead 5	3.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			V/µs	

Note

 $^{(1)}$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	MBOL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature range	T_{J} ⁽¹⁾ , T_{Stg}		-55 to +150	°C	
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾		12	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	0/11	
Approximate weight			0.24	g	
Approximate weight			0.008	oz.	
Marking device		Case style SMC (DO-214AB) 3F		F	

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB



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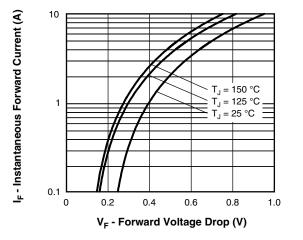


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

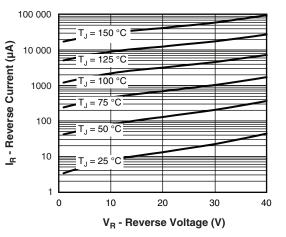


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

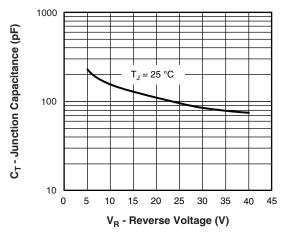


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

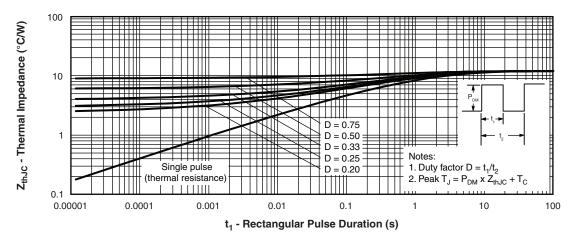
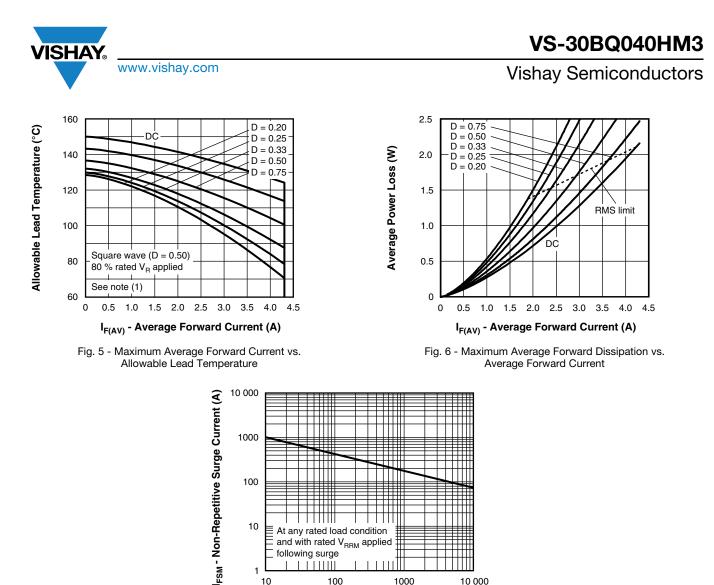


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



following surge

At any rated load condition and with rated $\mathrm{V}_{\mathrm{RRM}}$ applied

100

10

1 10 tp - Square Pulse Duration (µs) Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

1000

10 000

Note

- (1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

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(Pb)-free



ORDERING INFORMATION TABLE

Device code	vs-	30	в	Q	040	н	М3	
	1	2	3	4	5	6	7	I
	느		nay Sem		ctors pro	oduct		
	凹		rent rati	ng				
	3	- B=	SMC					
	브	- Q=	Schottk	ky "Q" se	eries			
	Ľ	- Vol	tage rati	ng (040	= 40 V))		
	6	- H=	AEC-Q	101 qua	lified			
	7	- Env	vironmer	ntal digit	:			
		М3	= halog	en-free,	RoHS-0	complia	nt, and t	termi

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-30BQ040HM3/9AT	9AT	3500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95402				
Part marking information	www.vishay.com/doc?95403				
Packaging information	www.vishay.com/doc?95404				
SPICE model	www.vishay.com/doc?96601				

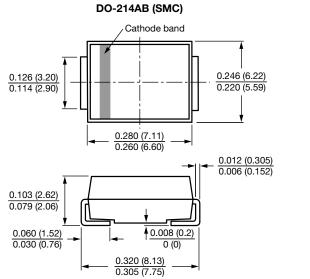


Outline Dimensions

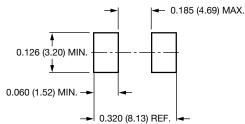
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DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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