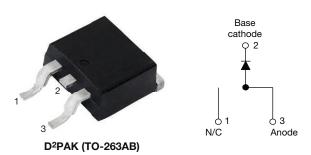


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Vishay Semiconductors

High Performance Schottky Rectifier, 16 A



PRIMARY CHARACTERISTICS							
I _{F(AV)}	16 A						
V_{R}	35 V, 45 V						
V _F at I _F	0.57 V						
I _{RM}	40 mA at 125 °C						
T _J max.	150 °C						
E _{AS}	24 mJ						
Package	D ² PAK (TO-263AB)						
Circuit configuration	Single						

FEATURES

- 150 °C T_J operation
- High frequency operation
- Low forward voltage drop

High purity, high temperature epoxy encapsulation for enhanced mechanical



- strength and moisture resistanceGuard ring for enhanced ruggedness and long term
- reliability
 Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

DESCRIPTION

This VS-MBRB16... Schottky rectifier has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I _{F(AV)}	Rectangular waveform	16	A						
V_{RRM}		35/45	V						
I _{FSM}	t _p = 5 μs sine	1800	A						
V _F	16 A _{pk} , T _J = 125 °C	0.57	V						
TJ		-65 to +150	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	VS-MBRB1635-M3	VS-MBRB1645-M3	UNITS					
Maximum DC reverse voltage	V_R	35	45	V					
Maximum working peak reverse voltage	V_{RWM}	33	45	V					

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONI	DITIONS	VALUES	UNITS				
Maximum average forward current	I _{F(AV)}	T _C = 134 °C, rated V _R	T_C = 134 °C, rated V_R						
Non-repetitive peak surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated V _{RRM} applied		1800	Α				
		Surge applied at rated load condition half wave single phase 60 Hz		150					
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 3.6 A, L = 3.7 mH		24	mJ				
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum V_A = 1.5 x V_R typical		3.6	Α				



VS-MBRB1635-M3, VS-MBRB1645-M3

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS						
Maximum forward valtage drap	V _{FM} ⁽¹⁾	16 A	T _J = 25 °C	0.63	V				
Maximum forward voltage drop	VFM (1)	16 A	T _J = 125 °C	0.57					
Maximum instantaneous	I _{RM} ⁽¹⁾	T _J = 25 °C	Rated DC voltage	0.2	mA				
reverse current	IRM (*)	T _J = 125 °C	hated DC voltage	40					
Maximum junction capacitance	C _T	V _R = 5 V _{DC} (test signal ra	1400	pF					
Typical series inductance	L _S	Measured lead from top	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R	10 000	V/µs					

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS		
Maximum junction temperatu	re range	T_J		-65 to 150	°C		
Maximum storage temperatu	re range	T _{Stg}		-65 to 175			
Maximum thermal resistance, junction to case		R _{thJC}	DC operation	1.50	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	C/VV		
Approximate weight				2	g		
Approximate weight				0.07	OZ.		
Manustina taurus minimum				6 (5)	kgf · cm		
Mounting torque	maximum			12 (10)	(lbf · in)		
Marking device			Case style D ² PAK (TO-263AB)		31635 31645		



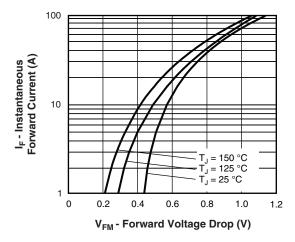


Fig. 1 - Maximum Forward Voltage Drop Characteristics

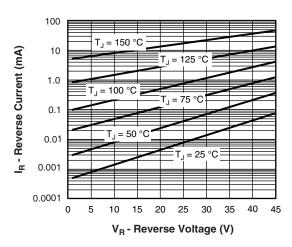


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

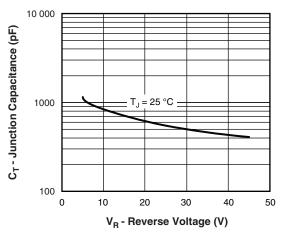


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

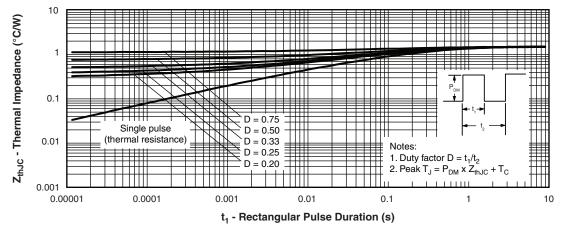


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

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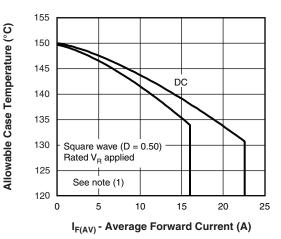


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

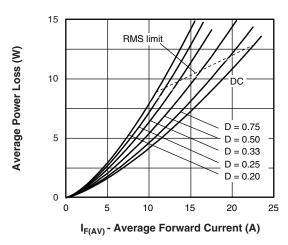


Fig. 6 - Forward Power Loss Characteristics

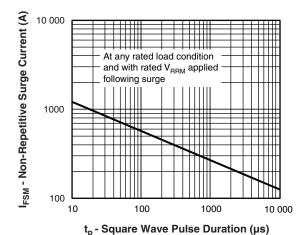


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

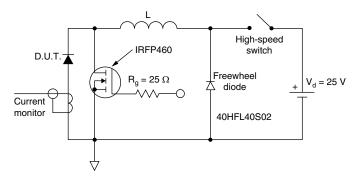


Fig. 8 - Unclamped Inductive Test Circuit

Note

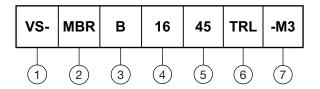
Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = forward power loss = I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = rated V_R$ applied

VS-MBRB1635-M3, VS-MBRB1645-M3

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ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

2 - Essential part number

3 - B = surface mount

Current rating (16 = 16 A)

6 - • None = tube

• TRL = tape and reel (left oriented)

• TRR = tape and reel (right oriented)

- -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-MBRB1635-M3	50	Antistatic plastic tubes							
VS-MBRB1635TRL-M3	800	13" diameter plastic tape and reel							
VS-MBRB1635TRR-M3	800	13" diameter plastic tape and reel							
VS-MBRB1645-M3	50	Antistatic plastic tubes							
VS-MBRB1645TRL-M3	800	13" diameter plastic tape and reel							
VS-MBRB1645TRR-M3	800	13" diameter plastic tape and reel							

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96164				
Part marking information	www.vishay.com/doc?95444				
Packaging information	www.vishay.com/doc?96424				
SPICE model	www.vishay.com/doc?95407				



Vishay Semiconductors

D²PAK

DIMENSIONS in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	NOTES	SYMBOL	MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	.5	STINIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inches
- (7) Outline conforms to JEDEC® outline TO-263AB

Revision: 13-Jul-17 Document Number: 96164



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