

Product Summary

BV _{DSS}	R _{D(on)} MAX	I _D T _A = +25°C
20V	0.10Ω @ V _{GS} = 4.5V	1.2A
	0.14Ω @ V _{GS} = 2.5V	0.5A
	0.25Ω @ V _{GS} = 1.5V	0.1A

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{D(on)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

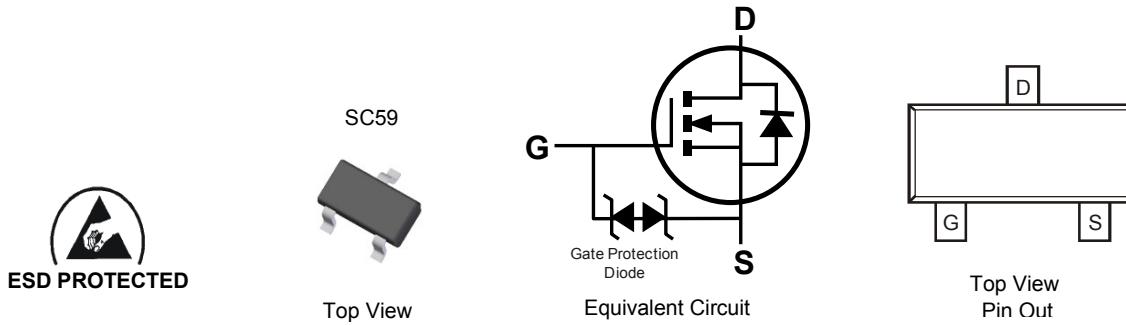
- Notebook Computer
- Portable Phone
- PCMCIA Cards and Battery Powered Circuits

Features

- Low On-Resistance
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Case: SC59
- Case Material - Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Terminal Connections: See Diagram
- Weight: 0.014 grams (Approximate)



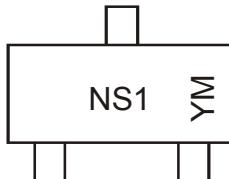
Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2112SN-7	SC59	3000/Tape & Reel

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



NS1 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: I = 2021)
 M = Month (ex: 9 = September)

Date Code Key

Year	2007	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	U	I	J	K	L	M	N	O	P	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	20	V
Gate-Source Voltage	V_{GSS}	± 8	V
Drain Current	I_D	1.2 4.0	A
Continuous Pulsed			

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation	P_d	500	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	250	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV_{DSS}	20	—	—	V	$V_{GS} = 0V, I_D = 250\mu\text{A}$
Zero Gate Voltage Drain Current @ $T_J = +25^\circ\text{C}$	I_{DSS}	—	—	10	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Body Leakage	I_{GS}	—	—	± 10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	$V_{GS(\text{th})}$	0.5	—	1.2	V	$V_{DS} = 10V, I_D = 1.0\text{mA}$
Static Drain-Source On-Resistance	$R_{DS(\text{on})}$	—	—	0.10 0.14 0.25	Ω	$V_{GS} = 4.5V, I_D = 0.5\text{A}$ $V_{GS} = 2.5V, I_D = 0.5\text{A}$ $V_{GS} = 1.5V, I_D = 0.1\text{A}$
Diode Forward Voltage	V_{SD}	—	0.8	1.1	V	$V_{GS} = 0V, I_S = 1\text{A}$
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C_{iss}	—	220	—	pF	$V_{DS} = 10V, V_{GS} = 0V$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	120	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	45	—	pF	
SWITCHING CHARACTERISTICS (Note 6)						
Turn-On Delay Time	$t_{D(\text{on})}$	—	10	—	ns	$V_{DD} = 5V, I_D = 0.5\text{A},$ $V_{GS} = 10V, R_{\text{GEN}} = 50\Omega$
Turn-Off Delay Time	$t_{D(\text{off})}$	—	75	—	ns	
Turn-On Rise Time	t_R	—	15	—	ns	
Turn-Off Fall Time	t_F	—	65	—	ns	

Notes: 5. Short duration pulse test used to minimize self-heating effect.
6. Guaranteed by design. Not subject to product testing.

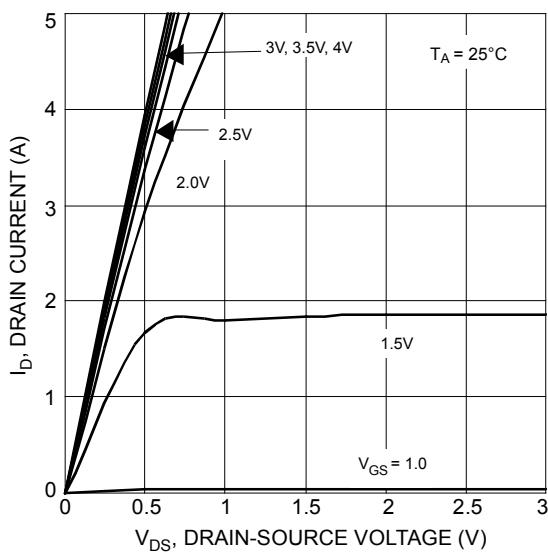


Fig. 1 Typical Output Characteristics

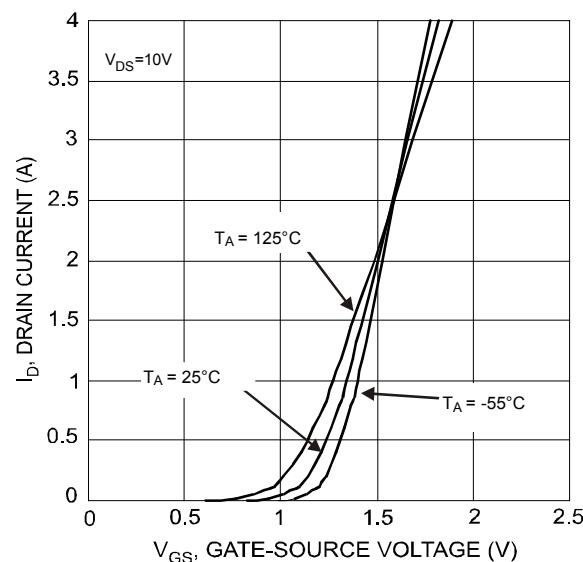


Fig. 2 Typical Transfer Characteristics

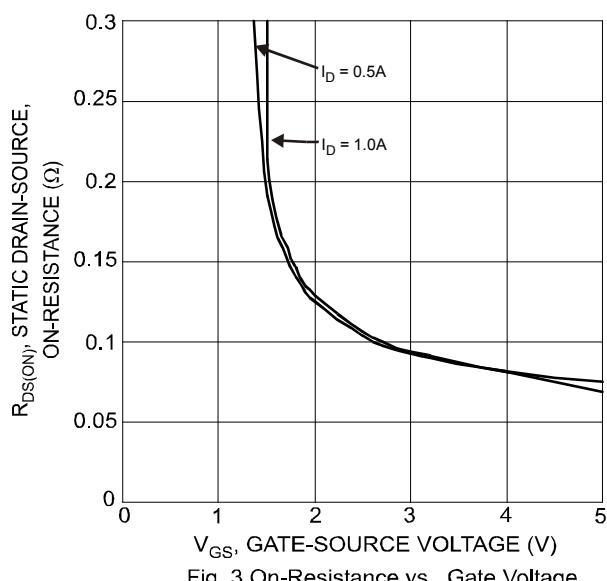


Fig. 3 On-Resistance vs. Gate Voltage

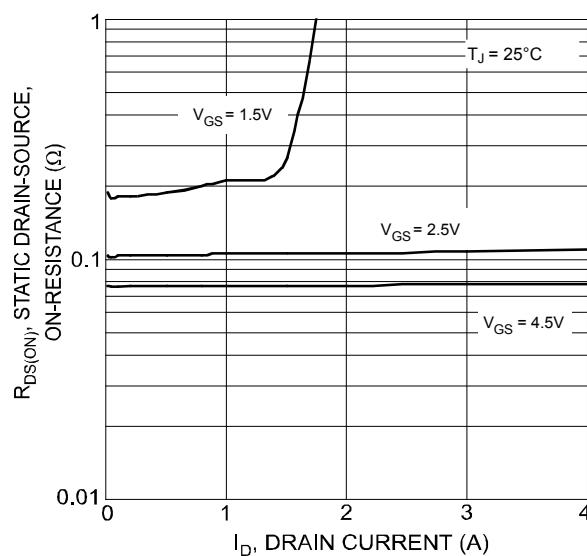


Fig. 4 On-Resistance vs. Drain Current

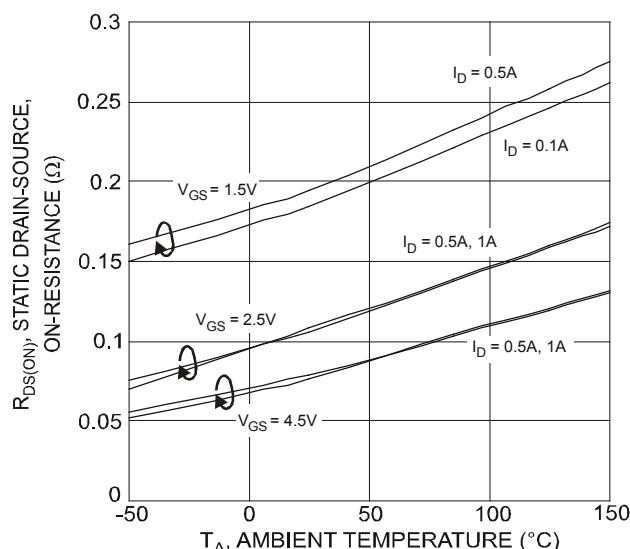


Fig. 5 On-Resistance Variation with Temperature

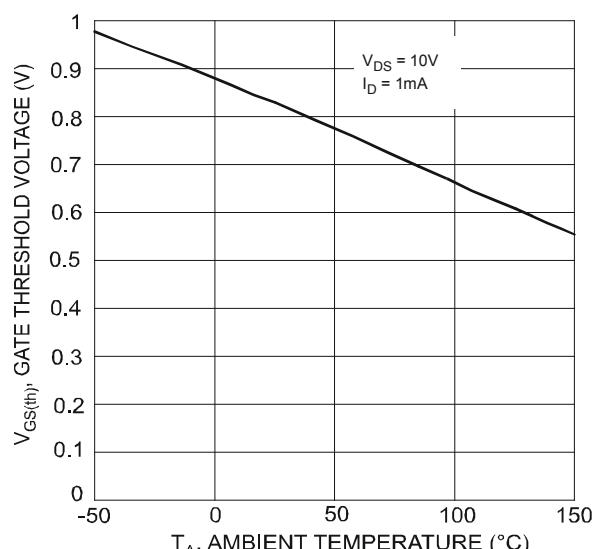


Fig. 6 Gate Threshold Voltage vs. Temperature

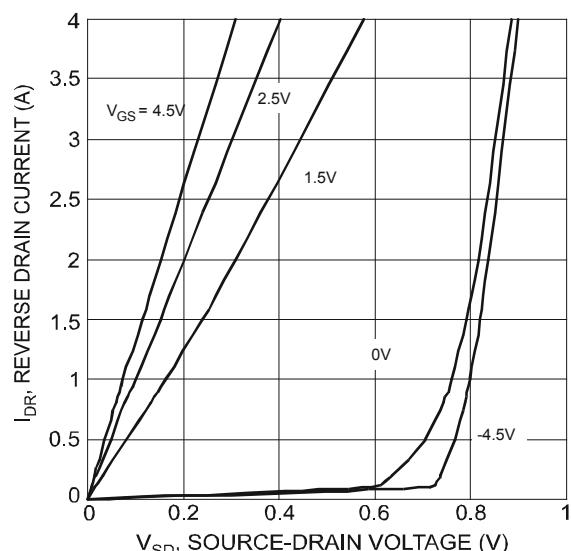


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

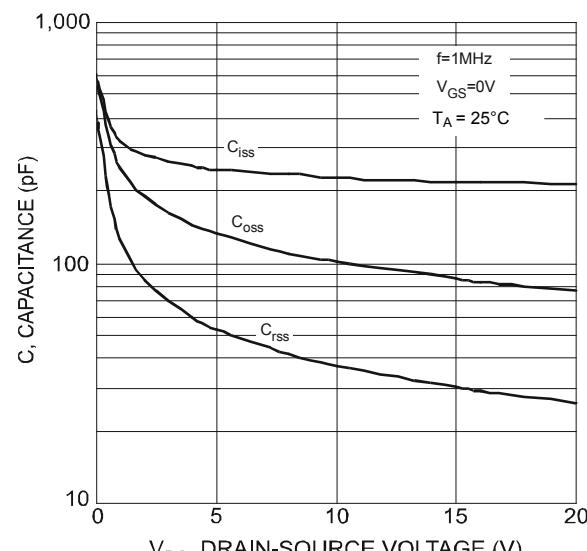
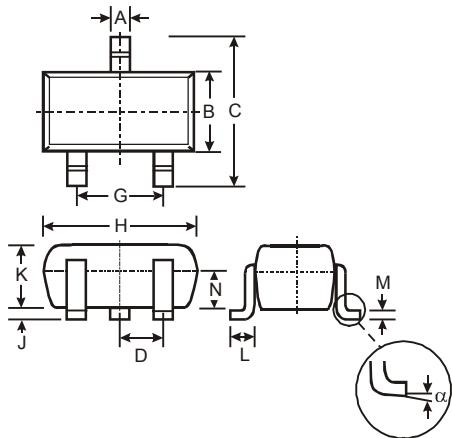


Fig. 8 Typical Junction Capacitance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SC59



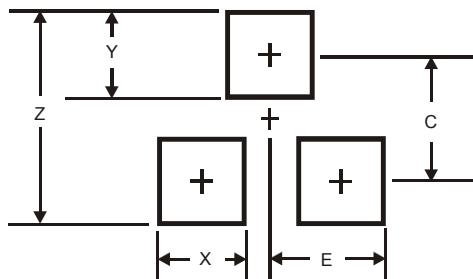
SC59			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
G	-	-	1.90
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
□	0°	8°	-

All Dimensions in mm

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SC59



Dimensions	SC59
Z	3.4
X	0.8
Y	1.0
C	2.4
E	1.35

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