

# VPS28-2800

## Electrical Specifications (@25C)

1. Maximum Power: 80.0VA
2. Primary – Series: 230VAC, 50/60Hz; Parallel: 115VAC, 50/60Hz
3. Secondary: Series: 28.0V CT@ 2.8A; Parallel: 14.0V @ 5.6A
4. Voltage Regulation: 25% TYP @ full load to no load
5. Temperature Rise: 30C TYP (45C MAX allowed)
6. Insulation Resistance: 100MΩ

## Construction:

Dual bobbin construction with an insulated shroud, both made of a high temperature material that exceeds UL flammability requirements.

## Safety:

These units are designed with 4000VAC isolation between the primary and secondary, and also, between each winding and the core. Since the dual bobbin construction effectively reduces capacitance, electrostatic shielding is not required. World Series Transformers are designed and manufactured to meet the following agency approvals:

## Agency File:

UL: File E53148, UL 5085-1 and 2 (formerly UL 506), General Purpose.  
 CSA: File LR 221330. C22.2 NO. 66, General Purpose.  
 TUV: File R72182067, EN 61558-1:2005+A1, EN61558-2-6:2009. Double Insulated.  
 Non-inherently Short-Circuit-Proof.



## A. Dimensions:

Unit: In inches

H	W	D	A	B	C	T	MW	ML
3	2-1/2	2-5/16	-	1-3/8	5/16	3/16	2	2-1/4

B. Mounting Hole Size: 13/64”X3/8”

C. WT Lbs. : 2.8

D. Terminal Size: 0.187” x 0.020”

## Connections<sup>1</sup>:

**Input:** Series – 6 and 1, Jumper 5 to 2

Parallel – 6 and 1, Jumper 6 to 2 and 5 to 1

**Output:** Series – 12 and 7, Jumper 11 to 8

Parallel – 12 and 7, Jumper 12 to 8 and 11 to 7

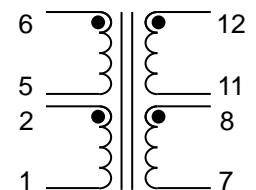
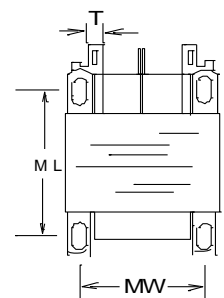
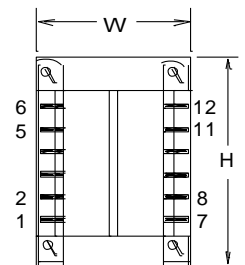
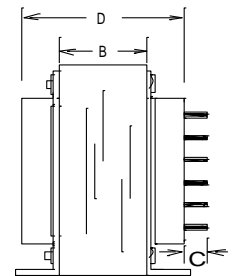
**RoHS Compliance:** As of manufacturing date February 2016, all standard products meet the requirements of 2015/863/EU, known as the RoHS 3 initiative.

\* Upon printing, this document is considered “uncontrolled”. Please contact Triad Magnetics’ website for the most current version.

<sup>1</sup> Primary and secondary windings are designed to be connected in series or parallel. Windings are not intended to be used independently.



Picture for Illustration Purposes only



**SCHEMATIC**