

# RJH65D27BDPQ-A0

650V - 50A - IGBT

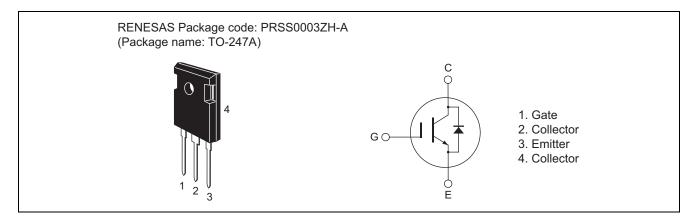
Application: Inverter

R07DS1328EJ0110
Rev.1.10
Mar 01, 2016

### **Features**

- Low collector to emitter saturation voltage  $V_{CE(sat)} = 1.3 \text{ V typ.}$  (at  $I_C = 50 \text{ A}$ ,  $V_{GE} = 15 \text{ V}$ ,  $Ta = 25^{\circ}\text{C}$ )
- Built in fast recovery diode in one package
- Trench gate and thin wafer technology
- High speed switching  $t_f = 120$  ns typ. (at  $_{CC} = 400$  V,  $V_{GE} = \pm 15$  V,  $I_C = 50$  A, Rg = 10  $\Omega$ , inductive load)
- Operation frequency  $(10kHz \le f < 20kHz)$

### **Outline**



## **Absolute Maximum Ratings**

 $(Tc = 25^{\circ}C)$ 

ltem		Symbol	Ratings	Unit
Collector to emitter voltage / diode reverse voltage		V <sub>CES</sub> / V <sub>R</sub>	650	V
Gate to emitter voltage		V <sub>GES</sub>	±30	V
Collector current	Tc = 25°C	lc	100	Α
	Tc = 100°C	lc	50	Α
Collector peak current		ic(peak) <sup>Note1</sup>	200	Α
Clamped inductive load cur	rent	I <sub>CL</sub> Note2	150	Α
Collector to emitter diode	Tc = 25°C	I <sub>DF</sub>	100	Α
forward current	Tc = 100°C	I <sub>DF</sub>	50	Α
Collector to emitter diode forward peak current		I <sub>DF</sub> (peak) Note1	200	Α
Collector dissipation		P <sub>C</sub> Note3	375	W
Junction to case thermal resistance (IGBT)		θj-c <sup>Note3</sup>	0.40	°C /W
Junction to case thermal resistance (Diode)		θj-cd Note3	0.50	°C /W
Junction temperature		Tj Note3	175	°C
Storage temperature		Tstg	-55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it are within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

### **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

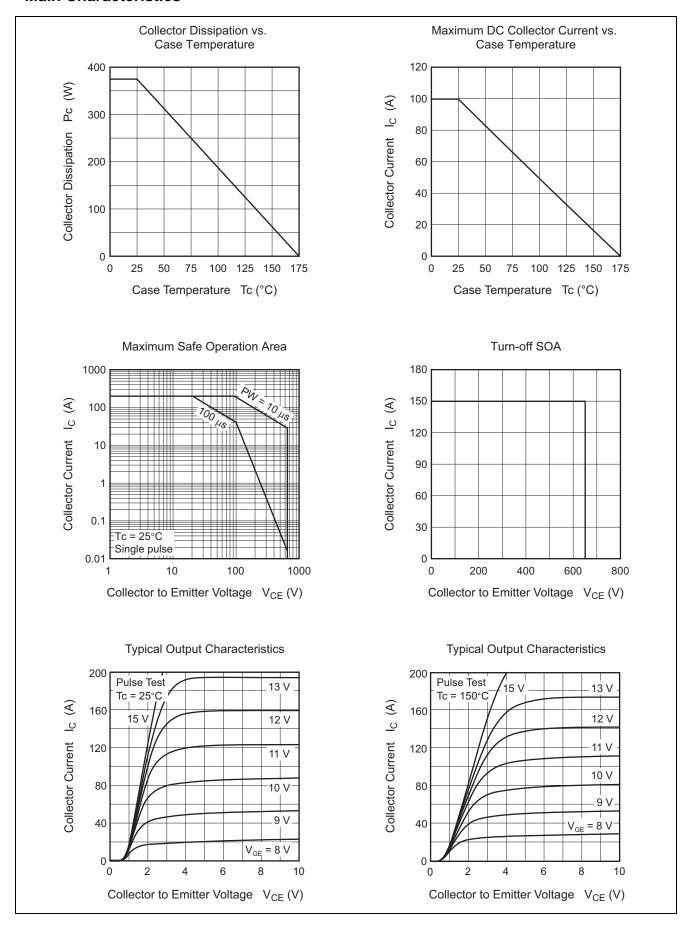
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Zero gate voltage collector current / Diode reverse current	I <sub>CES</sub> / I <sub>R</sub>	_	_	100	μΑ	V <sub>CE</sub> = 650 V, V <sub>GE</sub> = 0	
Gate to emitter leak current	I <sub>GES</sub>	_	_	±1	μΑ	V <sub>GE</sub> = ±30 V, V <sub>CE</sub> = 0	
Gate to emitter cutoff voltage	V <sub>GE(off)</sub>	4.5	_	6.5	V	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 1 mA	
Collector to emitter saturation voltage	V <sub>CE(sat)</sub>	_	1.3	1.65	V	I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V <sup>Note4</sup>	
Input capacitance	Cies	_	2850	_	pF	V <sub>CE</sub> = 25 V	
Output capacitance	Coes		175	_	pF	V <sub>GE</sub> = 0	
Reverse transfer capacitance	Cres		80	_	pF	f = 1 MHz	
Total gate charge	Qg	_	175	_	nC	V <sub>GE</sub> = 15V	
Gate to emitter charge	Qge	_	25	_	nC	V <sub>CE</sub> = 400 V I <sub>C</sub> = 50 A	
Gate to collector charge	Qgc	_	90	_	nC		
Turn-on delay time	t <sub>d(on)</sub>	_	20	_	ns	V <sub>CC</sub> = 400 V	
Rise time	t <sub>r</sub>	_	35	_	ns	V <sub>GE</sub> = ±15 V	
Turn-off delay time	t <sub>d(off)</sub>	_	165	_	ns	$I_C = 50 \text{ A}$ Rg = 10 $\Omega$	
Fall time	t <sub>f</sub>	_	120	_	ns		
Turn-on energy	Eon	_	1.0	_	mJ	(Inductive load) Note5	
Turn-off energy	E <sub>off</sub>	_	1.5	_	mJ		
Total switching energy	E <sub>total</sub>	_	2.5	_	mJ		
Turn-on delay time	t <sub>d(on)</sub>	_	20	_	ns	V <sub>CC</sub> = 400 V	
Rise time	t <sub>r</sub>	_	35	_	ns	V <sub>GE</sub> = ±15 V	
Turn-off delay time	t <sub>d(off)</sub>	_	200	_	ns	I <sub>C</sub> = 50 A	
Fall time	t <sub>f</sub>	_	140	_	ns	$Rg = 10 \Omega$	
Turn-on energy	Eon	_	1.5	_	mJ	$T_C = 150^{\circ}C$	
Turn-off energy	E <sub>off</sub>	_	1.9	_	mJ	(Inductive load) Note5	
Total switching energy	E <sub>total</sub>	_	3.4	_	mJ		
Short circuit withstand time	t <sub>sc</sub>	3	_	_	μS	$V_{CC} \le 360 \text{ V}, V_{GE} = 15 \text{ V}$ $T_{C} = 150^{\circ}\text{C}$	
	1	1	1	1	T		
FRD forward voltage	V <sub>F</sub>	_	1.7	2.2	V	I <sub>F</sub> = 50 A Note4	
FRD reverse recovery time	trr	_	80		ns	$I_F = 50 \text{ A}, \text{ dif/dt} = 300 \text{ A/}\mu\text{s}$	
EDD roverse recovery charge	$\circ$	1	0.25	1		1	

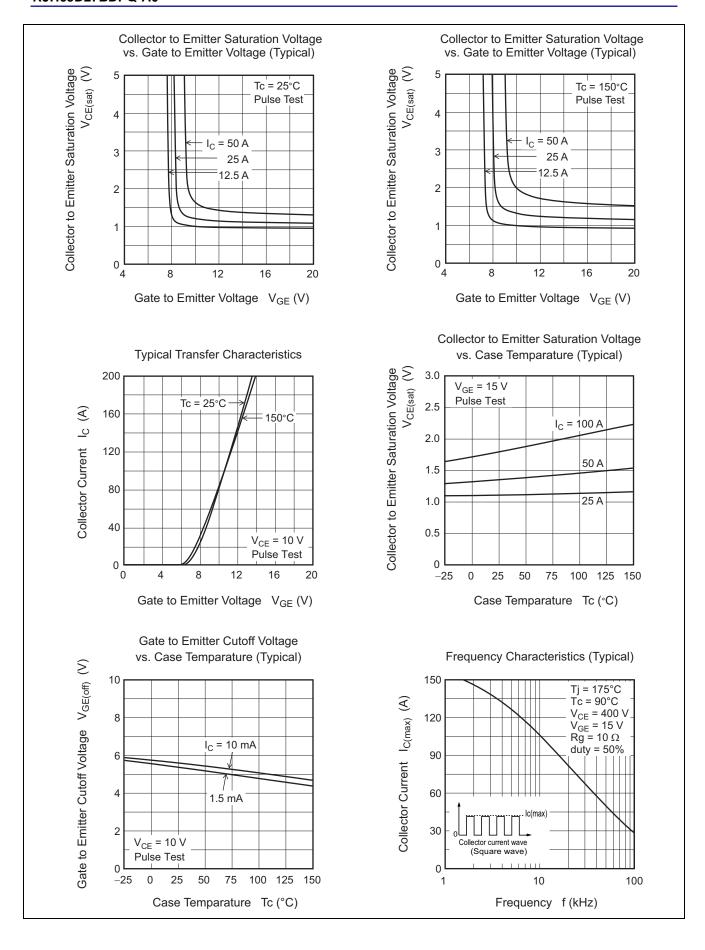
VF		1.7	2.2	V	$I_F = 50 \text{ A}^{\text{Note4}}$
trr	_	80	_	ns	I <sub>F</sub> = 50 A, di <sub>F</sub> /dt = 300 A/μs
Qrr	_	0.35	_	μC	
Im	_	7.5	_	Α	
	t <sub>rr</sub>	trr —	t <sub>rr</sub> — 80 Q <sub>rr</sub> — 0.35	t <sub>rr</sub> — 80 — Q <sub>rr</sub> — 0.35 —	t <sub>rr</sub> — 80 — ns Q <sub>rr</sub> — 0.35 — μC

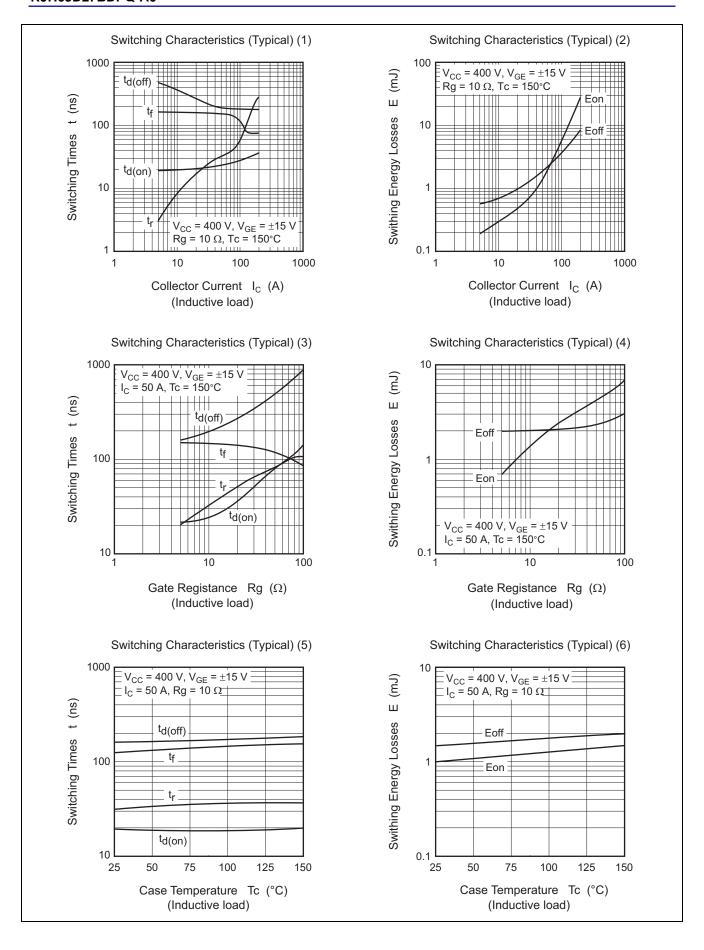
Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

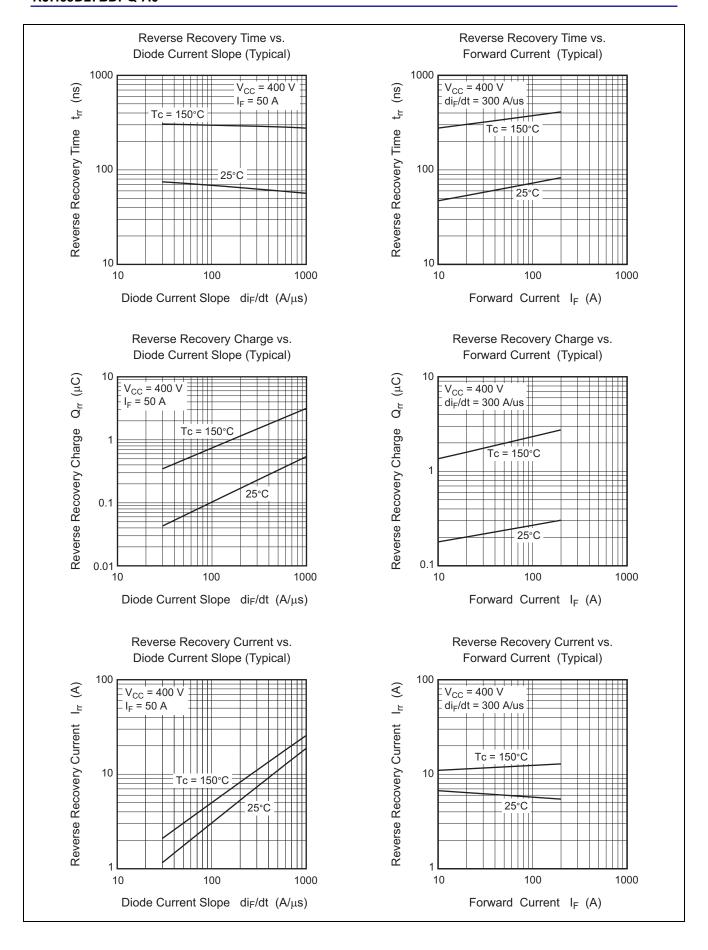
- 2. VGE = 15V
- 3. Please use this device in the thermal conditions which the junction temperature does not exceed 175°C Renesas IGBT Application Note is disclosed about reliability test and application condition up to 175°C
- Pulse test
- 5. Switching time test circuit and waveform are shown below.

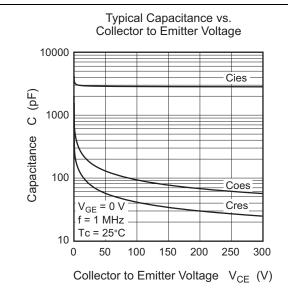
### **Main Characteristics**

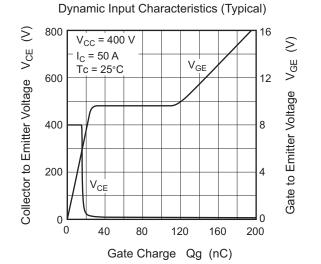




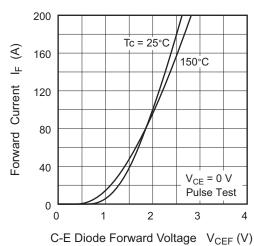


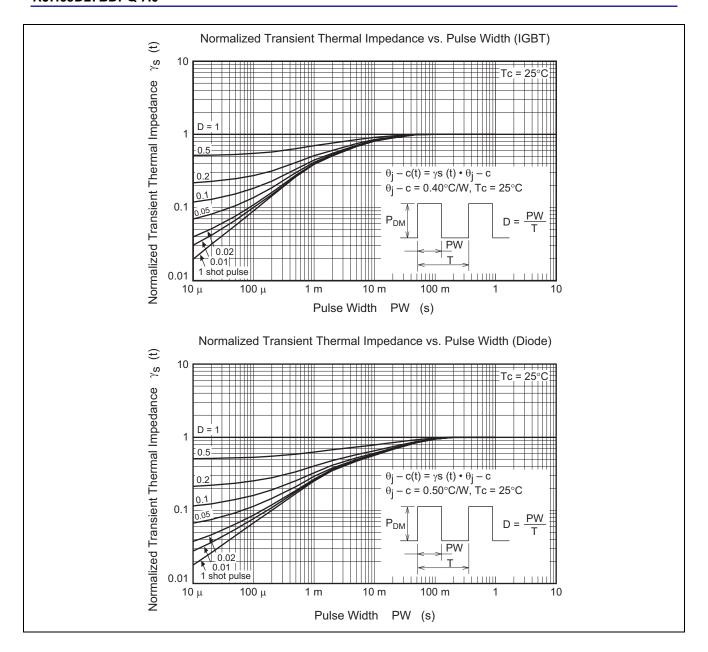


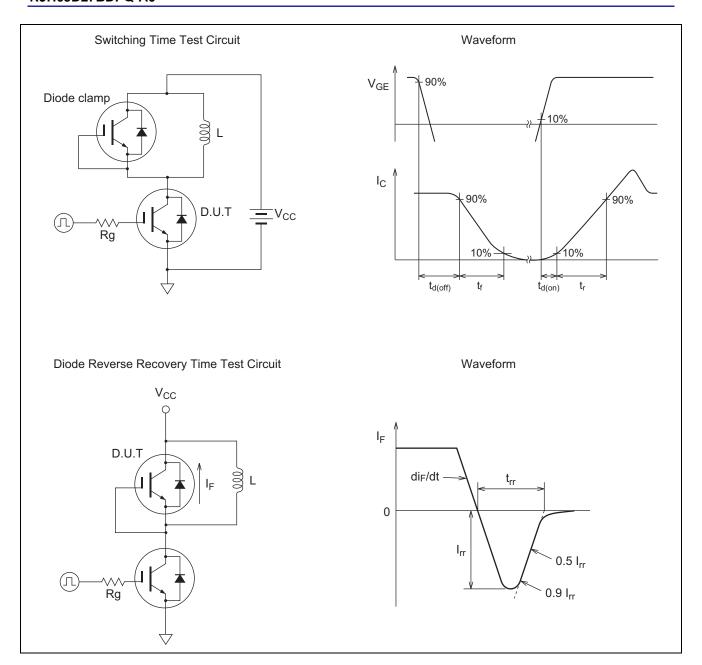




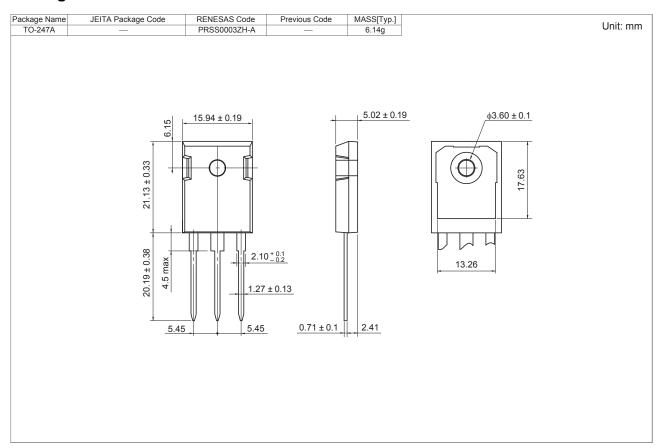
Forward Current vs. Forward Voltage (Typical)







# **Package Dimension**



# **Ordering Information**

Orderable Part No.	Quantity	Shipping Container
RJH65D27BDPQ-A0#T2	240 pcs	Box (Tube)

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