

# NSM21356DW6T1G

## Dual Complementary Transistors

### General Purpose PNP Transistor and NPN Transistors with Monolithic Bias Network

NSM21356DW6T1G contains a single PNP transistor and a monolithic bias network NPN transistor with two resistors; a series base resistor and a base-emitter resistor. This device is designed to replace multiple transistors and resistors on customer boards by integrating these components into a single device. NSM21356DW6T1G is housed in a SC-88/SOT-363 package which is ideal for low power surface mount applications in space constrained applications.

#### Features

- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- Q1: NPN BRT,  $R_1 = R_2 = 47\text{ k}$
- Q2: PNP
- This is a Pb-Free Device

#### Applications

- Logic Switching
- Amplification
- Driver Circuits
- Interface Circuits

#### MAXIMUM RATINGS

( $T_A = 25^\circ\text{C}$  unless otherwise noted)

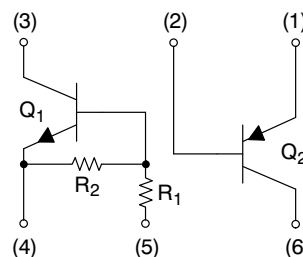
| Rating – Q1 (NPN BRT)          | Symbol        | Value | Unit |
|--------------------------------|---------------|-------|------|
| Collector-Base Voltage         | $V_{CBO}$     | 50    | Vdc  |
| Collector-Emitter Voltage      | $V_{CEO}$     | 50    | Vdc  |
| Collector Current              | $I_C$         | 100   | mAdc |
| Rating – Q2 (PNP)              | Symbol        | Value | Unit |
| Collector – Base Voltage       | $V_{(BR)CBO}$ | –80   | Vdc  |
| Collector – Emitter Voltage    | $V_{(BR)CEO}$ | –65   | Vdc  |
| Emitter – Base Voltage         | $V_{(BR)EBO}$ | –5.0  | Vdc  |
| Collector Current – Continuous | $I_C$         | –100  | mAdc |

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



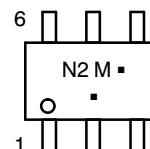
ON Semiconductor®

<http://onsemi.com>



SC-88/SOT-363  
CASE 419B  
STYLE 1

#### MARKING DIAGRAM



N2 = Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

#### ORDERING INFORMATION

| Device         | Package            | Shipping†        |
|----------------|--------------------|------------------|
| NSM21356DW6T1G | SC-88<br>(Pb-Free) | 3000/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

# NSM21356DW6T1G

## THERMAL CHARACTERISTICS

| Characteristic (One Junction Heated)                                                     | Symbol          | Max                           | Unit                       |
|------------------------------------------------------------------------------------------|-----------------|-------------------------------|----------------------------|
| Total Device Dissipation<br>$T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | $P_D$           | 180 (Note 1)<br>1.44 (Note 1) | mW<br>mW/ $^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient                                                  | $R_{\theta JA}$ | 692 (Note 1)                  | $^\circ\text{C/W}$         |
| Characteristic (Both Junctions Heated)                                                   | Symbol          | Max                           | Unit                       |
| Total Device Dissipation,<br>$T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | $P_D$           | 230<br>1.83                   | mW<br>mW/ $^\circ\text{C}$ |
| Thermal Resistance, Junction-to-Ambient                                                  | $R_{\theta JA}$ | 544                           | $^\circ\text{C/W}$         |
| Junction and Storage Temperature                                                         | $T_J, T_{stg}$  | -55 to +150                   | $^\circ\text{C}$           |

1. FR-4 @ Minimum Pad of 1.45 mm<sup>2</sup>, 1 oz Cu.

## ELECTRICAL CHARACTERISTICS – Q1 NPN BRT ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|----------------|--------|-----|-----|-----|------|
|----------------|--------|-----|-----|-----|------|

### OFF CHARACTERISTICS

|                                                                                       |               |    |   |     |      |
|---------------------------------------------------------------------------------------|---------------|----|---|-----|------|
| Collector-Base Cutoff Current<br>( $V_{CB} = 50\text{ V}$ , $I_E = 0$ )               | $I_{CBO}$     | -  | - | 100 | nAdc |
| Collector-Emitter Cutoff Current<br>( $V_{CE} = 50\text{ V}$ , $I_B = 0$ )            | $I_{CEO}$     | -  | - | 500 | nAdc |
| Emitter-Base Cutoff Current<br>( $V_{EB} = 6.0\text{ V}$ , $I_C = 0$ )                | $I_{EBO}$     | -  | - | 0.1 | mAdc |
| Collector-Base Breakdown Voltage<br>( $I_C = 10\text{ }\mu\text{A}$ , $I_E = 0$ )     | $V_{(BR)CBO}$ | 50 | - | -   | Vdc  |
| Collector-Emitter Breakdown Voltage (Note 2)<br>( $I_C = 2.0\text{ mA}$ , $I_B = 0$ ) | $V_{(BR)CEO}$ | 50 | - | -   | Vdc  |

### ON CHARACTERISTICS (Note 2)

|                                                                                                         |               |      |     |      |            |
|---------------------------------------------------------------------------------------------------------|---------------|------|-----|------|------------|
| DC Current Gain<br>( $V_{CE} = 10\text{ V}$ , $I_C = 5.0\text{ mA}$ )                                   | $h_{FE}$      | 80   | 140 | -    |            |
| Collector-Emitter Saturation Voltage<br>( $I_C = 10\text{ mA}$ , $I_B = 0.3\text{ mA}$ )                | $V_{CE(sat)}$ | -    | -   | 0.25 | Vdc        |
| Output Voltage (on)<br>( $V_{CC} = 5.0\text{ V}$ , $V_B = 3.5\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ )  | $V_{OL}$      | -    | -   | 0.2  | Vdc        |
| Output Voltage (off)<br>( $V_{CC} = 5.0\text{ V}$ , $V_B = 0.5\text{ V}$ , $R_L = 1.0\text{ k}\Omega$ ) | $V_{OH}$      | 4.9  | -   | -    | Vdc        |
| Input Resistor                                                                                          | $R_1$         | 32.9 | 47  | 61.1 | k $\Omega$ |
| Resistor Ratio                                                                                          | $R_1/R_2$     | 0.8  | 1.0 | 1.2  |            |

2. Pulse Test: Pulse Width < 300  $\mu\text{s}$ , Duty Cycle < 2.0%

# NSM21356DW6T1G

## ELECTRICAL CHARACTERISTICS – Q2 PNP ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic                                                                                                  | Symbol        | Min  | Typ | Max         | Unit                |
|-----------------------------------------------------------------------------------------------------------------|---------------|------|-----|-------------|---------------------|
| <b>OFF CHARACTERISTICS</b>                                                                                      |               |      |     |             |                     |
| Collector – Emitter Breakdown Voltage<br>( $I_C = -10\text{ mA}$ )                                              | $V_{(BR)CEO}$ | -65  | -   | -           | V                   |
| Collector – Emitter Breakdown Voltage<br>( $I_C = -10\text{ }\mu\text{A}$ , $V_{EB} = 0$ )                      | $V_{(BR)CES}$ | -80  | -   | -           | V                   |
| Collector – Base Breakdown Voltage<br>( $I_C = -10\text{ }\mu\text{A}$ )                                        | $V_{(BR)CBO}$ | -80  | -   | -           | V                   |
| Emitter – Base Breakdown Voltage<br>( $I_E = -1.0\text{ }\mu\text{A}$ )                                         | $V_{(BR)EBO}$ | -5.0 | -   | -           | V                   |
| Collector Cutoff Current ( $V_{CB} = -30\text{ V}$ )<br>( $V_{CB} = -30\text{ V}$ , $T_A = 150^\circ\text{C}$ ) | $I_{CBO}$     | -    | -   | -15<br>-4.0 | nA<br>$\mu\text{A}$ |

## ON CHARACTERISTICS

|                                                                                                                                                     |               |           |              |                |   |
|-----------------------------------------------------------------------------------------------------------------------------------------------------|---------------|-----------|--------------|----------------|---|
| DC Current Gain<br>( $I_C = -10\text{ }\mu\text{A}$ , $V_{CE} = -5.0\text{ V}$ )<br>( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ )           | $h_{FE}$      | -<br>220  | 150<br>290   | -<br>475       | - |
| Collector – Emitter Saturation Voltage<br>( $I_C = -10\text{ mA}$ , $I_B = -0.5\text{ mA}$ )<br>( $I_C = -100\text{ mA}$ , $I_B = -5.0\text{ mA}$ ) | $V_{CE(sat)}$ | -<br>-    | -<br>-       | -0.3<br>-0.65  | V |
| Base – Emitter Saturation Voltage<br>( $I_C = -10\text{ mA}$ , $I_B = -0.5\text{ mA}$ )<br>( $I_C = -100\text{ mA}$ , $I_B = -5.0\text{ mA}$ )      | $V_{BE(sat)}$ | -<br>-    | -0.7<br>-0.9 | -<br>-         | V |
| Base – Emitter On Voltage<br>( $I_C = -2.0\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ )<br>( $I_C = -10\text{ mA}$ , $V_{CE} = -5.0\text{ V}$ )          | $V_{BE(on)}$  | -0.6<br>- | -<br>-       | -0.75<br>-0.82 | V |

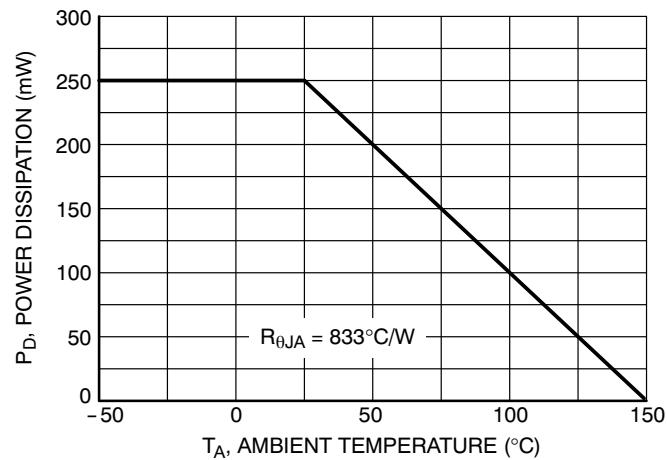


Figure 1. Derating Curve

# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

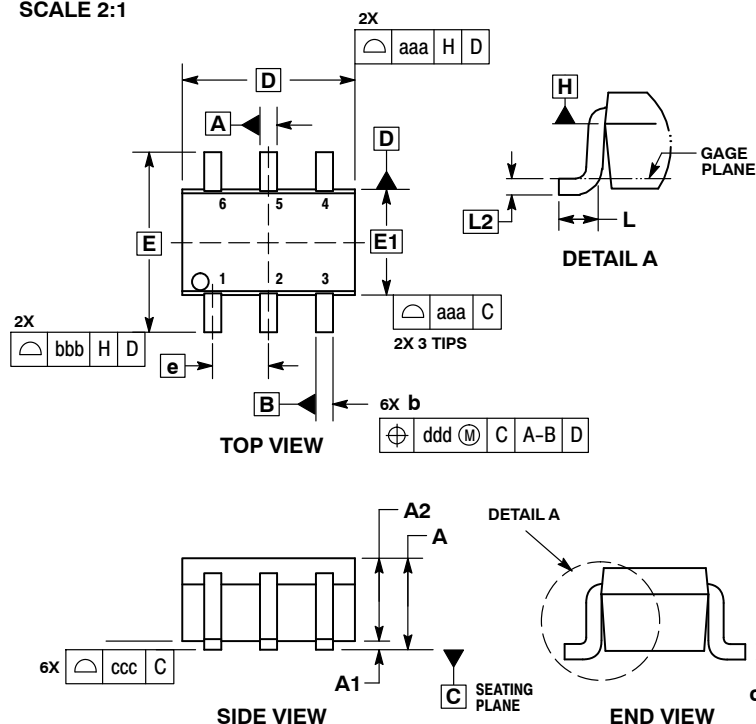
ON Semiconductor®



SCALE 2:1

SC-88/SC70-6/SOT-363  
CASE 419B-02  
ISSUE Y

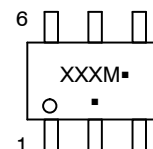
DATE 11 DEC 2012



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION: MILLIMETERS.
  3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
  4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
  5. DATUMS A AND B ARE DETERMINED AT DATUM H.
  6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
  7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

| DIM | MILLIMETERS |      |      | INCHES    |       |       |
|-----|-------------|------|------|-----------|-------|-------|
|     | MIN         | NOM  | MAX  | MIN       | NOM   | MAX   |
| A   | ---         | ---  | 1.10 | ---       | ---   | 0.043 |
| A1  | 0.00        | ---  | 0.10 | 0.000     | ---   | 0.004 |
| A2  | 0.70        | 0.90 | 1.00 | 0.027     | 0.035 | 0.039 |
| b   | 0.15        | 0.20 | 0.25 | 0.006     | 0.008 | 0.010 |
| C   | 0.08        | 0.15 | 0.22 | 0.003     | 0.006 | 0.009 |
| D   | 1.80        | 2.00 | 2.20 | 0.070     | 0.078 | 0.086 |
| E   | 2.00        | 2.10 | 2.20 | 0.078     | 0.082 | 0.086 |
| E1  | 1.15        | 1.25 | 1.35 | 0.045     | 0.049 | 0.053 |
| e   | 0.65 BSC    |      |      | 0.026 BSC |       |       |
| L   | 0.26        | 0.36 | 0.46 | 0.010     | 0.014 | 0.018 |
| L2  | 0.15 BSC    |      |      | 0.006 BSC |       |       |
| aaa | 0.15        |      |      | 0.006     |       |       |
| bbb | 0.30        |      |      | 0.012     |       |       |
| ccc | 0.10        |      |      | 0.004     |       |       |
| ddd | 0.10        |      |      | 0.004     |       |       |

## GENERIC MARKING DIAGRAM\*



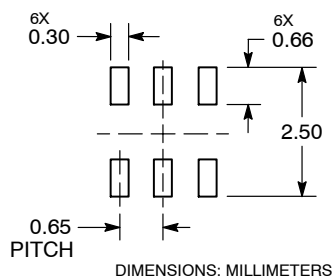
XXX = Specific Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

## RECOMMENDED SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## STYLES ON PAGE 2

|                  |                      |                                                                                                                                                                                     |
|------------------|----------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| DOCUMENT NUMBER: | 98ASB42985B          | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| DESCRIPTION:     | SC-88/SC70-6/SOT-363 | PAGE 1 OF 2                                                                                                                                                                         |

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

**SC-88/SC70-6/SOT-363**  
**CASE 419B-02**  
**ISSUE Y**

DATE 11 DEC 2012

|                                                                                                                   |                                                                                                               |                                                                                                                    |                                                                                                                    |                                                                                                                    |                                                                                                              |
|-------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------|
| <b>STYLE 1:</b><br>PIN 1. EMITTER 2<br>2. BASE 2<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. BASE 1<br>6. COLLECTOR 2 | <b>STYLE 2:</b><br>CANCELLED                                                                                  | <b>STYLE 3:</b><br>CANCELLED                                                                                       | <b>STYLE 4:</b><br>PIN 1. CATHODE<br>2. CATHODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. ANODE               | <b>STYLE 5:</b><br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | <b>STYLE 6:</b><br>PIN 1. ANODE 2<br>2. N/C<br>3. CATHODE 1<br>4. ANODE 1<br>5. N/C<br>6. CATHODE 2          |
| <b>STYLE 7:</b><br>PIN 1. SOURCE 2<br>2. DRAIN 2<br>3. GATE 1<br>4. SOURCE 1<br>5. DRAIN 1<br>6. GATE 2           | <b>STYLE 8:</b><br>CANCELLED                                                                                  | <b>STYLE 9:</b><br>PIN 1. EMITTER 2<br>2. EMITTER 1<br>3. COLLECTOR 1<br>4. BASE 1<br>5. BASE 2<br>6. COLLECTOR 2  | <b>STYLE 10:</b><br>PIN 1. SOURCE 2<br>2. SOURCE 1<br>3. GATE 1<br>4. DRAIN 1<br>5. DRAIN 2<br>6. GATE 2           | <b>STYLE 11:</b><br>PIN 1. CATHODE 2<br>2. CATHODE 2<br>3. ANODE 1<br>4. CATHODE 1<br>5. CATHODE 1<br>6. ANODE 2   | <b>STYLE 12:</b><br>PIN 1. ANODE 2<br>2. ANODE 2<br>3. CATHODE 1<br>4. ANODE 1<br>5. ANODE 1<br>6. CATHODE 2 |
| <b>STYLE 13:</b><br>PIN 1. ANODE<br>2. N/C<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE<br>6. CATHODE                 | <b>STYLE 14:</b><br>PIN 1. VREF<br>2. GND<br>3. GND<br>4. IOUT<br>5. VEN<br>6. VCC                            | <b>STYLE 15:</b><br>PIN 1. ANODE 1<br>2. ANODE 2<br>3. ANODE 3<br>4. CATHODE 3<br>5. CATHODE 2<br>6. CATHODE 1     | <b>STYLE 16:</b><br>PIN 1. BASE 1<br>2. EMITTER 2<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 1<br>6. COLLECTOR 1 | <b>STYLE 17:</b><br>PIN 1. BASE 1<br>2. EMITTER 1<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER 2<br>6. COLLECTOR 1 | <b>STYLE 18:</b><br>PIN 1. VIN1<br>2. VCC<br>3. VOUT2<br>4. VIN2<br>5. GND<br>6. VOUT1                       |
| <b>STYLE 19:</b><br>PIN 1. IOUT<br>2. GND<br>3. GND<br>4. V CC<br>5. V EN<br>6. V REF                             | <b>STYLE 20:</b><br>PIN 1. COLLECTOR<br>2. COLLECTOR<br>3. BASE<br>4. EMITTER<br>5. COLLECTOR<br>6. COLLECTOR | <b>STYLE 21:</b><br>PIN 1. ANODE 1<br>2. N/C<br>3. ANODE 2<br>4. CATHODE 2<br>5. N/C<br>6. CATHODE 1               | <b>STYLE 22:</b><br>PIN 1. D1 (i)<br>2. GND<br>3. D2 (i)<br>4. D2 (c)<br>5. VBUS<br>6. D1 (c)                      | <b>STYLE 23:</b><br>PIN 1. Vn<br>2. CH1<br>3. Vp<br>4. N/C<br>5. CH2<br>6. N/C                                     | <b>STYLE 24:</b><br>PIN 1. CATHODE<br>2. ANODE<br>3. CATHODE<br>4. CATHODE<br>5. CATHODE<br>6. CATHODE       |
| <b>STYLE 25:</b><br>PIN 1. BASE 1<br>2. CATHODE<br>3. COLLECTOR 2<br>4. BASE 2<br>5. EMITTER<br>6. COLLECTOR 1    | <b>STYLE 26:</b><br>PIN 1. SOURCE 1<br>2. GATE 1<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 2<br>6. DRAIN 1      | <b>STYLE 27:</b><br>PIN 1. BASE 2<br>2. BASE 1<br>3. COLLECTOR 1<br>4. EMITTER 1<br>5. EMITTER 2<br>6. COLLECTOR 2 | <b>STYLE 28:</b><br>PIN 1. DRAIN<br>2. DRAIN<br>3. GATE<br>4. SOURCE<br>5. DRAIN<br>6. DRAIN                       | <b>STYLE 29:</b><br>PIN 1. ANODE<br>2. ANODE<br>3. COLLECTOR<br>4. EMITTER<br>5. BASE/ANODE<br>6. CATHODE          | <b>STYLE 30:</b><br>PIN 1. SOURCE 1<br>2. DRAIN 2<br>3. DRAIN 2<br>4. SOURCE 2<br>5. GATE 1<br>6. DRAIN 1    |

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

|                         |                             |                                                                                                                                                                                     |
|-------------------------|-----------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98ASB42985B</b>          | Electronic versions are uncontrolled except when accessed directly from the Document Repository.<br>Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>SC-88/SC70-6/SOT-363</b> | <b>PAGE 2 OF 2</b>                                                                                                                                                                  |

ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Email Requests to: [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**onsemi Website:** [www.onsemi.com](http://www.onsemi.com)

### TECHNICAL SUPPORT

**North American Technical Support:**

Voice Mail: 1 800-282-9855 Toll Free USA/Canada

Phone: 011 421 33 790 2910

**Europe, Middle East and Africa Technical Support:**

Phone: 00421 33 790 2910

For additional information, please contact your local Sales Representative