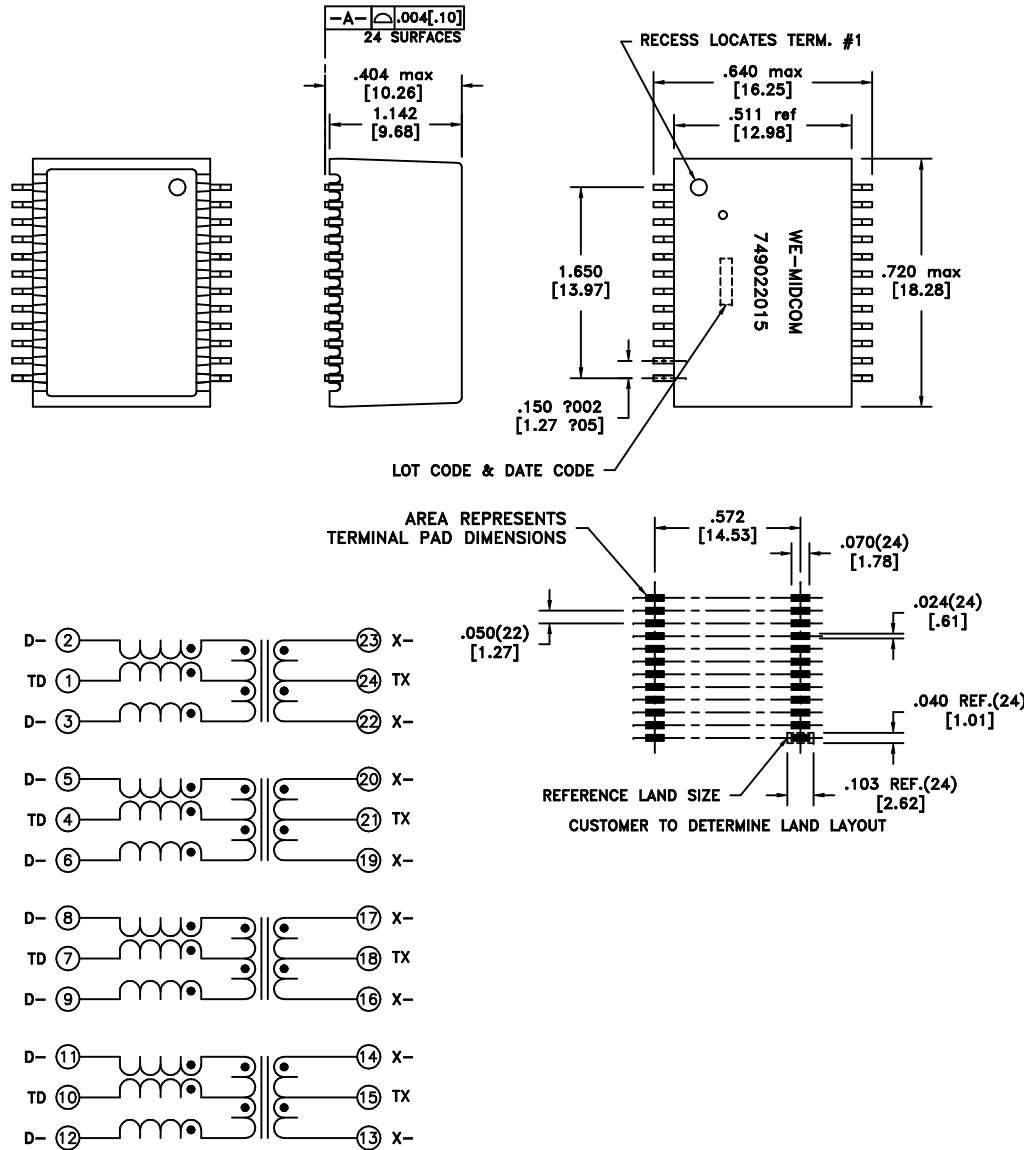


|            |          |      |               |
|------------|----------|------|---------------|
| CUSTOMER   | TERMINAL | RoHS | LEAD(Pb)-FREE |
| Sn96% Ag4% | Yes      | Yes  | Yes           |



**ELECTRICAL SPECIFICATIONS @ 25°C unless otherwise noted:**

| PARAMETER      | TEST CONDITIONS                                    | VALUE              |
|----------------|--|--------------------|
| INDUCTANCE     | 23-22 100kHz, 100mVAC, 24mADC, Lp                  | 350uH min.         |
| INDUCTANCE     | 20-19 100kHz, 100mVAC, 24mADC, Lp                  | 350uH min.         |
| INDUCTANCE     | 17-16 100kHz, 100mVAC, 24mADC, Lp                  | 350uH min.         |
| INDUCTANCE     | 14-13 100kHz, 100mVAC, 24mADC, Lp                  | 350uH min.         |
| DIELECTRIC     | 1-24 tie(1+4+7+10, 15+18+21+24) 1800Vrms, 1 second | 1500Vrms, 1 minute |
| TURNS RATIO    | (2-3):(23-22)                                      | 1:1, ±2%           |
| TURNS RATIO    | (5-6):(20-19)                                      | 1:1, ±2%           |
| TURNS RATIO    | (8-9):(17-16)                                      | 1:1, ±2%           |
| TURNS RATIO    | (11-12):(14-13)                                    | 1:1, ±2%           |
| INSERTION LOSS | 300k - 100MHz                                      | -1.0dB max.        |
| RETURN LOSS    | 500kHz - 40MHz                                     | -18dB min.         |
| RETURN LOSS    | 40M - 60MHz  | -15dB min.         |
| RETURN LOSS    | 60M - 80MHz  | -12dB min.         |
| RETURN LOSS    | 80M - 100MHz                                       | -10dB min.         |
| CROSSTALK      | 300kHz - 100MHz                                    | -35dB min.         |
| CMRR           | 300k - 32MHz                                       | -42dB min.         |
| CMRR           | 32M - 100MHz                                       | -33dB min.         |

**GENERAL SPECIFICATIONS:**

OPERATING TEMPERATURE RANGE: -40°C to +85°C.

Designed to comply with POE+ requirements, and handle 720mA DC current.

COPLANARITY: All 24 terminals must lie on a plane within .004 [.10] of Surface A after lead tinning.

| REV. | DATE  |
|------|-------|
| 01   | 10/09 |
| 00   | 3/09  |

Packaging Specifications  
 Method: Tape & Reel  
 PKG-0653  
 www.midcom-inc.com

Unless otherwise specified:  
 Tolerances: Fractions: ±1/64  
 Angles: ±1° Decimals: ±.005 [.127]

This drawing is dual dimensioned. Dimensions in brackets are in millimeters.

DRAWING TITLE  
**TRANSFORMER**  
**AEDFP-G24**  
 eiSos p/n: 749022015



DRAWING NO.  
**749022015**

REVISIONS: SEE SHEET 1

SCALE ---

SHEET 2 OF 7