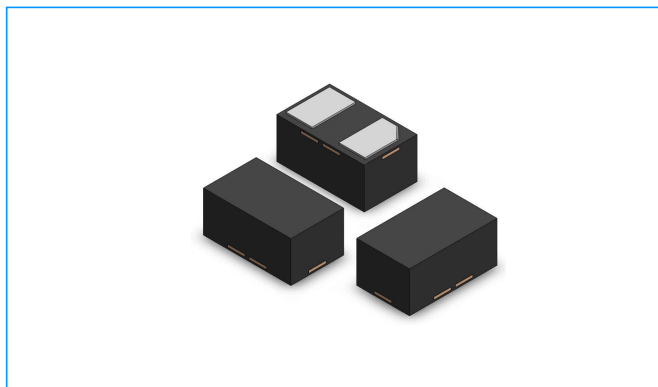


Transient Voltage Suppressors for ESD Protection

ESD3.3V88D-LA

Description

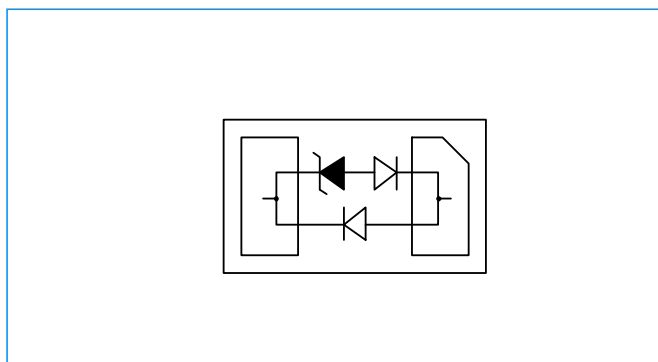
The ESD3.3V88D-LA is low capacitance TVS arrays designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from over-voltage caused by ESD (electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).



Feature

- ◆ 100 Watts Peak Pulse Power per Line (tp=8/20µs)
- ◆ Protects One Unidirectional I/O Line
- ◆ Low clamping voltage
- ◆ Low Capacitance
- ◆ Working voltages : 3.3V
- ◆ IEC61000-4-4 (EFT) 40A (5/50ns)
- ◆ IEC61000-4-5 (LIGHTING) 9A (8/20µs)
- ◆ IEC61000-4-2(ESD):±17kV (air discharge)
±15kV (contact discharge)

Functional Diagram



Applications

- ◆ Cellular Handsets & Accessories
- ◆ Keypads, Side Keys, Audio Ports
- ◆ Portable Instrumentation
- ◆ Notebooks, Desktops, and Servers
- ◆ Digital Lines
- ◆ Tablet PC

Mechanical Data

- ◆ SOD-882/DFN1006 (1.0x0.6x0.5mm) Package
- ◆ Molding Compound Flammability Rating : UL 94V-O
- ◆ Weight 0.5 Milligrams (Approximate)
- ◆ Lead Finish : Lead Free

Mechanical Characteristics

Symbol	Parameter	Value	Units
P _{PP}	Peak Pulse Power (tp=8/20µs waveform)	100	Watts
T _L	Lead Soldering Temperature	260 (10 sec.)	°C
T _{STG}	Storage Temperature Range	-55 to +150	°C
T _J	Operating Junction Temperature Range	-40 to +125	°C

Transient Voltage Suppressors for ESD Protection

ESD3.3V88D-LA

Electrical Characteristics (@ 25°C Unless Otherwise Specified)

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Reverse Working Voltage	V_{RWM}	--	--	--	3.3	V
Reverse Breakdown Voltage	V_{BR}	$I_T=1mA$;	4.5	--	--	V
Reverse Leakage Current	I_R	$V_{RWM}=3.3V, T=25^{\circ}C$;	--	--	1.0	μA
Positive Clamping Voltage	V_C	$I_{PP}=3A, T_P=8/20\mu s$;	--	--	8.3	V
		$I_{PP}=5A, T_P=8/20\mu s$;	--	--	9.0	V
		$I_{PP}=9A, T_P=8/20\mu s$;	--	--	11.0	V
Junction capacitance	C_J	$V_R=0V, f=1MHz$;	--	1.0	--	pF

Characteristic Curves

Fig1. 8/20 μs Pulse Waveform

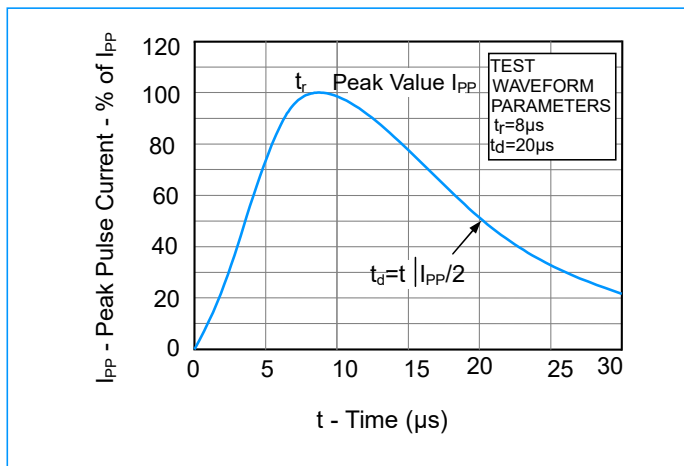


Fig2. Power Derating Curve

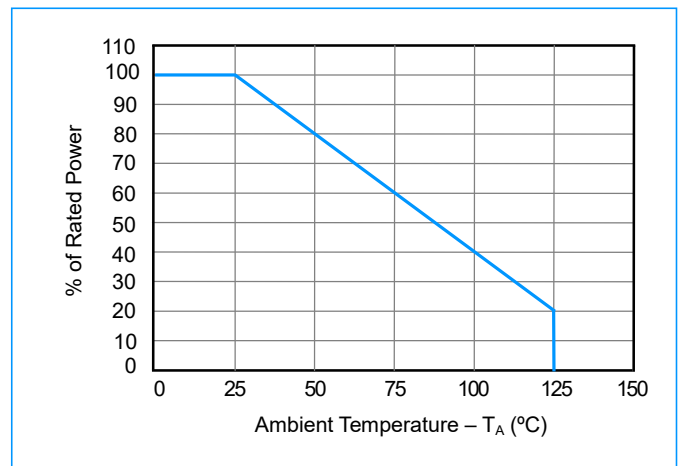


Fig3. Clamping Voltage vs. Peak Pulse Current ($t_p=8/20\mu s$)

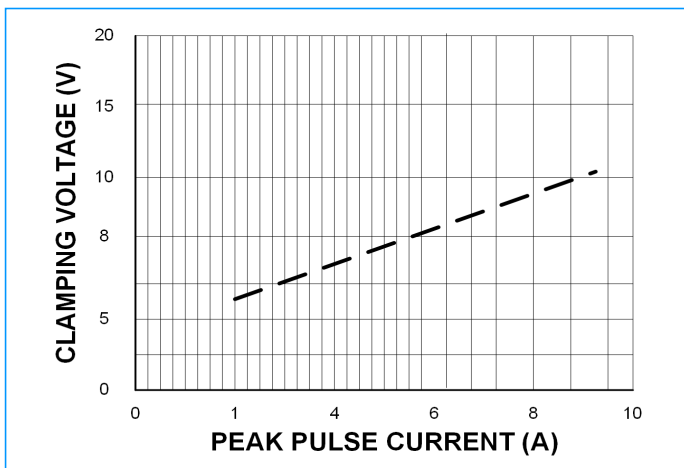
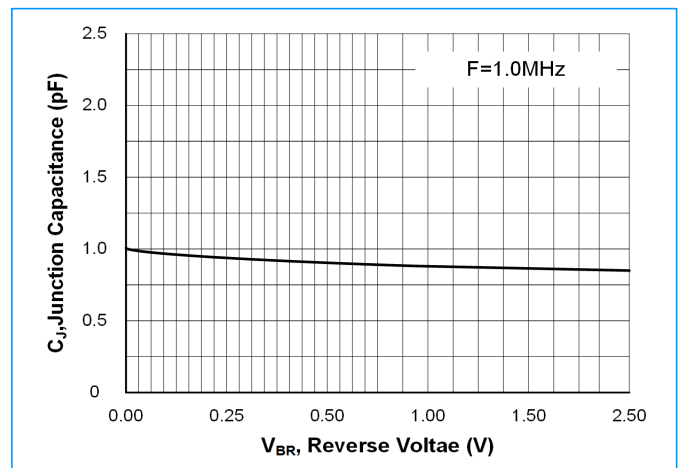


Fig4. Typical Capacitance vs. Reverse Voltage

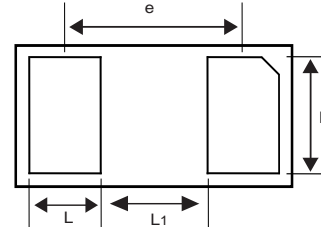
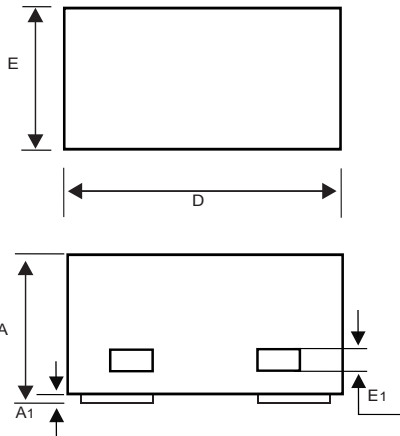


Transient Voltage Suppressors for ESD Protection

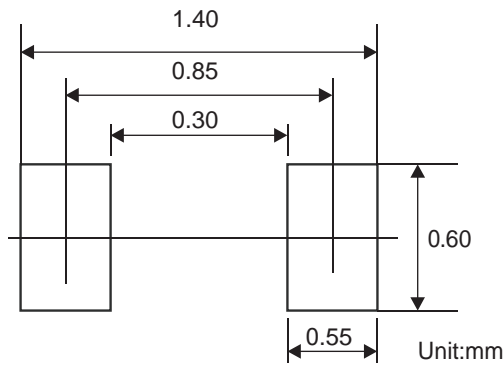
ESD3.3V88D-LA

SOD-882/DFN1006 Package Outline & Dimensions

SOD-882/DFN1006



Suggested PAD Layout



Symbol	Millimeters		
	Min	Nom	Max
A	0.450	0.500	0.550
A1	0	0.020	0.050
E1	0.013	0.063	0.113
D	0.900	1.000	1.100
E	0.500	0.600	0.700
e	0.65BSC		
L	0.150	0.250	0.350
b	0.400	0.500	0.600
L1	0.300	0.400	0.500

Ordering Information

Device	Marking	Package	Quantity	Reel Size
ESD3.3V88D-LA	4L	SOD-882/DFN1006	10,000pcs/Reel	7 inch