

RF-TCA8418-MVK MAVRK Module

User's Guide



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RF-TCA8418-MVK MAVRK Module

This document goes into the details of the RF-TCA8418-MVK board. It includes information about how the module functions and how to use the board. Schematics and layout documentation are also included.

1 EVM Overview

The RF-TCA8418-MVK module provides I2C access to a keypad and joystick with select. The module is compatible with any Modular and Versatile Reference Kit (MAVRK) [motherboard](#) with an open [RF](#) slot. The RF slot is designed for communications and as an input interface between the MAVRK motherboard and a PC, external components, or users. This module connects to the motherboard's RF port. For a full list of RF pinouts with description please see the [RF Pinout for MAVRK](#) wiki page.

1.1 EVM Description

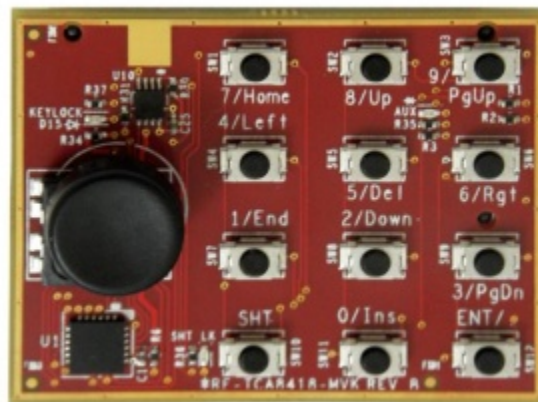


Figure 1. RF-TCA8418-MVK MAVRK Module

The RF-TCA8418-MVK Module provides row/column scanning of:

- Twelve (12) switch keypad with press and release detect.
- A four (4) position joystick (Up, Down, Right, Left) plus a *push to select* function.

TCA8418 features highlighted by this MAVRK module include:

- Last ten (10) keystroke storage.
- Keylock and Unlock functionality.

1.2 Highlighted Products

- [TCA8418 I2C Controlled Keypad Scan IC](#)
- [TS5A2066 Dual-Channel 10-Ohm SPST Analog Switch](#)

1.3 Block Diagram

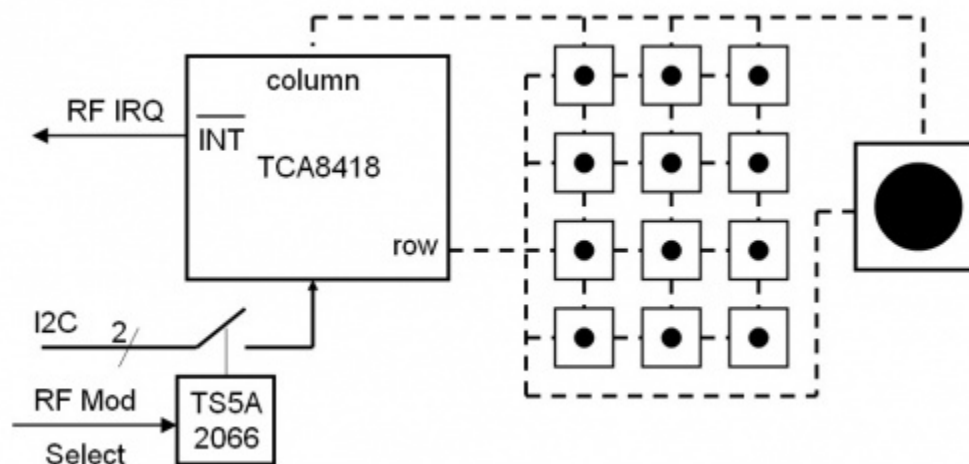


Figure 2. A block diagram of the RF-TCA8418-MVK MAVRK Module

1.4 EVM Wiki

[RF-TCA8418-MVK MAVRK Module wiki page](#)

1.5 EVM Landing Page

[RF-TCA8418-MVK MAVRK Module tool folder](#)

2 Hardware Description

2.1 Power Requirements

3.3V DC is supplied to the RF-TCA8418-MVK through the [RF Connector](#) (RF2, pin 9). The RF-TCA8418-MVK module can operate over the voltage range of 1.65V to 3.65V DC with a typical current draw of less than 1mA.

2.2 Getting Started: Configuring the EVM

Configuring the RF-TCA8418-MVK EVM is as simple as installing it into one (1) of the RF slots on a motherboard, such as the [MAVRK Pro Motherboard](#).

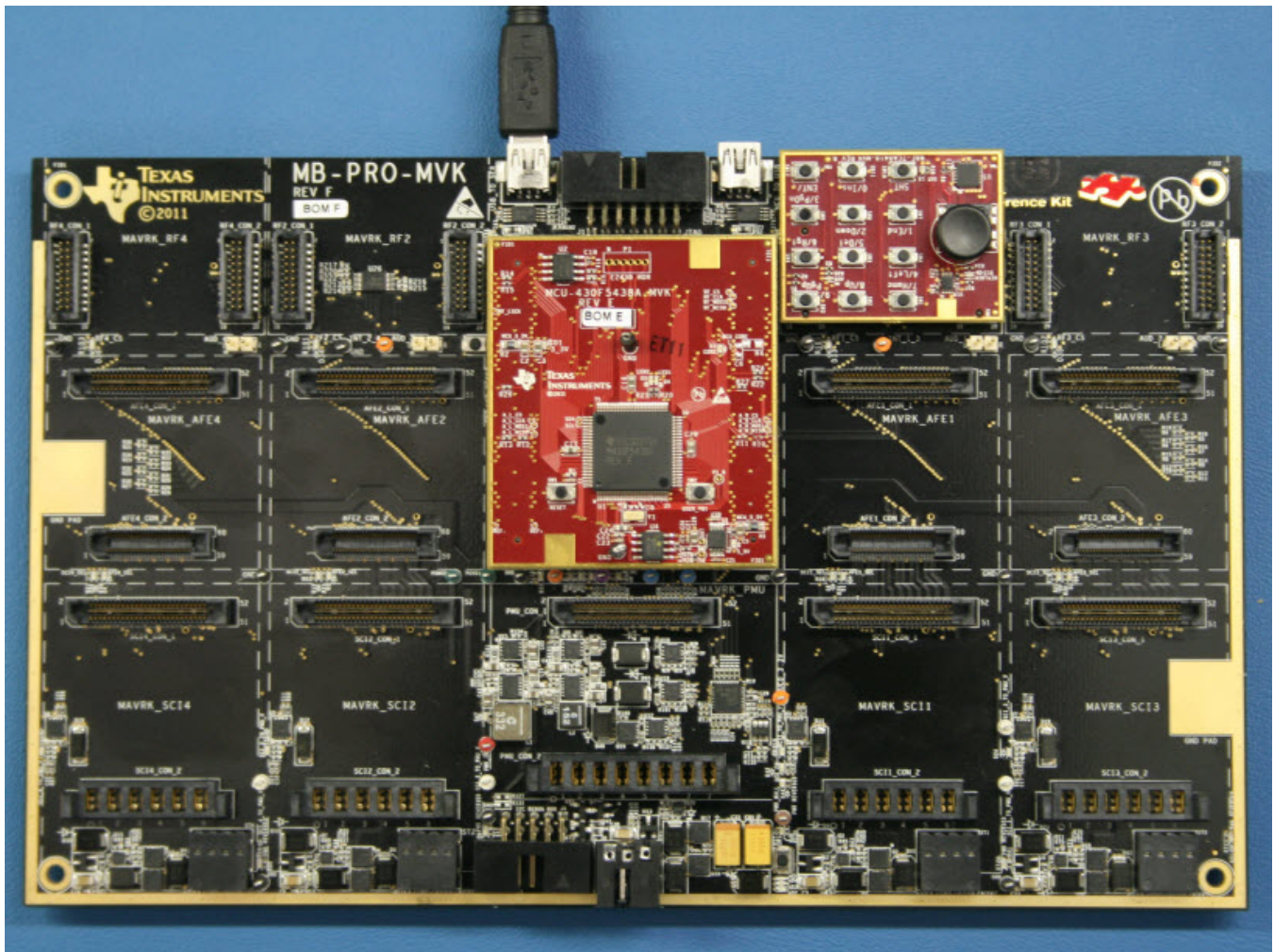


Figure 3. RF-TCA8418-MVK Module on the MAVRK Pro Motherboard

Note: Most demos and test routines use MAVRK_RF1, but any of the RF slots can be used as long as the changes are noted in the #define of the software. For example, in the TCA8418 demo project, the RIGHT and LEFT are #defined to the MAVRK_RF3 and MAVRK_RF4 slots.


```

74/*****
75*           Definitions
76/*****
77#define RIGHT   MAVRK_RF3
78#define LEFT    MAVRK_RF4
79/*****
80*           Prototypes
81/*****
82
83/*****
84*           Global Variables
85/*****
86// We will allocate 3 PC communication packets for this test. One will be used to send TCA8418 data, one will be used to ACK PC co
87// and one will be used to receive commands from the PC. Note that we could have re-used a packet for the
88// ACKs in order to save memory.
89
90PC_Interface_Packet_type   PcRcvPacket;
91

```

Figure 4. TCA8418 RF Slots Defined in the C Code

These definitions are used further in the code for initialization of the module and later for the checking for a key press for that module.

```

124
125/*****
126/*   Initialize TCA8418 Component
127/*****
128// Right is defined in the Definitions section of this file
129   mvk_Initialize_Keyboard (RIGHT);
130// For Demo's requiring a second keyboard remove comments on the line below
131   mvk_Initialize_Keyboard (LEFT);
132/*****
133/*   Use for testing the PC Interface from MAVRK TCA8418 board to a host PC
134/*****
135//   mvk_Send_TCA8418_Test_Packet(TCA8418_Packet_Data, TCA8418_DEVICE_SLOT, 1);           // send one set of test packets
136
137#ifndef DEBUG
138// This call is necessary to enable any devices using the GPIOs on the AFE slots. It is used in DEBUG mode to light
139   mvk_Enable_AFE_Latched_GPIOs ();           // Enable the AFE Latched GPIO to start output of data
140#endif
141
142/*****
143*           main.c - Background Tasks
144/*****
145   mvk_UART_Debug_Printf_Flush ("\033[2J", 4);
146   mvk_UART_Debug_Printf_Flush ("\r\nTCA8418-MVK Demo\r\n", 20);
147   mvk_UART_Debug_Printf_Flush ("\r\nMAVRK packets and terminal messages will be provided for", 58);
148   mvk_UART_Debug_Printf_Flush ("\r\nEach press and release of keys.\r\n\r\n", 37);
149
150   while (1)
151   {
152       idle_count++;           // Idle Count is incremented every time
153
154       if (mvk_Is_User_Button_Pushed () )
155       {
156           mvk_Toggle_MCU_LED (MCU_YELLOW);
157           mvk_Clear_User_Button_Status ();
158       }
159
160       // Is there a keypress and TCA8418 data ready to send to the PC?
161       if (TCA8418_DATA_AVAILABLE == mvk_Check_and_Process_Key_Press (RIGHT)) // Send current key events to the PC Int
162       {
163           mvk_Toggle_MCU_LED (MCU_YELLOW);
164           idle_count = 0;
165       }

```

Figure 5. TCA8418 RF Slot Definitions Used as C Code Variable Names

The default slots of MAVRK_RF3 and MAVRK_RF4 from the demo can be easily changed to, for example, MAVRK_RF1 and MAVRK_RF2 by simply changing the RIGHT (#define RIGHT MAVRK_RF1) and LEFT (#define LEFT MAVRK_RF2).

2.3 EVM Connectors, Fuses, and Switches

The RF-TCA8418-MVK EVM has two connectors on the back side of the module that connect it to an RF slot on a motherboard like the [MAVRK Pro Motherboard](#). For a full list of RF pinouts with description please see the [RF Pinout for MAVRK](#) wiki page.



Figure 6. Back side of the RF-TCA8418-MVK MAVRK Module, showing the RF connectors

The RF-TCA8418-MVK EVM consists of twelve (12) momentary switch push buttons and one (1) 8 position navigation switch with a *push to select* function. The function of 11 of the momentary switches is defined by the 12th switch, the SHIFT switch.

The primary functions (SHIFT disabled) are shown below:

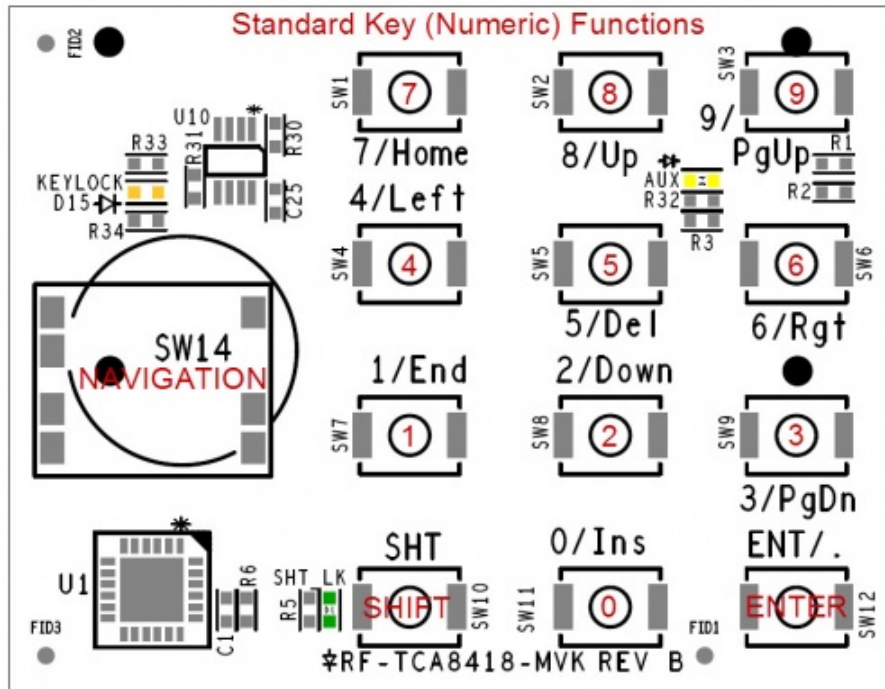


Figure 7. Functions of the keys while SHIFT is disabled

The SHIFT Lock functions (SHIFT enabled) are shown below:

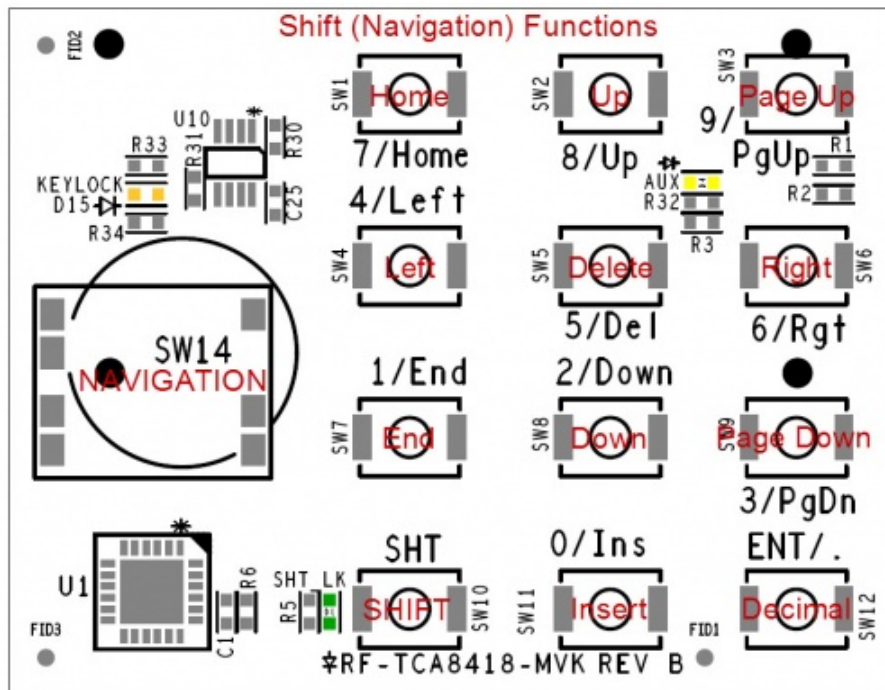


Figure 8. Functions of the keys while SHIFT is enabled

2.4 EVM Test Points

Due to the simple nature of the RF-TCA8418-MVK, the EVM has no test points. The polling of the I2C lines is can be done by utilizing an [RF Breakout module](#) in the RF slot adjacent to the slot containing the RF-TCA8418-MVK.

2.5 EVM LEDs

The RF-TCA8418-MVK Module has 3 LEDs:

- A green LED signifying a Shift-Lock function
- An orange LED signifying a Key-Lock function
- A user defined yellow LED

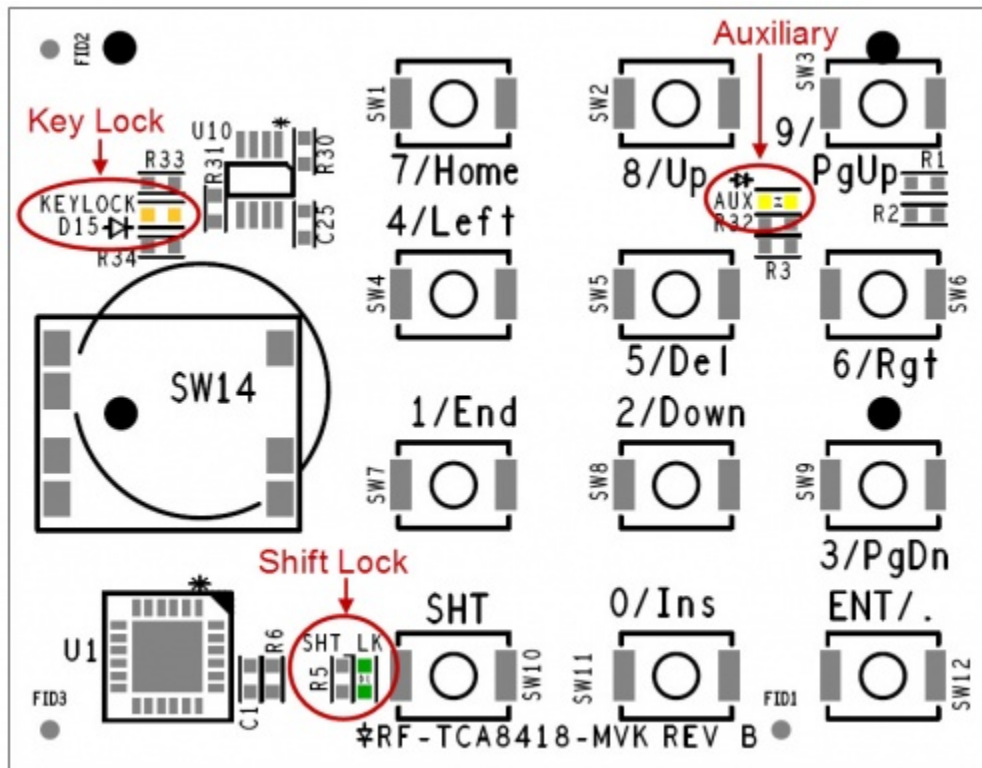


Figure 9. Map of LEDs on the RF-TCA8418-MVK MAVRK Module

3 Software Description

3.1 MAVRK Software Minimum Requirements

- [IAR Embedded Workbench](#) software or [TI Code Composer Studio](#) software installed on PC
- [MSP-FET430UIF - MSP430 USB Debugging Interface](#)
- USB Cable(A to Micro AB) to power the MAVRK Pro motherboard
- Windows XP SP3 or Windows 7

3.2 How to get the MAVRK Software

You will need the MAVRK Software repository installed on your PC. This repository will sync the MAVRK firmware to your PC.

Please see [Software Installation Guide](#).

3.3 Where do I find the MAVRK Qt Demo Application?

An application to visual packet information from the embedded system can be found in the **mavrk_qt_tool** software repository under the **Released Version - QT Demo Application** directory. Please see [Software Installation Guide](#) for instructions on cloning the QT Tool project.

If you desire to create your own Qt demonstration, please reference the following resources:

- [MAVRK Qt GUI SDK Installation Guide](#)
- [MAVRK Qt GUI Build Guide](#)

3.4 Where do I find the Demo and Test Code?

From the software library, synchronized from the Gerrit server you will find:

- Driver code related to the specific part can be found in a folder under the **mavrk_embeddedModular_EVM_Libraries\Components** directory.
- Projects utilizing this part are located under the **mavrk_embeddedModular_EVM_Projects** folder.
- Specific related projects for this part are:

Please refer to the links below for instructions on running a demo application with the TCA8418.

- [Running the TCA8418 Demo on CCS](#)
- [Running the TCA8418 Demo on IAR](#)

4 Board Files

4.1 Bill of Materials (BOM)

[Download a PDF](#) of the bill of materials.

QTY	DESCRIPTION	REF	VALUE	UNIT	MANUFACTURER	PART NO.
1	RES 0603	R33	10K	RES	YAGEO	RC0603F10K00
1	RES 0603	R34	10K	RES	YAGEO	RC0603F10K00
1	RES 0603	R35	10K	RES	YAGEO	RC0603F10K00
1	RES 0603	R36	10K	RES	YAGEO	RC0603F10K00
1	RES 0603	R37	10K	RES	YAGEO	RC0603F10K00
1	CAP 0603	C1	100N	CAP	TAIYO YUDEN	C1005C100K0J0000
1	CAP 0603	C25	100N	CAP	TAIYO YUDEN	C1005C100K0J0000
1	SWITCH	SW1	7/Home	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW2	8/Up	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW3	9/PgUp	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW4	4/Left	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW5	5/Del	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW6	6/Rgt	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW7	1/End	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW8	2/Down	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW9	3/PgDn	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW10	0/Ins	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW11	ENT/.	SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW12		SW	TEKNOLOGIE	SW1000000000
1	SWITCH	SW14		SW	TEKNOLOGIE	SW1000000000
1	IC	U1		IC	TI	U1000000000

Figure 10. RF-TCA8418-MVK Bill of Materials

4.2 Layout (PDF)

[Download a PDF](#) of additional board layers.

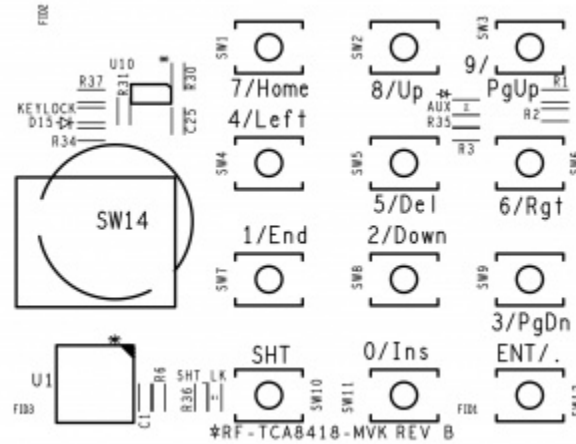


Figure 11. RF-TCA8418-MVK Board Top Silkscreen

4.3 Schematics (PDF)

[Download a PDF](#) of the schematic.

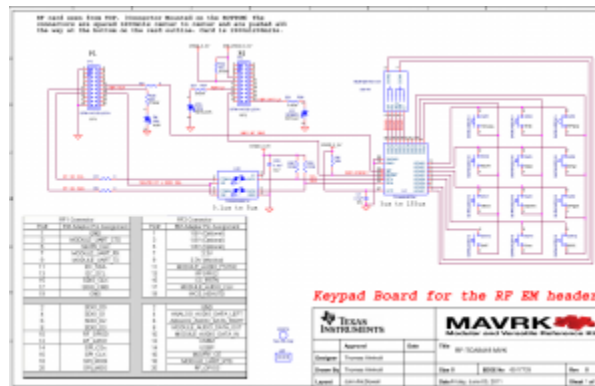


Figure 12. RF-TCA8418-MVK Schematic

4.4 Fabrication Drawings (PDF)

[Download a PDF](#) of the fabrication drawing.

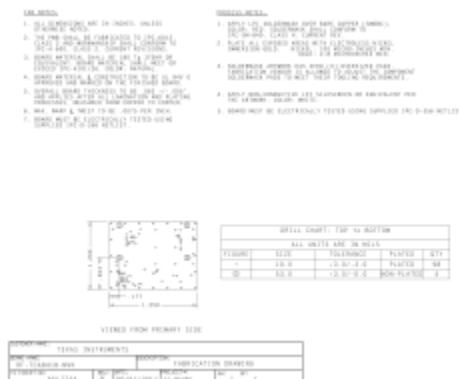


Figure 13. RF-TCA8418-MVK Fabrication Drawing

4.5 Request Gerber and Schematic files

To request Gerber or schematic files for the RF-TCA8418-MVK module, please visit the [MAVRK Gerber Request](#) webpage.

5 MAVRK Links

5.1 *I want more info on MAVRK*

[MAVRK Home Page](#)

5.2 *I have MAVRK Questions*

[MAVRK Forum](#) (Recommended):

5.3 *I want more Technical Info on MAVRK Hardware*

<ul style="list-style-type: none"> • Hardware Design Guide for MAVRK MCU Modules • Hardware Design Guide for MAVRK Modules • Hardware Design Guide for MAVRK PMU Charger Sub-Modules • Hardware Design Guide for MAVRK PMU DC/DC Sub-Modules 	<ul style="list-style-type: none"> • Hardware Design Guide for MAVRK PMU Gas Gauge Sub-Modules • Hardware Design Guide for MAVRK PMU High-Power DC/DC Sub-Modules • Hardware Design Guide for MAVRK SCI Modules • Hardware Design Guide for MAVRK SCI Sub-Modules 	<ul style="list-style-type: none"> • Hardware Design Guide for the uMAVRK Analog Interface • Hardware Design Guide for the uMAVRK Power Interface • Template - Hardware User's Guide
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5.4 *I want more Technical Info on MAVRK Software*

<ul style="list-style-type: none"> • How to Convert a Project from IAR to CCS 	<ul style="list-style-type: none"> • Software - CC11xx, CC25xx, CC430 Radio API Guide
--	--

5.5 *I want to get a MAVRK board*

[MAVRK Home Page](#)

6 Important Notices

6.1 ESD Precautions

The following guidelines should be followed in order to avoid ESD damage to the board components:

- Any person handling boards must be grounded either with a wrist strap or ESD protective footwear, used in conjunction with a conductive or static-dissipative floor or floor mat.
- The work surface where boards are placed for handling, processing, testing, etc., must be made of static-dissipative material and be grounded to ESD ground.
- All insulator materials either must be removed from the work area or they must be neutralized with an ionizer. Static-generating clothes should be covered with an ESD-protective smock.
- When boards are being stored, transferred between operations or workstations, or shipped, they must be maintained in a Faraday-shield container whose inside surface (touching the boards) is static dissipative.

6.2 Certifications

[FCC standard EMC test report for the RF-TCA8418-MVK MAVRK Module aboard a MAVRK Pro Motherboard](#)

[ICES standard EMC test report for the RF-TCA8418-MVK MAVRK Module aboard a MAVRK Pro Motherboard](#)

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6.5 Evaluation Board/Kit/Module (EVM) Warnings, Restrictions, and Disclaimers

6.5.1 Your Sole Responsibility and Risk

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1. You have unique knowledge concerning Federal, State and local regulatory requirements (including but not limited to Food and Drug Administration regulations, if applicable) which relate to your products and which relate to your use (and/or that of your employees, affiliates, contractors or designees) of the EVM for evaluation, testing and other purposes.
2. You have full and exclusive responsibility to assure the safety and compliance of your products with all such laws and other applicable regulatory requirements, and also to assure the safety of any activities to be conducted by you and/or your employees, affiliates, contractors or designees, using the EVM. Further, you are responsible to assure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard.
3. Since the EVM is not a completed product, it may not meet all applicable regulatory and safety compliance standards which may normally be associated with similar items. You assume full responsibility to determine and/or assure compliance with any such standards and related certifications as may be applicable. You will employ reasonable safeguards to ensure that your use of the EVM will not result in any property damage, injury or death, even if the EVM should fail to perform as described or expected.

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6.5.3 Agreement to Defend, Indemnify and Hold Harmless

You agree to defend, indemnify and hold TI, its licensors and their representatives harmless from and against any and all claims, damages, losses, expenses, costs and liabilities (collectively, "Claims") arising out of or in connection with any use of the EVM that is not in accordance with the terms of the agreement. This obligation shall apply whether Claims arise under law of tort or contract or any other legal theory, and even if the EVM fails to perform as described or expected.

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If you intend to evaluate the components for possible use in safety critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, such as devices which are classified as FDA Class III or similar classification, then you must specifically notify TI of such intent and enter into a separate Assurance and Indemnity Agreement.

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OMAP Mobile Processors	www.ti.com/omap
Wireless Connectivity	www.ti.com/wirelessconnectivity

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Industrial	www.ti.com/industrial
Medical	www.ti.com/medical
Security	www.ti.com/security
Space, Avionics and Defense	www.ti.com/space-avionics-defense
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