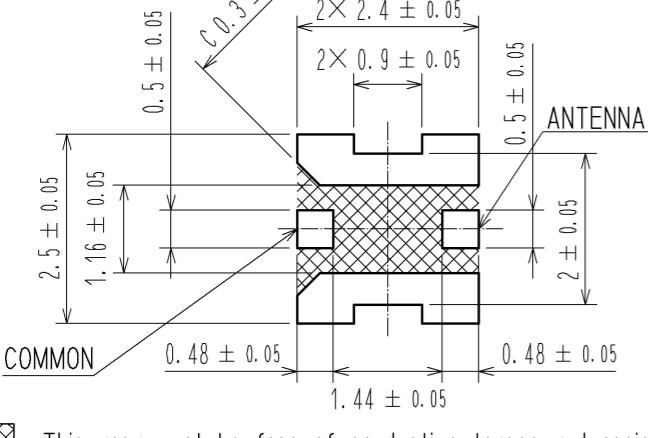
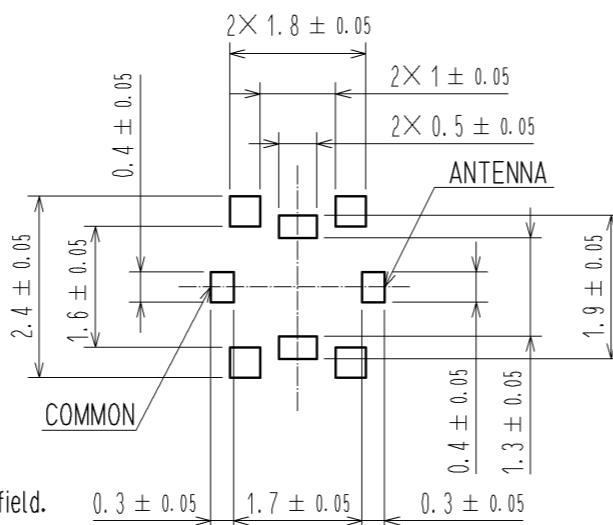


5 SPECIFIED PCB LAYOUT



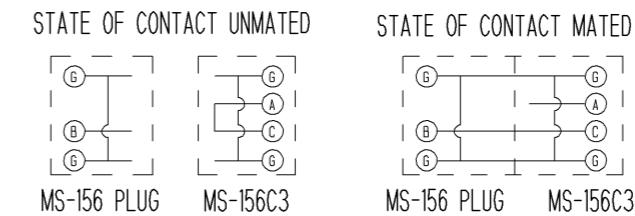
 :This area must be free of conductive traces and resist field.

5 SPECIFIED STENCIL PLATE



ld.

NOTE 1. Circuit shall be as follows.



2. For use of this product, be sure to put contact area of plug on position P perpendicularly.
 Lead co-planarity is to be 0.1mm MAX.
3. Do not use the washing process.
4. Please do not change the specified layout of the PCB dimension and the layout of the stencil plate dimension and thickness otherwise could not be responsible for the solderability of the product.
5. This product is intended to be used for circuit inspection only. Consult us if any other application is considered.
6. Do not use hand soldering for mounting of MS-156C3.

RoHS COMPLIANT

HALOGEN FREE COMPLIANT

3	CU-NI-SI-ALLOY	SELECTIVE GOLD PLATING	6	PET			
2	PA-M		5	PS			
1	PHOSPHOR BRONZE	GOLD PLATING 	4	PHOSPHOR BRONZE	SELECTIVE GOLD PLATING		
NO.	MATERIAL	FINISH, REMARKS	NO.	MATERIAL	FINISH, REMARKS		
UNITS mm	 	SCALE 10 : 1	COUNT 	DESCRIPTION OF REVISIONS 1 DIS-D-003384	DESIGNED DS. YAMAKOSHI	CHECKED NK. NINOMIYA	DATE 14.09.02
HIROSE ELECTRIC CO., LTD.	APPROVED : MH. YAMANE	13.11.29	DRAWING NO.	EDC3-350543-00			
	CHECED : NK. NINOMIYA	13.11.28	PART NO.	MS-156C3			
	DESIGNED : TS. NAKAGAWA	13.11.27	CODE NO.	CL358-0340-0-00			1 
	DRAWN : TS. NAKAGAWA	13.11.27					

