

優恩半導體 **UN Semiconductor®** 



# **Transient Voltage Suppressors for ESD Protection**

## ESD3.3V88D-ULC

#### Description

The ESD3.3V88D-ULC is ultra low capacitance TVS arrays designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from over-voltage caused by ESD (electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).



#### **Functional Diagram**

#### **Feature**

- ٠ 95 Watts Peak Pulse Power per Line (tp=8/20µs)
- ٠ Protects One Bidirectional I/O Line
- ٠ Low clamping voltage
- Working voltages : 3.3V ٠
- ٠ Low leakage current
- IEC61000-4-4 (EFT) 40A (5/50ns)
- ٠ IEC61000-4-5 (LIGHTING) 5A (8/20µs)
- ٠ IEC61000-4-2(ESD):±25kV (air discharge)
  - ±20kV (contact discharge)

#### Applications

- Cellular Handsets & Accessories
- Notebooks & Handhelds
- Portable Instrumentation
- LVDS Interfaces
- Peripherals

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- **Digital cameras**
- **CCD** Camera Lines
- Automobile Applications

#### Me

echanical Characteristics					
Symbol	Parameter	Value	Units		
P <sub>PP</sub>	Peak Pulse Power (tp=8/20µs waveform)	95	Watts		
TL	Lead Soldering Temperature	260 (10 sec.)	°C		
Т <sub>ѕтс</sub>	Storage Temperature Range	-55 to +150	°C		
TJ	Operating Junction Temperature Range	-40 to +125	°C		

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Revision May 19, 2021

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#### **Mechanical Data**

- SOD-882/DFN1006 (1.0x0.6x0.5mm) Package
- Molding Compound Flammability Rating : UL 94V-O
- Weight 0.5 Milligrams (Approximate)
- Lead Finish : Lead Free





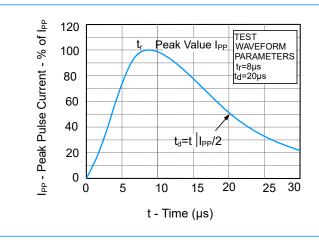
# Transient Voltage Suppressors for ESD Protection

## ESD3.3V88D-ULC

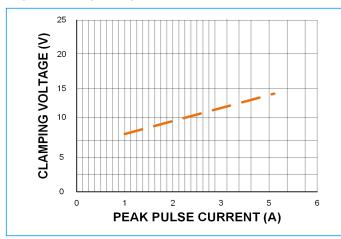
Electrical Characteristics (@ 25°C Unless Otherwise Specified )						
Characteristics	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Reverse Working Voltage	V <sub>RWM</sub>				3.3	V
Reverse Breakdown V <sub>BR</sub>		I <sub>T</sub> =1mA;	4.5			V
Reverse Leakage Current	I <sub>R</sub>	V <sub>RWM</sub> =3.3V, T=25°C;			0.1	μA
	y Vc	I <sub>PP</sub> =1A, T <sub>P</sub> =8/20μs;			10.0	V
Positive Clamping Voltage		I <sub>PP</sub> =3A, T <sub>P</sub> =8/20μs;			13.0	V
		I <sub>PP</sub> =5A, T <sub>P</sub> =8/20μs;			19.0	V
TLP Clamping Voltage	V <sub>CL</sub>	I <sub>PP</sub> =16A, T <sub>P</sub> =100ns;		19.6		V
Junction capacitance	С	V <sub>R</sub> = 0V, f = 1MHz;		0.25		pF

#### **Characteristic Curves**

#### Fig1. 8/20µs Pulse Waveform

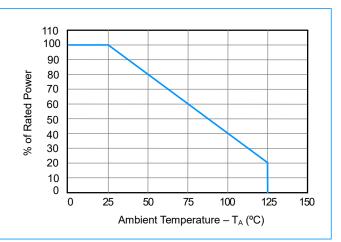


#### Fig3. Clamping Voltage vs. Peak Pulse Current

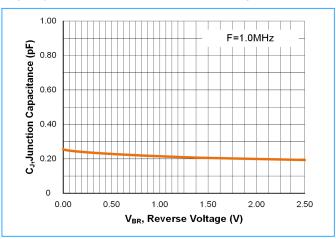


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#### Fig2. Power Derating Curve







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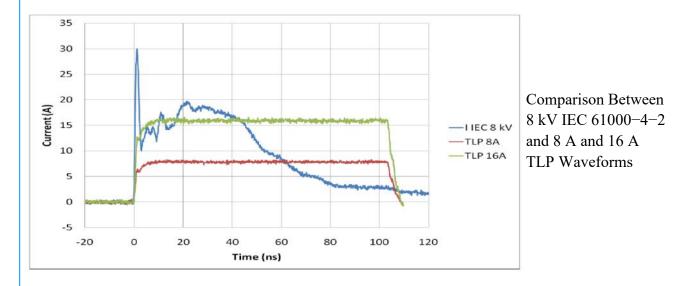


# **Transient Voltage Suppressors for ESD Protection**

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#### Transmission Line Pulse (TLP)

Transmission Line Pulse (TLP) is a measurement technique used in the Electrostatic Discharge (ESD) arena to characterize performance attributes of devices under ESD stresses. TLP is able to obtain current versus voltage (I–V) curves in which each data point is obtained with a 100ns long pulse, with currents up to 40 A. TLP was first used in the ESD field to study human body model (HBM) in integrated circuits, but it is an equally valid tool in the field of system level ESD. The applicability of TLP to system level ESD is illustrated in Figure 1, which compares an 8 KV IEC 61000–4–2 current waveform with TLP current pulses of 8 and 16 A. The current levels and time duration for the pulses are similar and the initial rise time for the TLP pulse is comparable to the rise time of the IEC 61000–4–2's initial current spike. This application note will give a basic introduction to TLP measurements and explain the data sheet parameters extracted from TLP for SDI Technology's protection products.



Comparison of a Current Waveform of IEC 61000-4-2 with TLP Pulses at 8 and 16 A.

The IEC 61000-4-2 ESD waveforms is true to the Standard and is shown here as captured on an oscilloscope. The points A, B, and C show the points on the waveforms specified in IEC 61000-4-2. Transmission Line Pulse (TLP) Version.

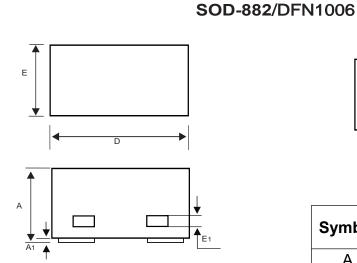




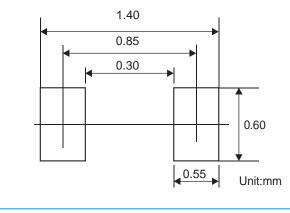
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## ESD3.3V88D-ULC

#### SOD-882/DFN1006 Package Outline & Dimensions



#### Suggested PAD Layout



-	е	-	_
			b
			•
<b>↓</b>	<b>↓</b>		

Symbol	Millimeters			
Symbol	Min	Nom	Max	
A	0.450	0.500	0.550	
A1	0	0.020	0.050	
E1	0.013	0.063	0.113	
D	0.900	1.000	1.100	
E	0.500	0.600	0.700	
е	0.65BSC			
L	0.150	0.250	0.350	
b	0.400	0.500	0.600	
L1	0.300	0.400	0.500	

#### **Ordering Information**

Device	Marking	Package	Quantity	Reel Size
ESD3.3V88D-ULC	HD	SOD-882/DFN1006	10,000pcs/Reel	7 inch