

General Description

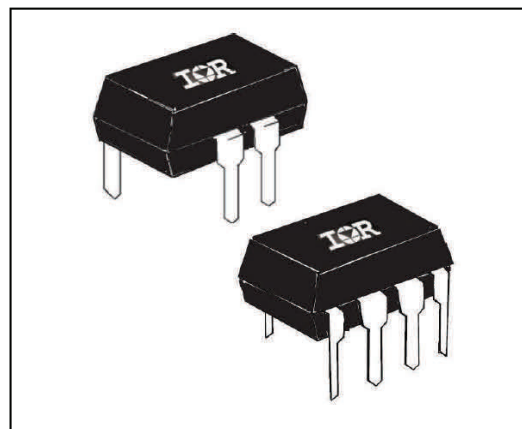
The PVI Series Photovoltaic Isolator generates an electrically isolated DC voltage upon receipt of a DC input signal. It is capable of directly driving gates of power MOSFETs or IGBTs. It utilizes a monolithic integrated circuit photovoltaic generator of novel construction as its output. The output is controlled by radiation from a GaAlAs light emitting diode (LED), which is optically isolated from the photovoltaic generator.

The PVI Series is ideally suited for applications requiring high-current and/or high-voltage switching with optical isolation between the low-level driving circuitry and high-energy or high-voltage load circuits. It can be used for directly driving gates of power MOSFETs. The dual-channel device allows its outputs to drive independent discrete power MOSFETs, or be connected in parallel or in series to provide higher current drive for power MOSFETs or higher voltage drive for IGBTs. The PVI Series Photovoltaic isolators employ fast turn-off circuitry.

These PVI Series Photovoltaic Isolators are packaged in 8-pin, molded DIP packages and available with either thru-hole or surface-mount ("gull-wing") leads, in plastic shipping tubes.

Features

- Isolated Voltage Source
- Monolithic Construction
- Up to 5 μ A Output
- Single or Dual Output
- Solid-State Reliability



Applications

- Load Distribution
- Industrial Controls
- Current-to-Voltage Conversion
- Custom Solid-State Relay

Part Identification

PVI1050NPbF	thru-hole
PVI5050NPbF	thru-hole
PVI1050NSPbF	Surface-mount (gull-wing)
PVI5050NSPbF	Surface-mount (gull-wing)
PVI1050NS-TPbF	Surface-mount, tape and reel

Electrical Specifications ($-40^{\circ}\text{C} \leq T_A \leq +85^{\circ}\text{C}$ unless otherwise specified)

INPUT CHARACTERISTICS	Limits	Units
Input Current Range (see figure 4)	2.0 to 50	mA _(DC)
Maximum Forward Voltage Drop @ 10mA, 25°C (see figure 5)	1.4	V _(DC)
Maximum Reverse Voltage	6.0	V _(DC)
Maximum Reverse Current @ -6.0V (DC), 25°C	100	μA _(DC)
Maximum Pulsed Input Current @ 25°C (see figure 6)	1.0	A _(peak)

OUTPUT CHARACTERISTICS	Limits	Units
Maximum Forward Voltage @ 10μA	8.0 per channel	V _(DC)
Maximum Reverse Current @ -10V _{DC}	10	μA _(DC)

COUPLED CHARACTERISTICS	Limits PVI5050N	Limits PVI1050N	Units
Minimum Open Circuit Voltage @ ILED = 10mA, 25°C, RL = >10MΩ (see figures 1 to 2)	5.0	5.0/channel 10 series	V _(DC)
Minimum Short Circuit Current @ ILED = 10mA, 25°C (see figures 1 to 2)	5.0	5.0/channel 10 series	μA _(DC)
Maximum Capacitance (Input/Output)	1.0	2.0	pF
Maximum Ton Time @ ILED=10mA, CLOAD=10pF (See Figure7) RL > 20MΩ RL=10MΩ RL=4.7MΩ	300		μS
	160		μS
	90		μS
Maximum Toff Time @ ILED=10mA, CLOAD=10pF (See Figure7)	220		μS

GENERAL CHARACTERISTICS	Limits PVI5050N	Limits PVI1050N	Units
Minimum Dielectric Strength, Input-Output	4000	2500	V _{RMS}
Minimum Dielectric Strength, Output-to-Output	1200		V _{DC}
Minimum Insulation Resistance, Input-to-Output, @T _A =+25°C, 50%RH, 100V _{DC}	10 ¹²		Ω
Maximum Pin Soldering Temperature (10 seconds maximum)	+260		°C
Ambient Temperature Range:			

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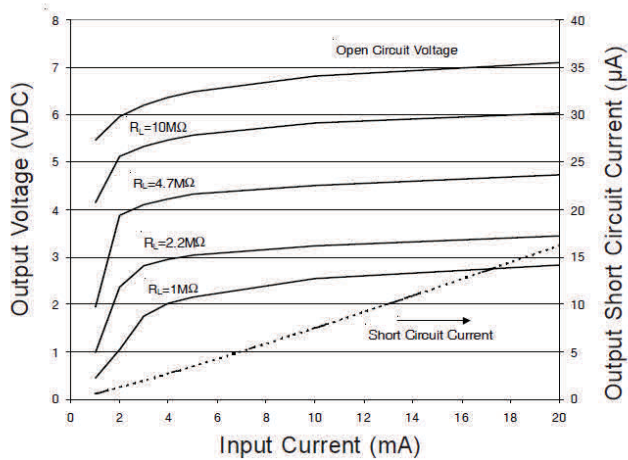


Figure 1. Typical Output Characteristics

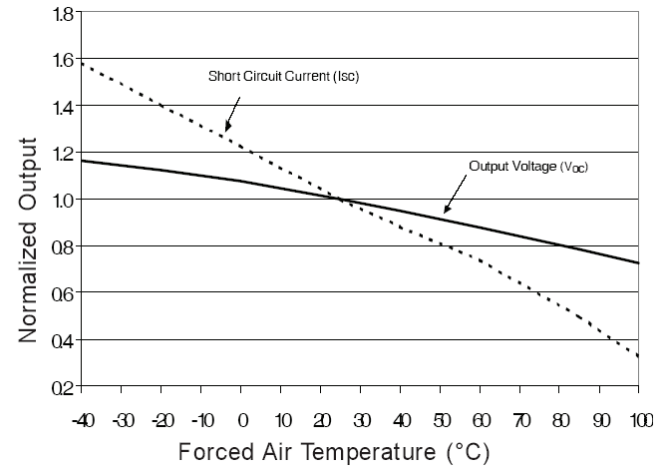


Figure 2. Typical Variation of Output

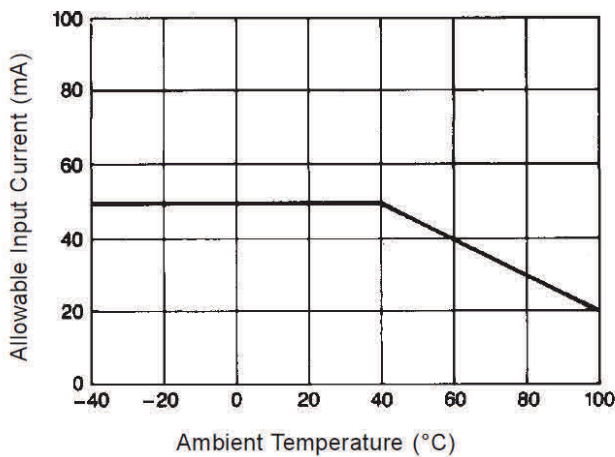


Figure 3. Input Current Derating

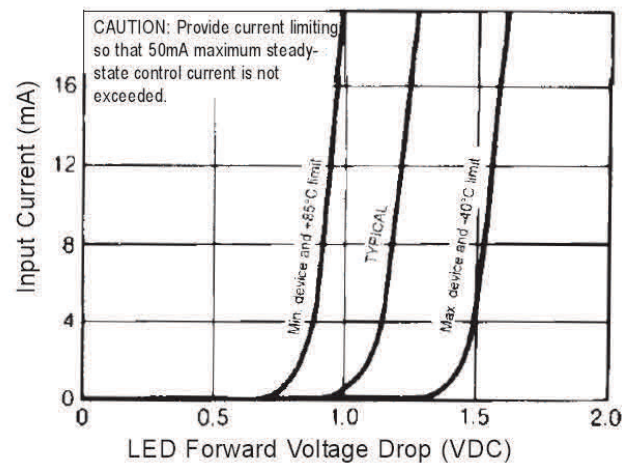


Figure 4. Input Characteristics

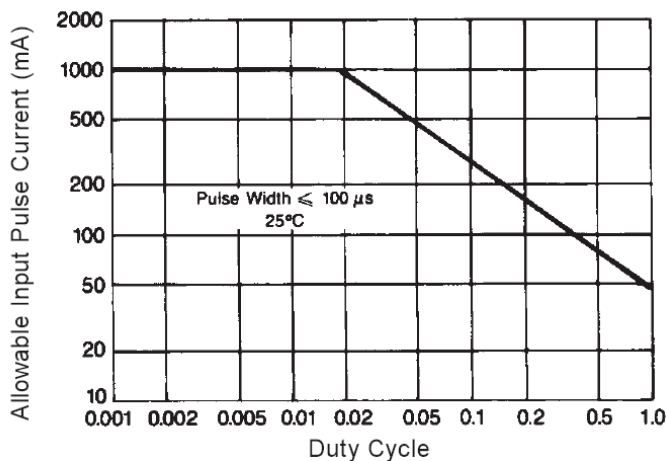


Figure 5. Input Pulse Capability

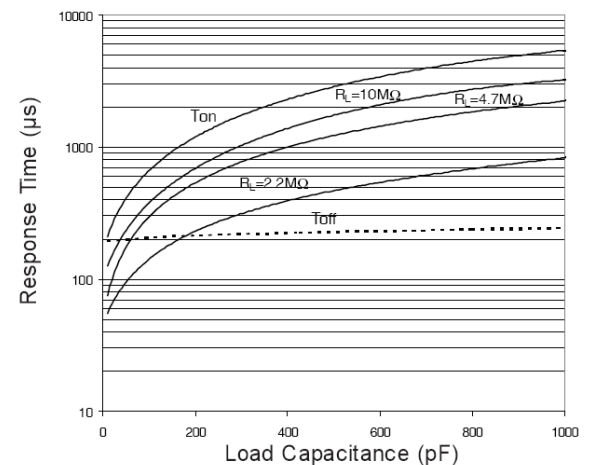
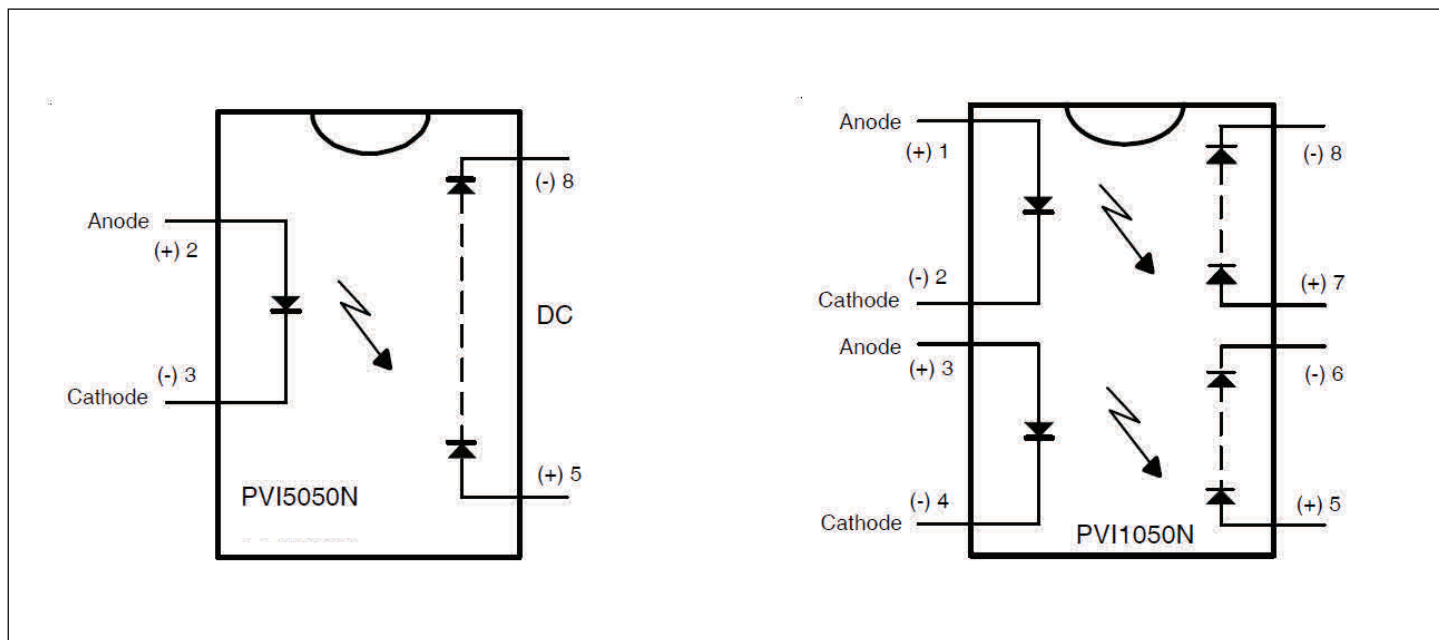


Figure 6. Typical Response Time

Wiring Diagram



Application Note:

The outputs of the PVI1050N (pins 5-6 and 7-8) may be placed in series connection to produce a 10-volt output with a 5 μ A minimum short circuit current. Alternatively, the two outputs of the PVI1050 may be connected in parallel to produce a 5.0-volt output with a 10 μ A minimum short circuit current.

The two outputs of the PVI1050N may be applied separately with a maximum 1200VDC between the outputs. Input-to-output isolation to either output is 2500V (RMS).

Technical drawing of the PVI1050N connector showing top, side, and detail views with dimensions in millimeters and inches.

Top View Dimensions:

- Overall width: 9.39 [.370] MAX.
- Pin pitch (between pins 1-4): 1.65 [.065]
- Pin pitch (between pins 5-8): 1.40 [.055]
- Pin 1 to centerline: 1.14 [.045]
- Pin 2 to centerline: 0.89 [.035]
- Pin 3 to centerline: 0.55 [.022]
- Pin 4 to centerline: 0.46 [.018]
- Pin 5 to centerline: 3.93 [.155]
- Pin 6 to centerline: 3.69 [.145]
- Pin 7 to centerline: 0.63 [.025]
- Pin 8 to centerline: 0.39 [.015]
- Pin 1 to pin 8: 6.47 [.255]
- Pin 1 to pin 5: 6.23 [.245]

Side View Dimensions:

- Overall height: 7.87 [.310]
- Base height: 7.37 [.290]
- Pin height: 9.65 [.380]
- Pin base height: 8.13 [.320]
- Pin tip height: 0.304 [.012]
- Pin base tip height: 0.204 [.008]

Detail View Dimensions:

- Pin diameter: 2.54 [.100]
- Pin length: 3.42 [.135]
- Pin base length: 3.18 [.125]
- Pin base diameter: 1.27 [.050]
- Pin base wall thickness: 0.25 [.010]
- Pin base fillet radius: 0.25 [.010]
- Pin base chamfer: 0.25 [.010]
- Pin base chamfer angle: 45°

Notes:

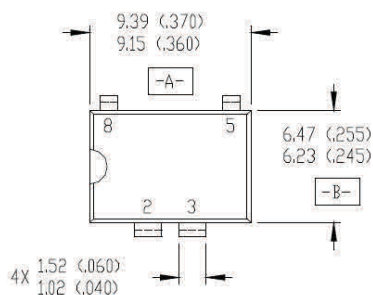
1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
4. DIMENSION DOES NOT INCLUDE MOLD PROTRUSIONS. MOLD PROTRUSIONS SHALL NOT EXCEED 0.25 [.010].
5. PRODUCTS AFFECTED: PVI1050N, PVT322, PVT322A, PVT422, PVI5013R AND PVI5033R.

PVI1050N

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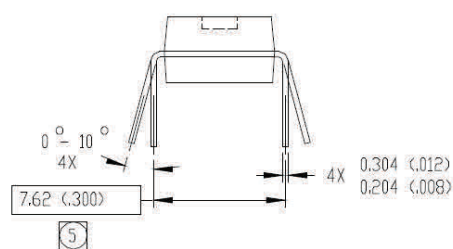
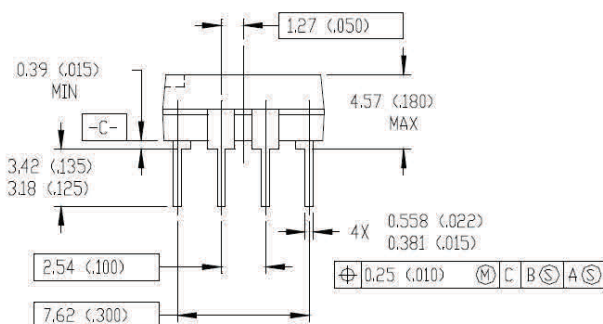


Case Outlines



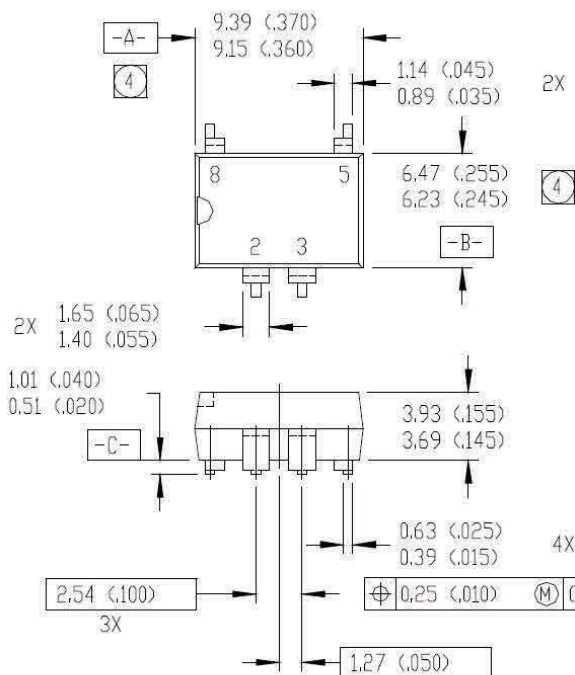
NOTES:

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4. OUTLINE CONFORMS TO JEDEC OUTLINE MS-001AB.
5. MEASURED WITH THE LEADS CONSTRAINED TO BE PERPENDICULAR TO DATUM PLANE C.



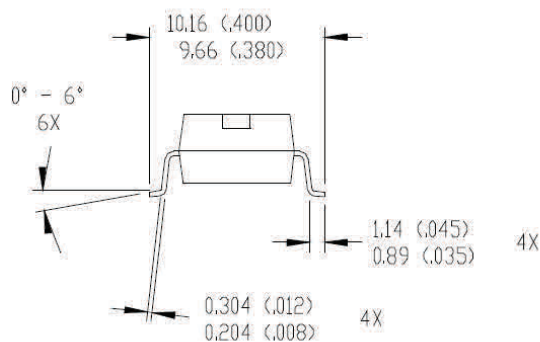
PVI5050N

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PVI5050NS

01-2019 00

Qualification Information

Qualification Level	Industrial (per JEDEC JESD47F [†] guidelines)	
Moisture Sensitivity Level	PVI1050NPbF	N/A
	PVI5050NPbF	
	PVI1050NSPbF	MSL4 (per JEDEC J-STD-020E & JEDEC J-STD-033C) [†]
	PVI5050NSPbF	
	PVI1050NS-TPbF	
RoHS Compliant	Yes	

† Applicable version of JEDEC standard at the time of product release.

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