

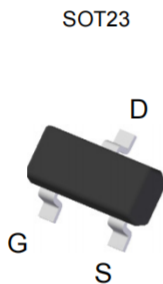
General Features

- $V_{DS} = 100V, I_D = 3.5A$
- $R_{DS(ON)} < 130m\Omega @ V_{GS}=10V$ (96m Ω Typ)
- $R_{DS(ON)} < 180m\Omega @ V_{GS}=4.5V$ (140m Ω Typ)

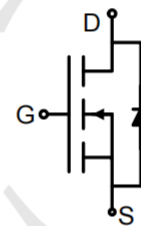
Application

- Load/Power Switching
- Interfacing Switching
- Battery Management for Ultra Small Portable
- Logic Level Shift

Package and Pin Configuration



Circuit diagram



Marking:



“P” is TECHPUBLIC LOGO
 “3N” is Part number, fixed
 “xx” is internal code

Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous	I_D	3.5	A
Drain Current-Pulsed ^(Note 1)	I_{DM}	20	A
Maximum Power Dissipation	P_D	1.5	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 To 175	$^\circ C$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient ^(Note 2)	$R_{\theta JA}$	100	$^\circ C/W$
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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.0	1.5	2.0	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =3A	-	96	130	mΩ
		V _{GS} =4.5V, I _D =1A	-	140	180	
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =3A	-	5	-	S
Dynamic Characteristics (Note 4)						
Input Capacitance	C _{iss}	V _{DS} =50V, V _{GS} =0V, F=1.0MHz	-	650	-	PF
Output Capacitance	C _{oss}		-	24	-	PF
Reverse Transfer Capacitance	C _{rss}		-	20	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}	V _{DD} =50V, R _L =19Ω V _{GS} =10V, R _G =3Ω	-	6	-	nS
Turn-on Rise Time	t _r		-	4	-	nS
Turn-Off Delay Time	t _{d(off)}		-	20	-	nS
Turn-Off Fall Time	t _f		-	4	-	nS
Total Gate Charge	Q _g	V _{DS} =50V, I _D =3A, V _{GS} =10V	-	20	-	nC
Gate-Source Charge	Q _{gs}		-	2.1	-	nC
Gate-Drain Charge	Q _{gd}		-	3.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V, I _S =3A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S			3.5		A

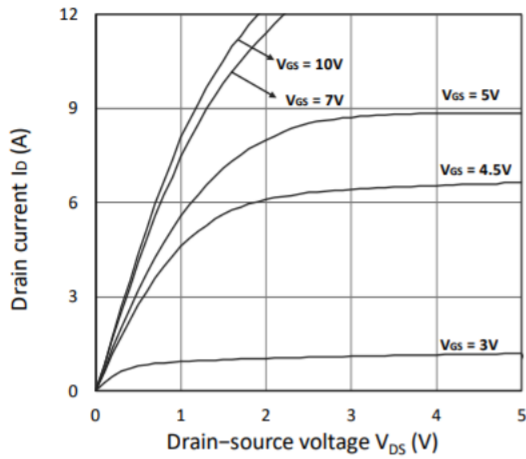


Figure 1. Output Characteristics

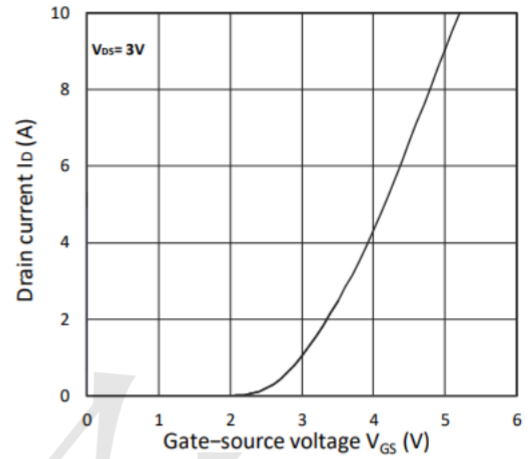


Figure 2. Transfer Characteristics

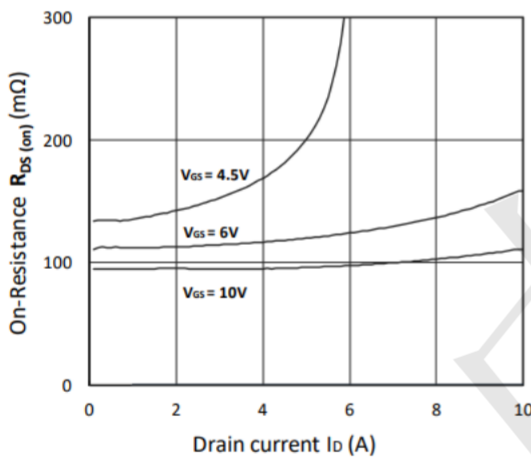


Figure 3. $R_{DS(ON)}$ VS. I_D

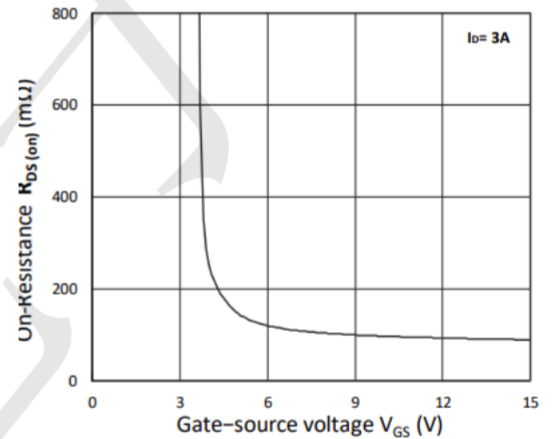


Figure 4. $R_{DS(ON)}$ VS. V_{GS}

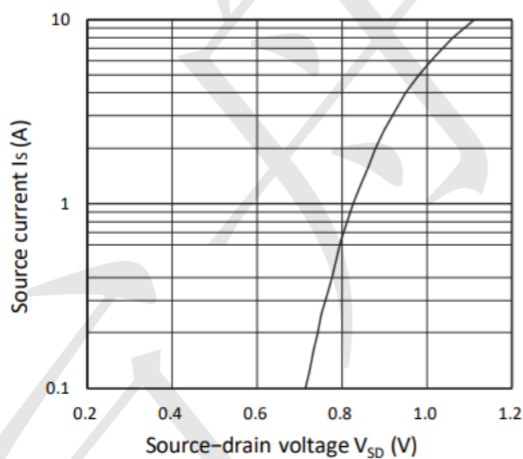


Figure 5. I_S VS. V_{SD}

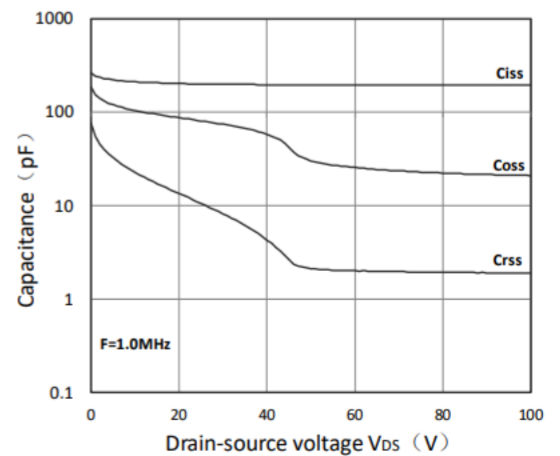
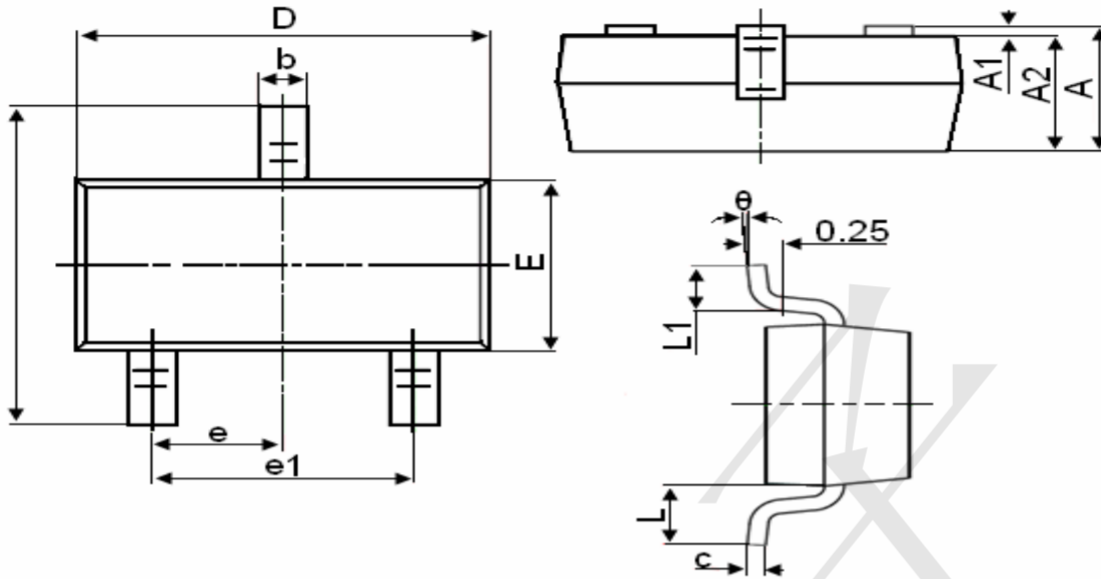


Figure 6. Capacitance Characteristics

Package Outline Dimensions (SOT-23)



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°