



UPG25N120

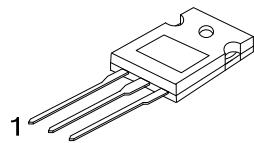
Insulated Gate Bipolar Transistor

1200V, SMPS N-CHANNEL IGBT

■ DESCRIPTION

The UTC **UPG25N120** is a N-channel IGBT. It uses UTC's advanced technology to provide customers with high input impedance, high switching speed and low conduction loss, etc.

The UTC **UPG25N120** is suitable for high voltage switching, high frequency switch mode power supplies.

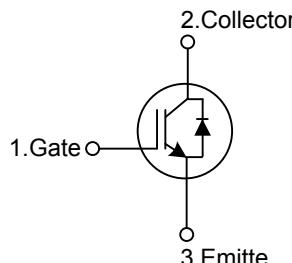


TO-247

■ FEATURES

- * $V_{CE(SAT)} \leq 2.8V$ @ $I_C=25A$, $V_{GE}=15V$
- * 1200V Switching SOA Capability
- * High switching speed
- * High input impedance
- * Low conduction loss

■ SYMBOL



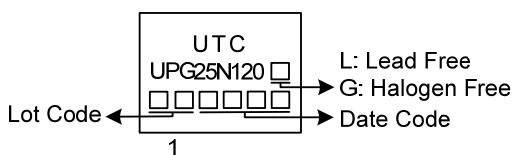
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
UPG25N120L-T47-T	UPG25N120G-T47-T	TO-247	G	C	E	Tube

Note: Pin Assignment: G: Gate C: Collector E: Emitter

UPG25N120G-T47-T	(1)Packing Type (2)Package Type (3)Green Package	(1) T: Tube (2) T47: TO-247 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Emitter Voltage	V_{CES}	1200	V
Gate to Emitter Voltage Continuous	V_{GES}	± 20	V
Continuous Collector Current	I_C	50	A
		25	A
Collector Current Pulsed (Note 2)	I_{CM}	100	A
Power Dissipation	P_D	350	W
Junction Temperature	T_J	-55 ~ +150	$^\circ\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Case	θ_{JC}	0.35	$^\circ\text{C/W}$

■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Emitter Breakdown Voltage	BV_{CES}	$I_C=250\mu\text{A}, V_{GE}=0\text{V}$	1200			V
Collector-Emitter Leakage Current	I_{CES}	$V_{CE}=1200\text{V}, V_{GE}=0\text{V}$			200	μA
Collector-Emitter Saturation Voltage	$V_{CE(\text{SAT})}$	$I_C=25\text{A}, V_{GE}=15\text{V}$	$T_J=25^\circ\text{C}$	2.3	2.8	V
			$T_J=150^\circ\text{C}$	2.65		V
Gate to Emitter Threshold Voltage	$V_{GE(\text{TH})}$	$I_C=250\mu\text{A}, V_{CE}=V_{GE}$	4.0		6.5	V
Gate to Emitter Leakage Current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=15\text{V}$			± 400	nA
Input Capacitance	C_{IES}	$V_{CE}=25\text{V}, V_{GE}=0\text{V}, f=1\text{MHz}$	1600			pF
Output Capacitance	C_{OES}		180			pF
Reverse Transfer Capacitance	C_{RES}		50			pF
Total Gate Charge	Q_G	$I_C=25\text{A}, V_{CE}=100\text{V}, V_{GE}=15\text{V}$	126			nC
Gate-Emitter Charge	Q_{GE}		27			nC
Gate-Collector Charge	Q_{GC}		45			nC
Current Turn-On Delay Time	$t_{D(\text{ON})}$	$I_C=25\text{A}, V_{CE}=100\text{V}, V_{GE}=15\text{V}, R_G=10\Omega$	16			ns
Current Rise Time	t_R		27			ns
Current Turn-Off Delay Time	$t_{D(\text{OFF})}$		92			ns
Current Fall Time	t_F		75			ns

SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS

Forward Voltage Drop	V_{FM}	$I_F=25\text{A}$		2.1		V
Reverse Recovery Time	t_{rr}	$I_F=25\text{A}, dI/dt=100\text{A}/\mu\text{s}$		210		ns
Reverse Recovery Charge	Q_{rr}			0.35		μC

Note: Pulse Test: Pulse width $\leq 50\mu\text{s}$.

■ TEST CIRCUIT AND WAVEFORMS

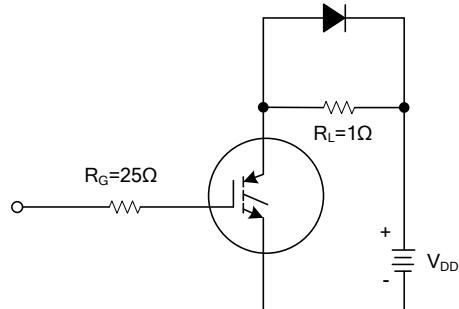


Fig 1. INDUCTIVE SWITCHING TEST CIRCUIT

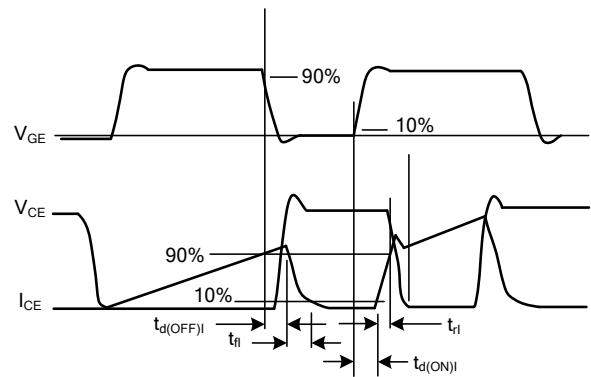
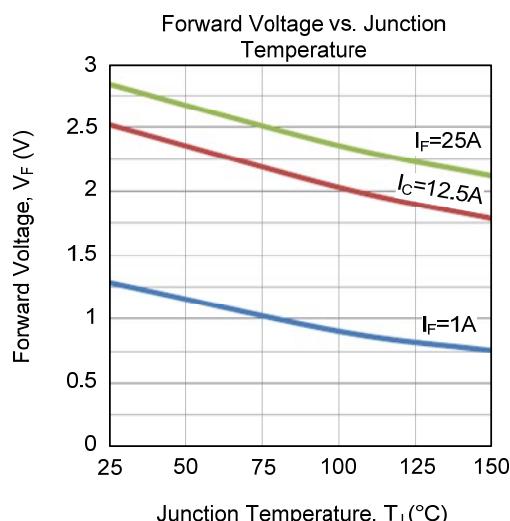
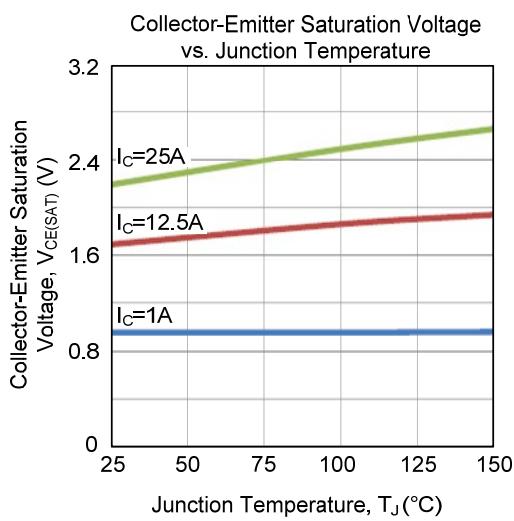
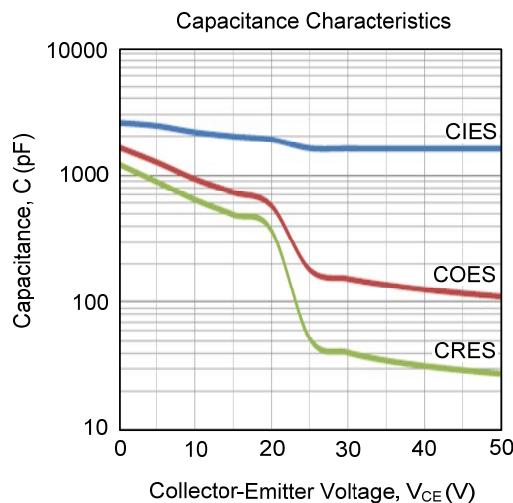
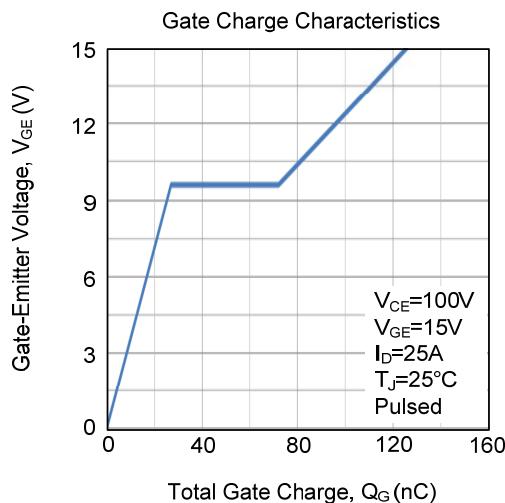
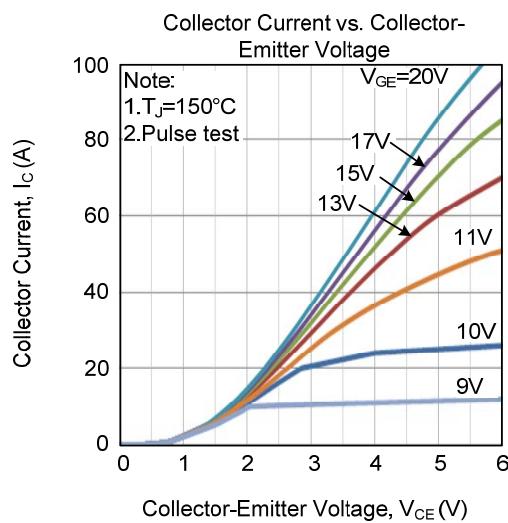
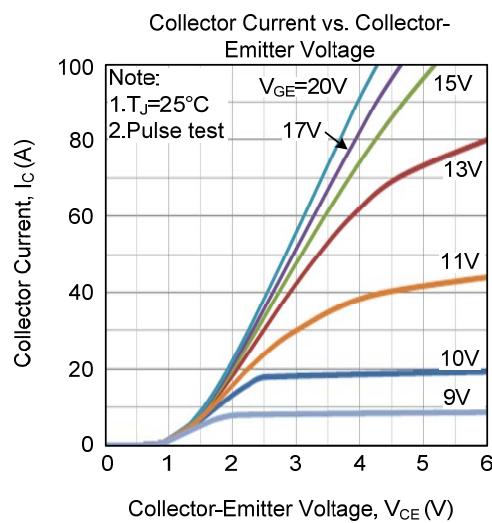
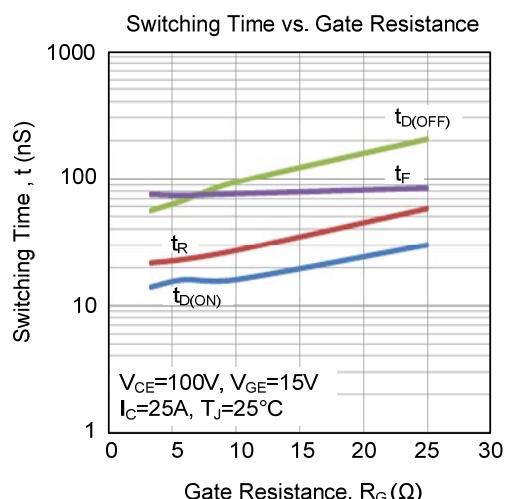
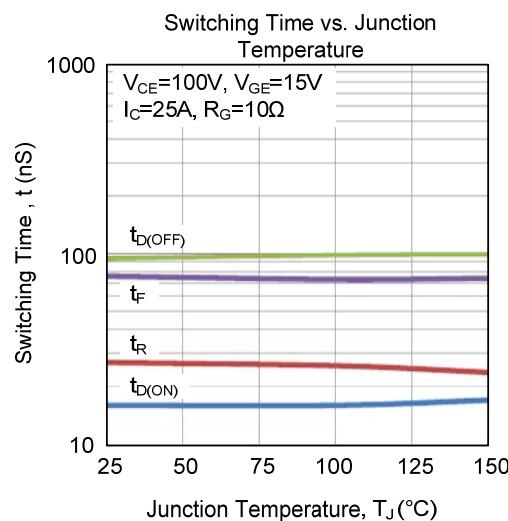
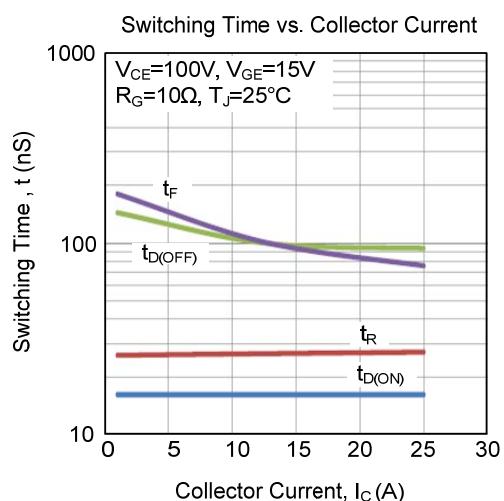
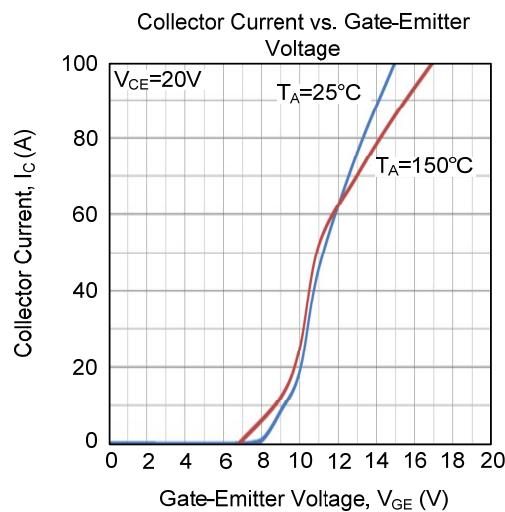
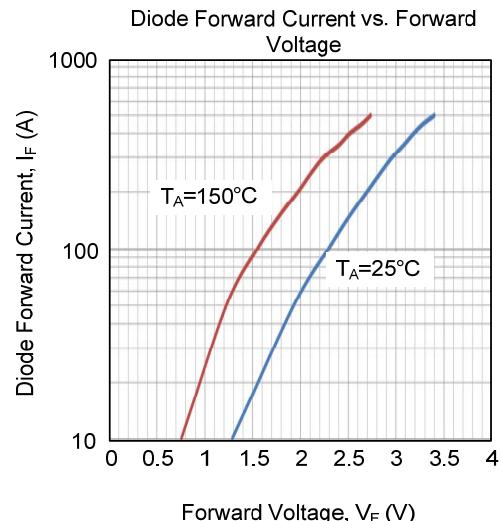
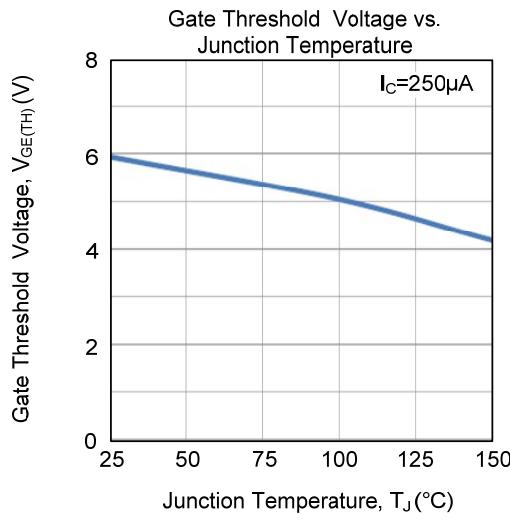


Fig 2. SWITCHING TEST WAVEFORMS

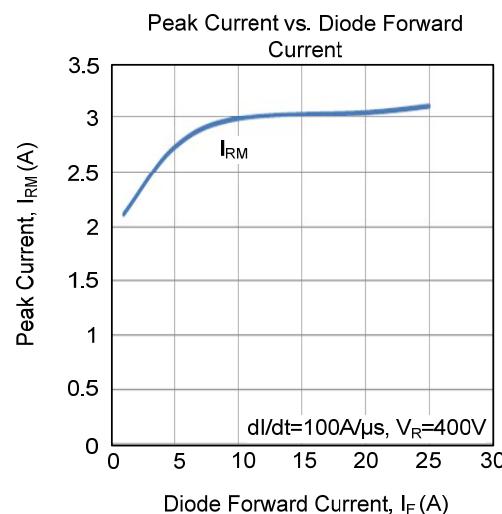
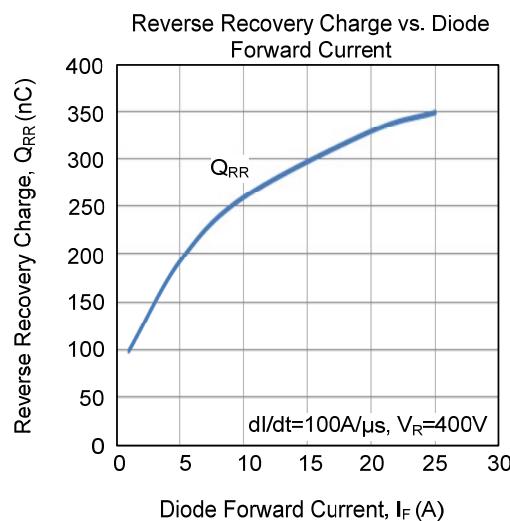
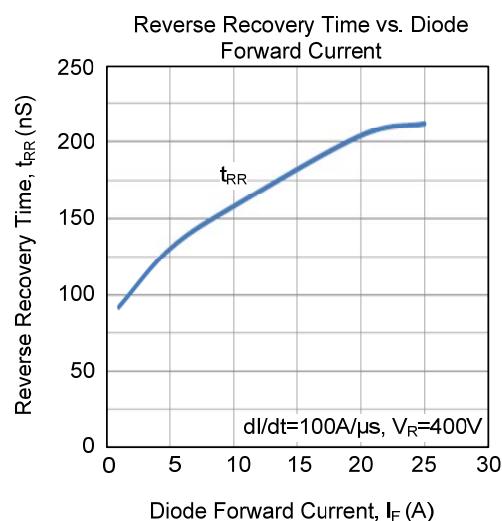
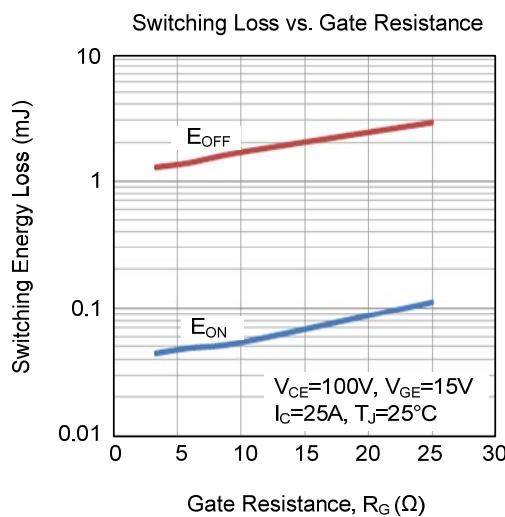
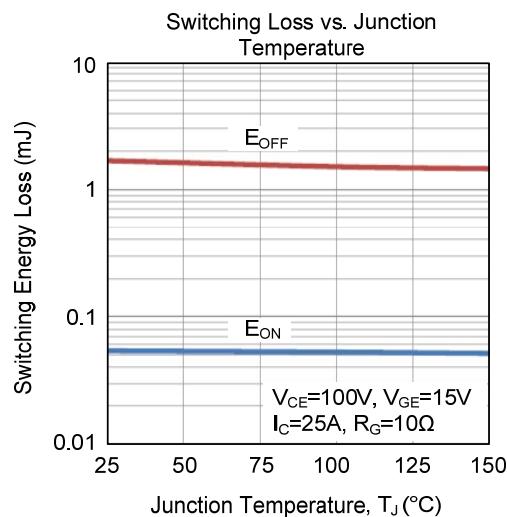
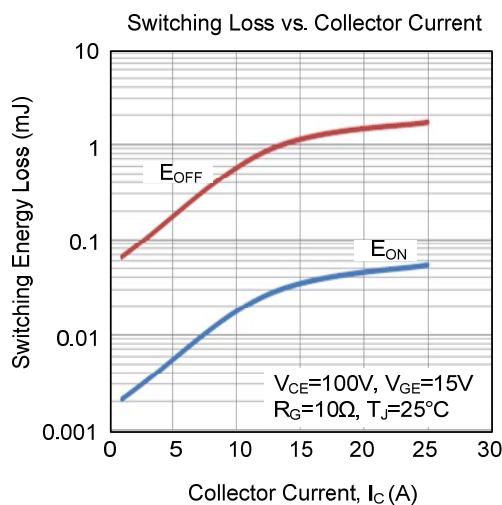
■ TYPICAL CHARACTERISTICS



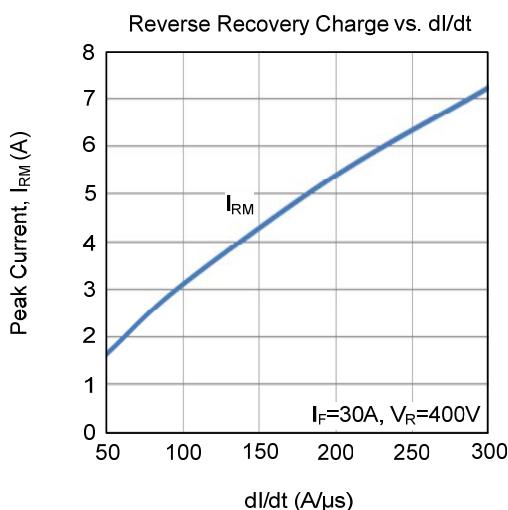
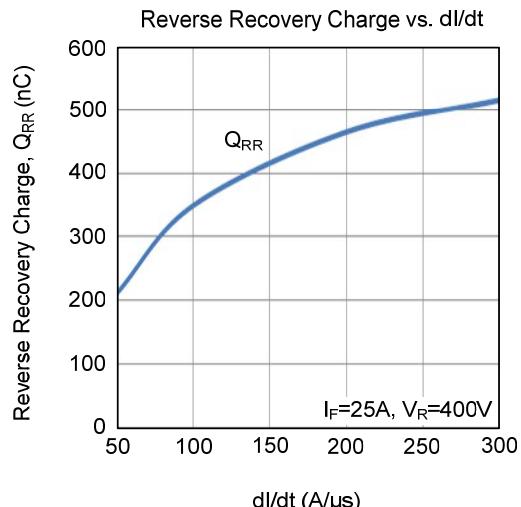
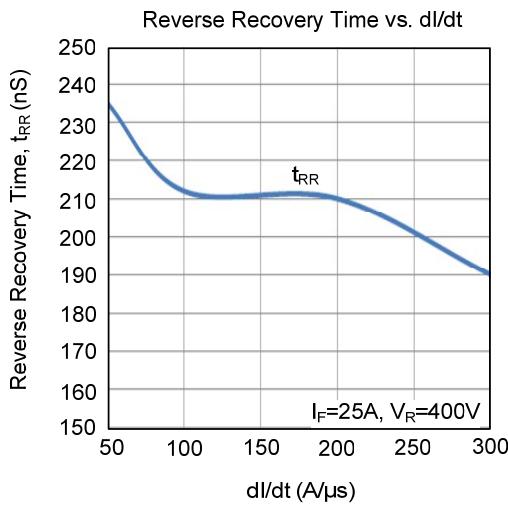
■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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