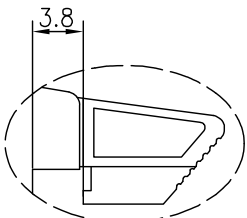
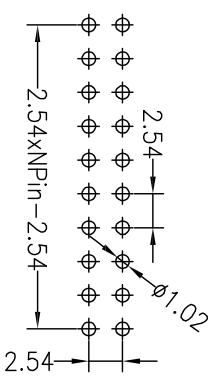


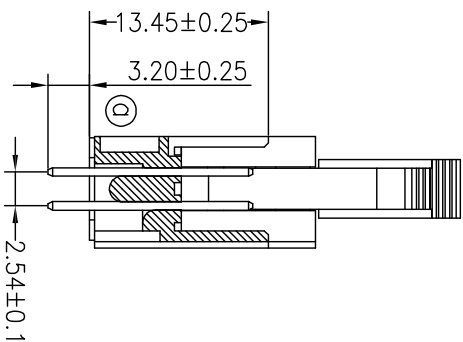
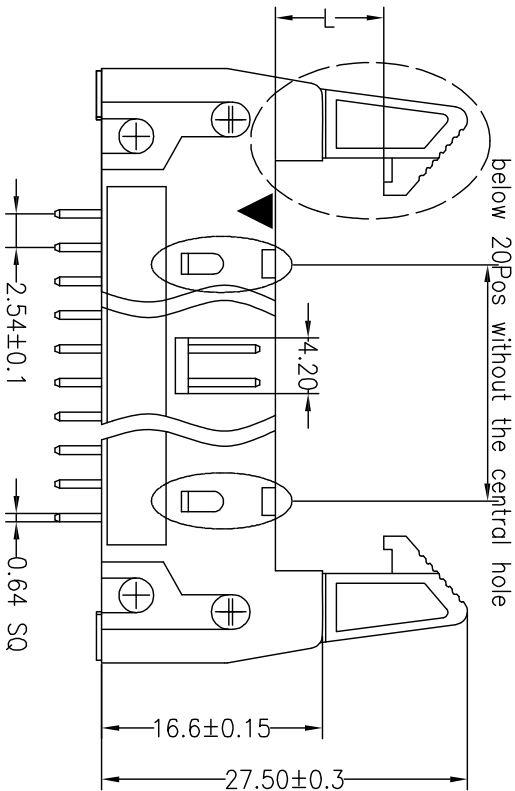
Long Latch



Short Latch



Recommended P.C.B Layout(Top Side)
(PCB BOARD TOLERANCE±0.05)



SPECIFICATIONS

Rated Current:3.0AMP

Contact Resistance:20mΩ Max

Withstand Voltage:1000V AC/DC

Insulation Resistance:1000MΩ Min

Operation Temperature:—40°C to +105°C

Contact Material:Brass

Contact Plating:Au or Sn Over Ni

Insulator Material:Polyester(UL94V-0)

Standard: PBT

Max.Processing Temp: 180°C for 30—60 seconds
(200°C for 10 seconds)

MODEL: 2.54—DC2SG—2×nP	DRAWN BY : <i>Leiqin</i>	DATE:	GENERAL TOLERANCE: (UNLESS SPECIFIED)
TITLE : 牛角插座耳扣直插	CHECKED BY : <i>Liwei</i>	DATE:	X. ±0.30mm X.* ± 3°
DRAWING NO:	REV. : A	DATE:	X.X ±0.25mm X.X* ± 2°
SHEET: 1 OF 1	SCALE: 3:1	DATE:	X.XX ±0.20mm X.XX* ± 1°
	SIZE: A4	DATE:	X.XXX±0.10mm X.XXX ± 0.5°

MEASURE POINT:

QUADRANT POINT

TANGENCY POINT

INTERSECTION POINT



深圳市吉威电子有限公司
SZJUSTWELL ELECTRONICS CO., LTD

NO:	PARTICULAR	BY	DATE
REVISIONS			
1			
2			
3			
4			
5			
6			
7			
8			