

## Feature

- Split Gate Trench Technology
- Low RDS(ON)
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

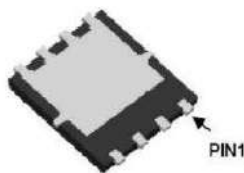
## Application

- Power Switching Application

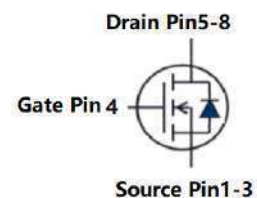
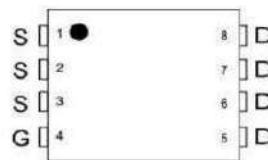


## Product Summary

$V_{DS}$	40	V
$R_{DS(on), Typ} @ V_{GS}=10\text{ V}$	6.5	mΩ
$I_D$	36	A



DFN5\*6-8



N-Channel

## ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

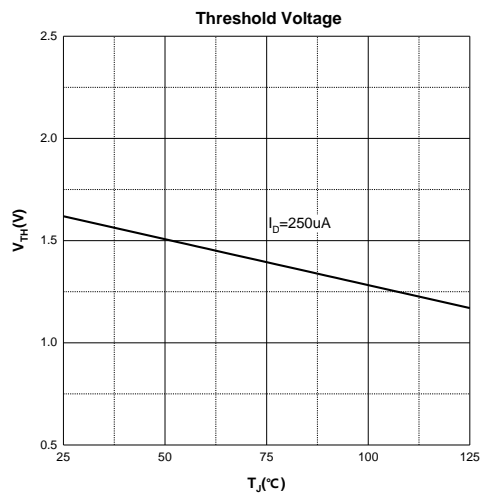
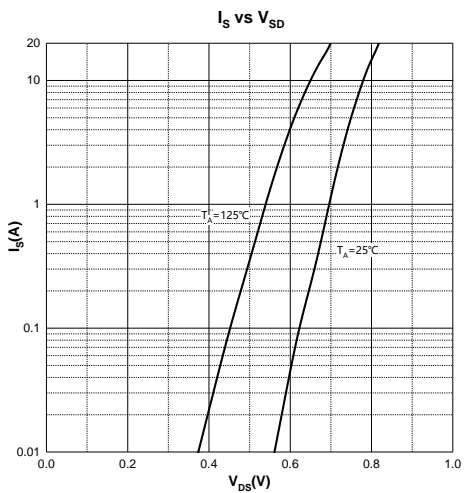
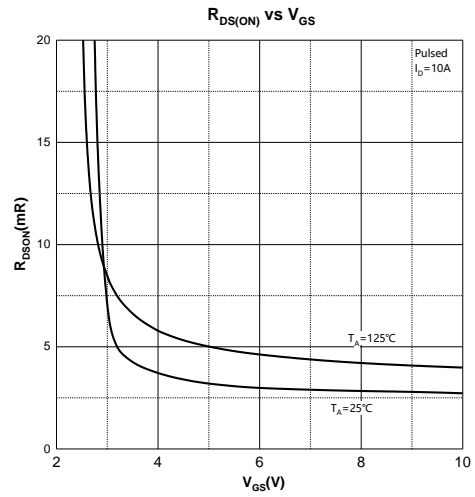
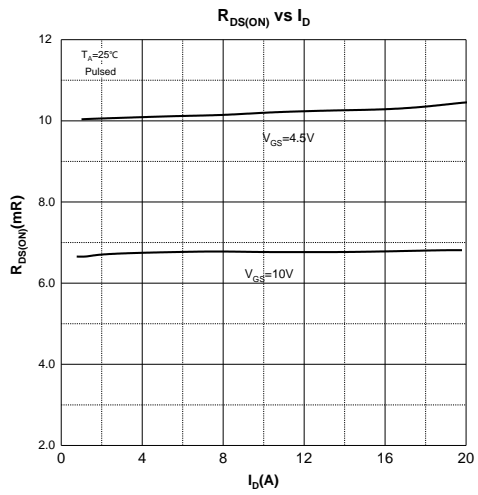
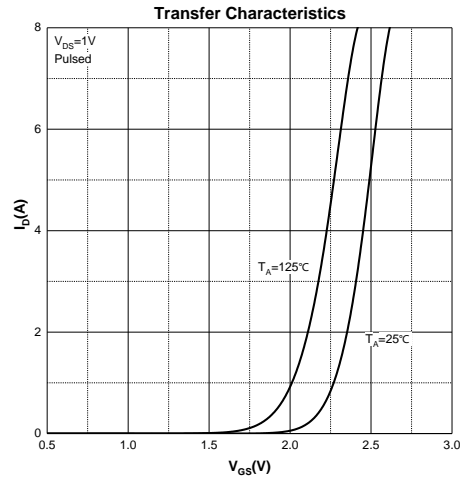
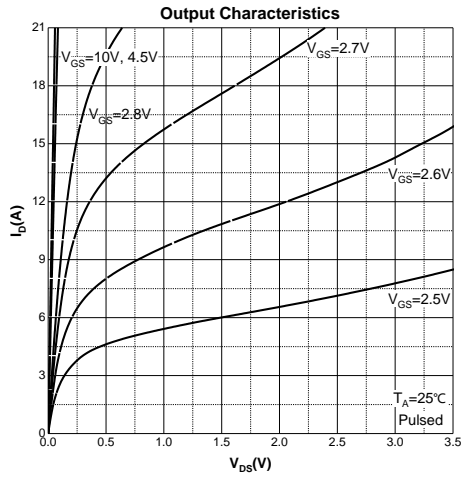
Parameter	Symbol	Value	Unit	
Drain - Source Voltage	$V_{DS}$	40	V	
Gate - Source Voltage	$V_{GS}$	$\pm 20$	V	
Continuous Drain Current <sup>1</sup>	$I_D$	$T_C = 25^\circ\text{C}$	36	A
Continuous Drain Current <sup>1</sup>		$T_C = 100^\circ\text{C}$	21	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	144	A	
Single Pulsed Avalanche Current <sup>3</sup>	$I_{AS}$	31	A	
Single Pulsed Avalanche Energy <sup>3</sup>	$E_{AS}$	240	mJ	
Power Dissipation <sup>5</sup>	$P_D$	$T_C = 25^\circ\text{C}$	83	W
Thermal Resistance from Junction to Ambient <sup>6</sup>		$R_{\theta JA}$	62	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	2.4	$^\circ\text{C/W}$	
Junction Temperature	$T_J$	150	$^\circ\text{C}$	
Storage Temperature	$T_{STG}$	-55~ +150	$^\circ\text{C}$	

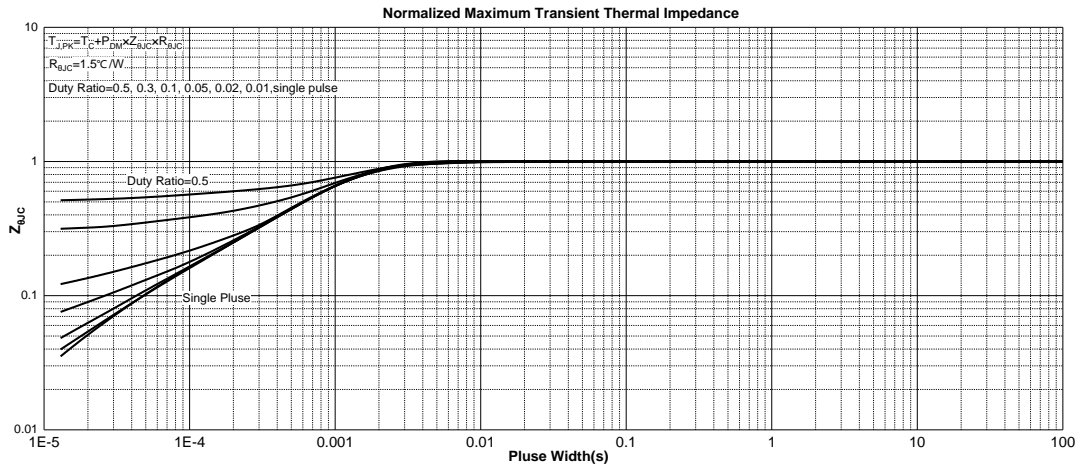
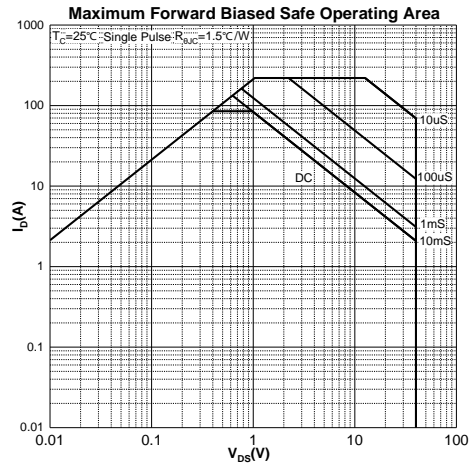
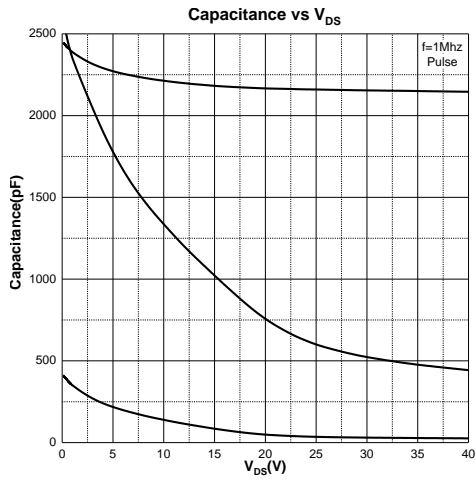
**MOSFET ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	40			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V			1	μA
Gate - Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
<b>On Characteristics<sup>4</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1.0	1.7	2.5	V
Drain-source On-resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 10A		6.5	8.0	mΩ
		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 10A		10.5	13	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = 10V, I <sub>D</sub> = 10A		21		S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, f = 1MHz		798		pF
Output Capacitance	C <sub>oss</sub>			289		
Reverse Transfer Capacitance	C <sub>rss</sub>			19		
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz		3		Ω
<b>Switching Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		31		nC
Gate-source Charge	Q <sub>gs</sub>			6		
Gate-drain Charge	Q <sub>gd</sub>			3.8		
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 20V, V <sub>GS</sub> = 10V, R <sub>L</sub> = 1Ω R <sub>G</sub> = 3Ω		7		ns
Turn-on Rise Time	t <sub>r</sub>			2.8		
Turn-off Delay Time	t <sub>d(off)</sub>			24		
Turn-off Fall Time	t <sub>f</sub>			3.9		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 10A			1.2	V

Notes :

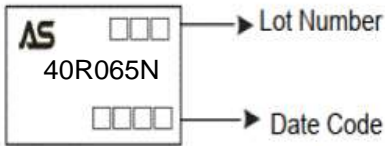
- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width ≤ 10μs, duty cycle ≤ 1%.
- 3.E<sub>AS</sub> condition: V<sub>DD</sub> = 20V, V<sub>GS</sub> = 10V, L = 0.5mH, R<sub>G</sub> = 25Ω Starting T<sub>J</sub> = 25°C.
- 4.Pulse Test : Pulse Width ≤ 300μs, duty cycle ≤ 2%.
- 5.The power dissipation P<sub>D</sub> is limited by T<sub>J(MAX)</sub> = 150°C.And device mounted on a large heatsink
- 6.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25°C.

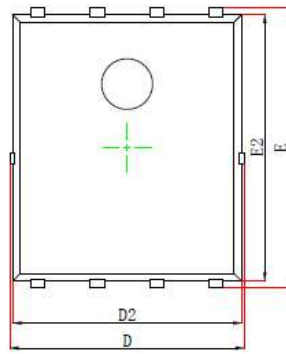




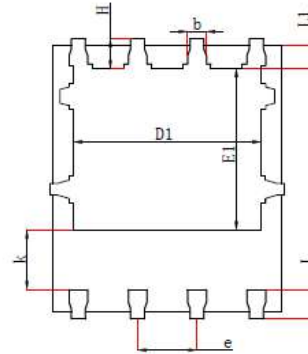
## Ordering and Marking Information

Ordering Device No.	Marking	Package	Packing	Quantity
ASDM40R065NQ-R	40R065N	DFN5*6-8	Tape&Reel	4000/Reel

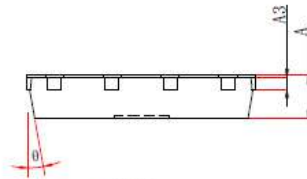
PACKAGE	MARKING
DFN5*6-8	 <p>AS    □□    → Lot Number 40R065N □□□□    → Date Code</p>

**DFN5x6\_P, 8 Leads**


Top View  
[顶视图]



Bottom View  
[背视图]



Side View  
[侧视图]

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.000	0.035	0.039
A3	0.254REF.		0.010REF.	
D	4.944	5.096	0.195	0.201
E	5.974	6.126	0.235	0.241
D1	3.910	4.110	0.154	0.162
E1	3.375	3.575	0.133	0.141
D2	4.824	4.976	0.190	0.196
E2	5.674	5.826	0.223	0.229
k	1.190	1.390	0.047	0.055
b	0.350	0.450	0.014	0.018
e	1.270TYP.		0.050TYP.	
L	0.559	0.711	0.022	0.028
L1	0.424	0.576	0.017	0.023
H	0.574	0.726	0.023	0.029
θ	10°	12°	10°	12°

**IMPORTANT NOTICE**

ShenZhen Ascend Semiconductor incorporated MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

ShenZhen Ascend Semiconductor Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. ShenZhen Ascend Semiconductor Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does ShenZhen Ascend Semiconductor Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume .

all risks of such use and will agree to hold Ascendsemi Incorporated and all the companies whose products are represented on ShenZhen Ascend Semiconductor Incorporated website, harmless against all damages.

ShenZhen Ascend Semiconductor Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use ShenZhen Ascend Semiconductor Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold ShenZhen Ascend Semiconductor Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

**[www.ascendsemi.com](http://www.ascendsemi.com)**