AUTOMOTIVE

COMPLIANT

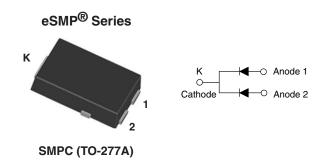
HALOGEN

FREE



Vishay Semiconductors

Hyperfast Rectifier, 2 x 3 A FRED Pt®



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 3 A			
V _R	200 V			
V _F at I _F	0.75 V			
t _{rr (typ.)}	27 ns			
T _J max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Common cathode			

FEATURES

- \bullet Hyperfast recovery time, reduced $\mathbf{Q}_{\text{rr}},$ and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating

Halogen-free, RoHS compliant

Terminals: matte tin plated leads, solderable per

J-STD-002

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		V_{RRM}		200	V	
Average restified forward current	per device	I	T _{Sp} = 165 °C	6		
Average rectified forward current	per diode	I _F (AV)		3		
Non-repetitive peak surge current	per device		T OF °C C management mules	150	Α	
per di		IFSM	$T_J = 25$ °C, 6 ms square pulse	80		
Operating junction and storage ten	nperatures	T _J , T _{Stg}		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	200	-	-	
Farward valtage per diade	V _F	I _F = 3 A	-	0.87	0.94	V
Forward voltage, per diode		I _F = 3 A, T _J = 125 °C	-	0.75	0.79	
Reverse leakage current, per diode	I _R	V _R = V _R rated	-	-	2	
neverse leakage current, per diode		$T_J = 125 ^{\circ}\text{C}, V_R = V_R \text{rated}$	-	2	10	μA
Junction capacitance	C _T	V _R = 200 V	-	12	-	pF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt = 50 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	27	-	no
Payersa rassyony tima	t _{rr}	$I_F = 0.5 \text{ A}, I_R = 1 \text{ A}, I_{rr} = 0.25 \text{ A}$		-	-	25	
Reverse recovery time		T _J = 25 °C	I _F = 3 A dI _F /dt = 200 A/μs V _B = 160 V	=.	20	-	ns
		T _J = 125 °C		=.	26	-	
Peak recovery current	DDM	T _J = 25 °C		-	2.4	-	Α
reak recovery current		T _J = 125 °C		=.	3.8	-	^
Poverne receivery charge	T _J = 25 °C		-	23	-	nC	
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	50	-	IIC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C
Thermal resistance, junction to mount, per diode	R _{thJM}		-	2.8	4	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style SMPC (TO-277A)		NC	CH2	

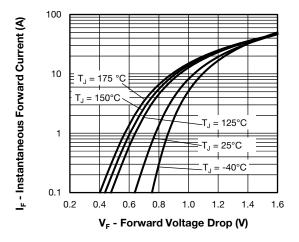


Fig. 1 - Typical Forward Voltage Drop Characteristics

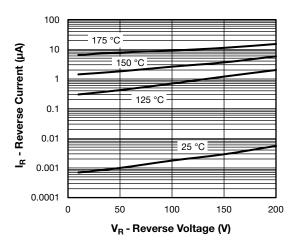


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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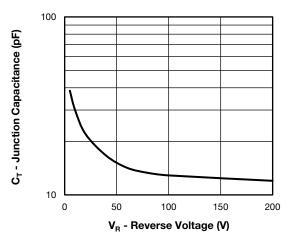


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

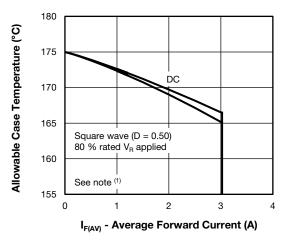


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

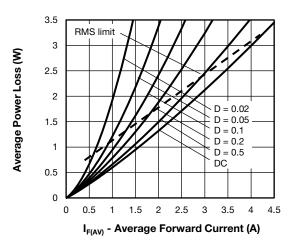


Fig. 5 - Forward Power Loss Characteristics

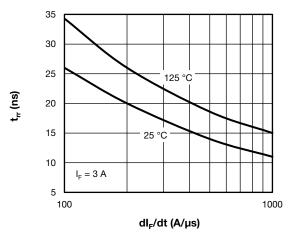


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt

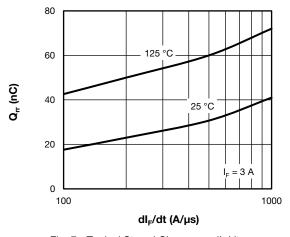
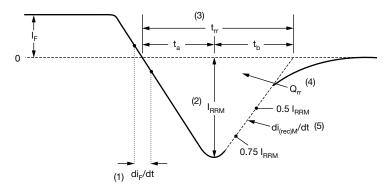


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

 $[\]begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 5)}; \\ Pd_{REV} = \text{inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{rated } V_R \\ \end{array}$

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- (1) di_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm l_{r}$ to point where a line passing through 0.75 $\rm l_{RRM}$ and 0.50 $\rm l_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

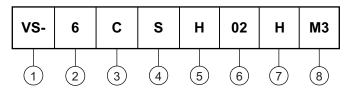
$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) di_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

Device code



- 1 Vishay Semiconductors product
- 2 Current rating (6 = 6 A)
- **3** Circuit configuration:

C = common cathode

4 - S = SMPC package

5 - Process type,

H = hyper fast recovery

6 - Voltage code (02 = 200 V)

7 - H = AEC-Q101 qualified

8 - M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-6CSH02HM3/86A	1500	1500	7" diameter plastic tape and reel				
VS-6CSH02HM3/87A	6500	6500	13" diameter plastic tape and reel				

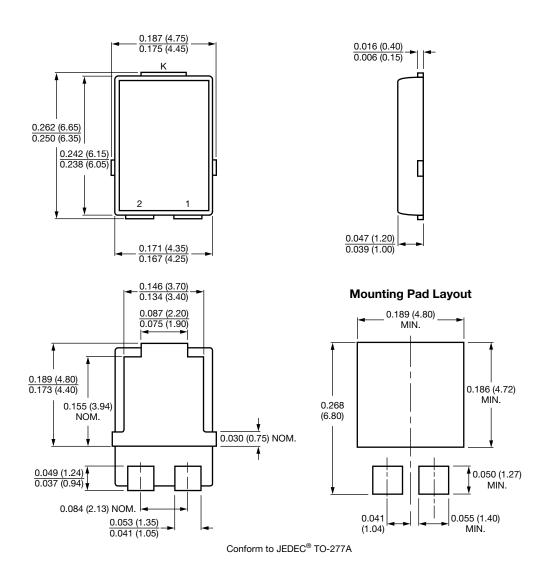
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95570</u>					
Part marking information	www.vishay.com/doc?95565				
Packaging information	www.vishay.com/doc?88869				
SPICE model	www.vishay.com/doc?96378				



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SMPC (TO-277A)

DIMENSIONS in inches (millimeters)





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