

January 16, 1998

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STANDARD RECOVERY, PCB MOUNTING, 1-PHASE FULL WAVE BRIDGE RECTIFIER ASSEMBLIES

- Low forward voltage drop
- Low reverse leakage current
- Subminiature design
- Three lead configurations
- Pcb mounting

QUICK REFERENCE DATA

- $V_R = 200V - 1000V$
- $I_F = 3.0A$
- $I_R = 2.0 \mu A$
- $t_{rr} = 2.0 \mu S$

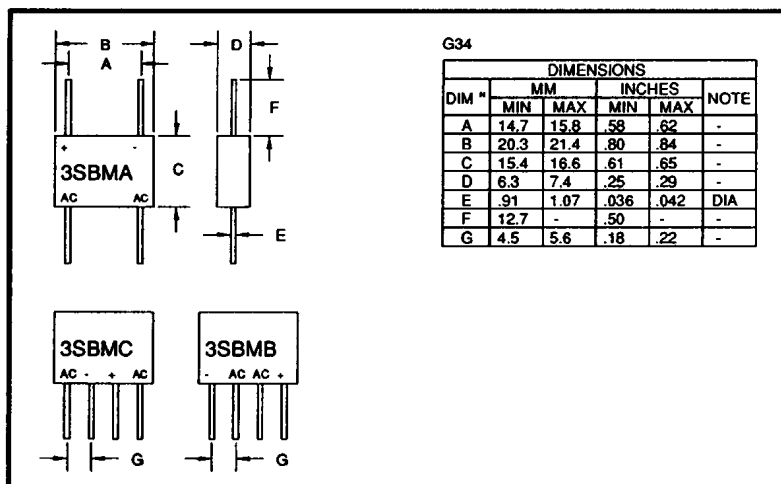
ABSOLUTE MAXIMUM RATINGS & CHARACTERISTICS

Device Type	Working Reverse Voltage VRWM	Average Rectified Current IF(AV)		1 Cycle Surge Current IFSM tp = 8.3mS	Repetitive Surge Current IFRM	Reverse Leakage Current IR @ VRWM		Forward Voltage drop VF @ 3A/leg @ 25°C	Reverse Recovery Time trr
		@ 55°C	@ 100°C	@ 25°C	@ 25°C	@ 25°C	@ 100°C		@ 25°C
	Volts	Amps	Amps	Amps	Amps	µA	µA	Volts	µS
3SBM*2	200	3.0	1.5	150	25	2.0	40	1.0	2.0
3SBM*4	400	3.0	1.5	150	25	2.0	40	1.0	
3SBM*6	600	3.0	1.5	150	25	2.0	40	1.0	
3SBM*8	800	3.0	1.5	150	25	2.0	40	1.0	
3SBM*0	1000	3.0	1.5	150	25	2.0	40	1.0	

* Add A, B, C for desired circuit configuration
(see Mechanical outline)

¹ Measured on discrete devices prior to assembly

MECHANICAL



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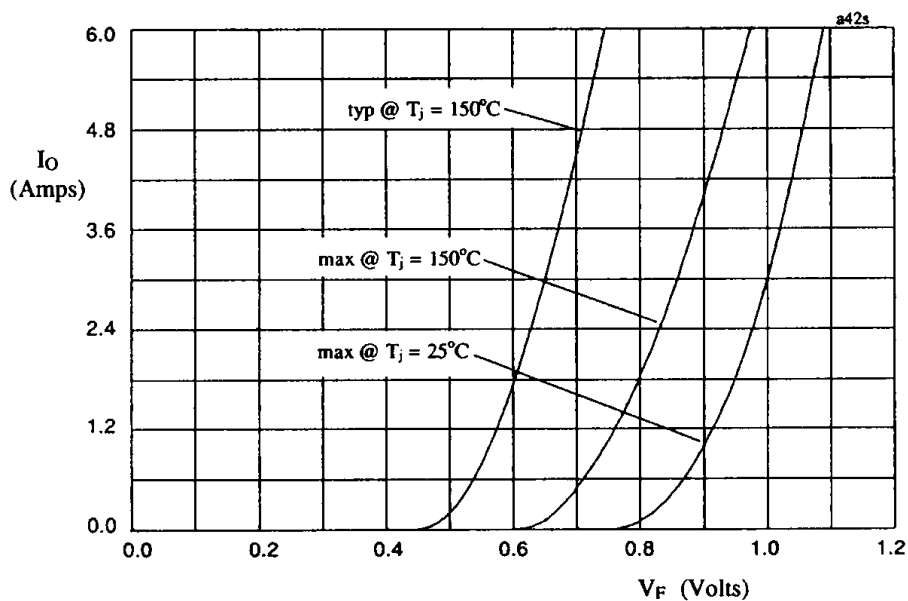


Fig 1. Forward voltage drop against output current per leg

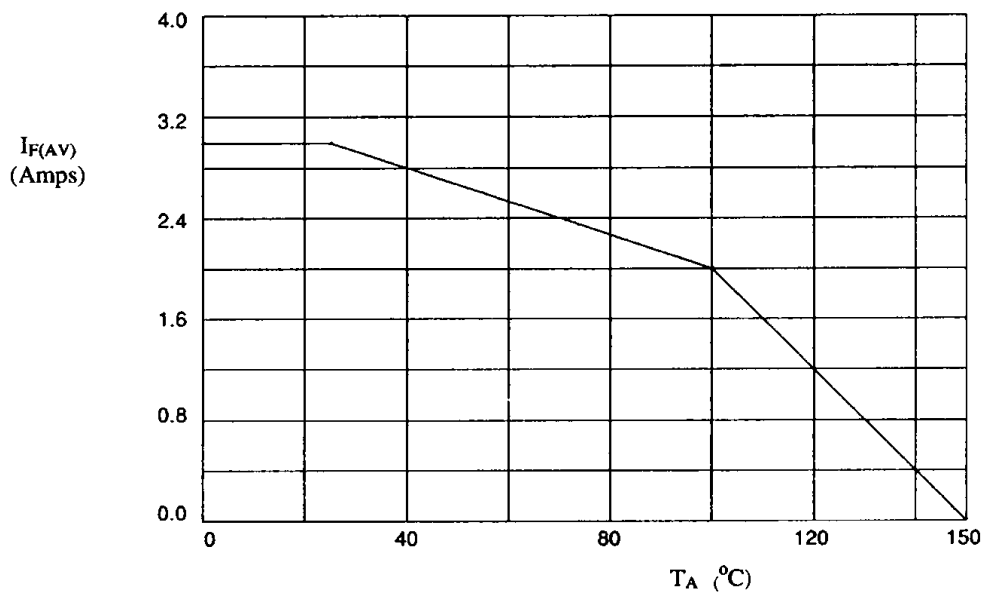


Fig 2. Maximum average forward current against ambient temperature.

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